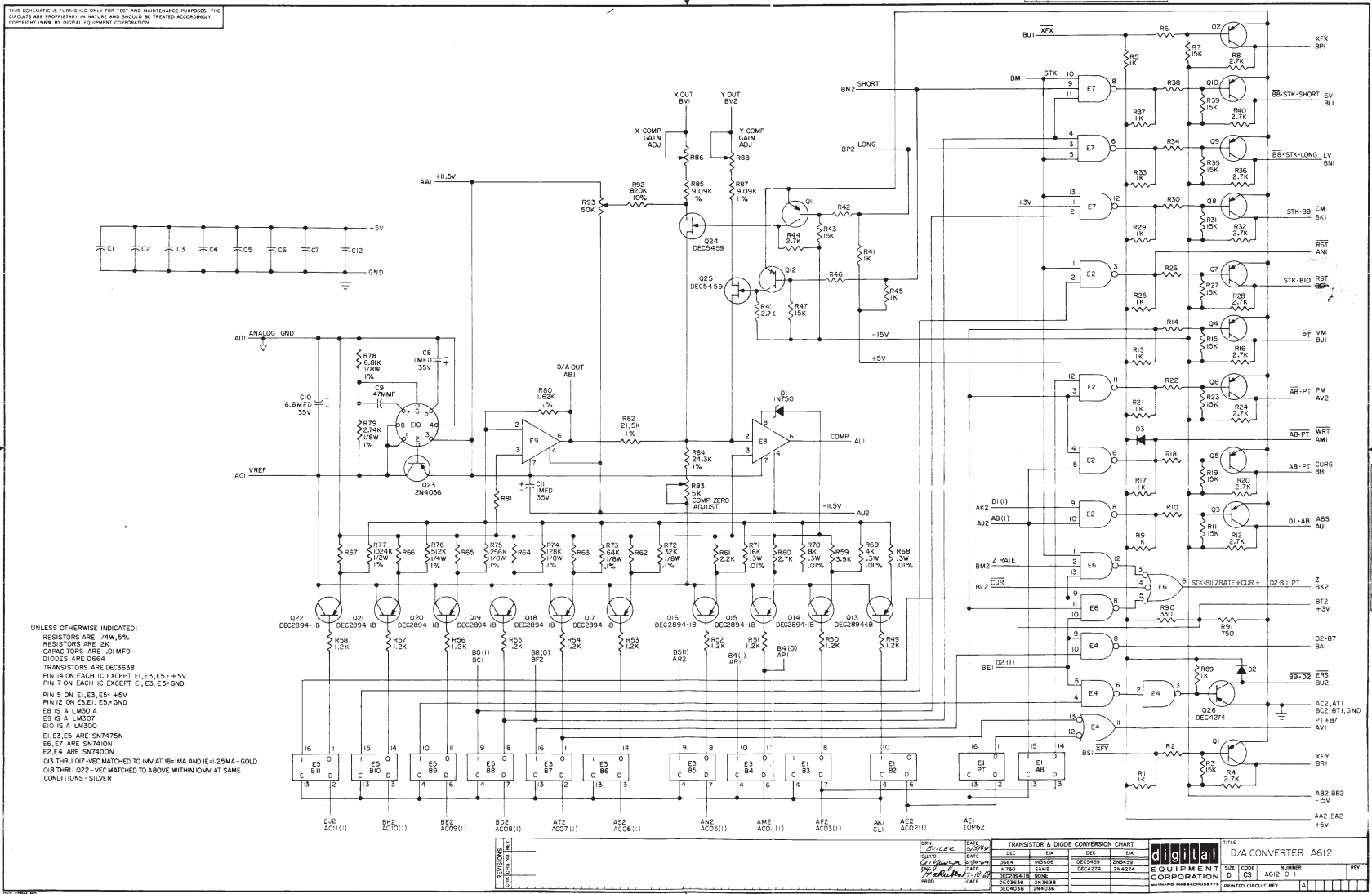


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12/22/69	WJ	WJ	WJ	DEC	EA	DEC	EA
12/22/69	WJ	WJ	WJ	DEC	EA	DEC	EA
12/22/69	WJ	WJ	WJ	DEC	EA	DEC	EA

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12/22/69	WJ	WJ	WJ	12/22/69	WJ	WJ	WJ
12/22/69	WJ	WJ	WJ	12/22/69	WJ	WJ	WJ

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12/22/69	WJ	WJ	WJ	12/22/69	WJ	WJ	WJ
12/22/69	WJ	WJ	WJ	12/22/69	WJ	WJ	WJ

Figure 7-2. Digital-to-Analog Converter and Gate Logic Module A612, Schematic Diagram, A612-0-1