



FLIP CHIP MODULES TEST SPECS

TYPE: M207

FLIP FLOP

TEST	CONDITIONS	MAXIMUM	MINIMUM
$V_{OUT} (0)$	* $V_{IN} = +2.0V (J), +0.8V (K)$ $I_{LOAD} = +16mA, \text{CLOCK PULSE } 30 \text{ NS}$	+ 0.4 V	/
$V_{OUT} (0)$	* $V_{IN} = +0.8V (J), +2.0V (K)$ $I_{LOAD} = -400\mu A, \text{CLOCK PULSE } 30 \text{ NS}$	/	+ 2.4 V
$V_{OUT} (1)$	* $V_{IN} = +2.0V (K), +0.8V (J)$ $I_{LOAD} = +16mA, \text{CLOCK PULSE } 30 \text{ NS}$	+ 0.4 V	/
$V_{OUT} (1)$	* $V_{IN} = +0.8V (K), +2.0V (J)$ $I_{LOAD} = -400\mu A, \text{CLOCK PULSE } 30 \text{ NS}$	/	+ 2.4 V
$I_{IN} (\text{CLOCK})$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 3.2 MA	/
$I_{IN} (J, K)$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 1.6 MA	/
$I_{IN} (\text{CLEAR})$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 9.6 MA	/
$I_{IN} (\text{CLOCK})$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO V_{CC}	+ 1.0 MA	/
$I_{IN} (J, K)$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +2.4 V	+ 40 μA	/
$I_{IN} (\text{CLEAR})$	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +2.4 V	+ 240 μA	/
TD_1, TD_0	$V_{CC} = +5.00 \text{ V}$ CLOCK (J, K TO +2.0) NO LOAD 50% TO 50%	50 NS	10 NS
TD_1, TD_0	$V_{CC} = +5.00 \text{ V}$ CLEAR, CLOCK (J, K TO +2.0) ** RC LOAD 50% TO 50%	75 NS	10 NS

TECHNICAL INFORMATION

MAINTENANCE INFORMATION

11/2/67

Instruction literature and technical bulletins are available on all digital products, if you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

* $V_{CC} = +4.75V$

** 150 PARALLEL 330Ω TO +5.0V

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting.
Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.

JBN