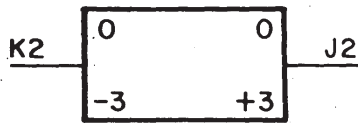
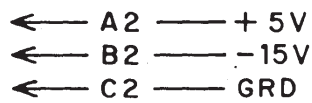


# BUS CONVERTER M507

## M SERIES



### POWER



**INPUT**  
GRD  
-3V

**OUTPUT**  
GRD  
+3V

The M507 contains six inverting level shifters which will accept  $-3$  volts and GRD as inputs. The input to each level shifter consists of a 10 ma. clamped load and is diode protected against positive voltage excursions.

The output consists of an open collector NPN transistor. The output of each level shifter will sink 100 ma. to GRD. The maximum voltage which may be applied to the output is  $+20$  volts. The output transistor is protected against negative voltage excursions by a diode connected between the collector and GRD. The output rise is delayed by 100 nsec. for pulse spreading.

The principle use of this module is to convert negative voltage logic levels or pulses of duration greater than 100 nsec.

**Inputs:** Input loading is equivalent to a 3 ma. clamped load.

**Outputs:** Each output can sink 100 ma. to GRD. Maximum voltage applied to any output is  $+20$  volts.

**Power:**  $+5$  volts, 42 ma. (max.);  $-15$  volts, 115 ma. (max.).

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M507 — \$45

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