

This drawing is a preliminary sketch. It is not to be used for manufacturing or assembly of parts without written approval.

PARTS REFERENCE

DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1. E23	DEC 74H50N	IC 805050	2
2. E24, E10, E28, E37	DEC 74H02N	IC 805004	2
3. E25	DEC 74H74N	IC 805067	2
4. E25, E26, E29, E38, E43, E50, E54	DEC 74H40N	IC 805586	8
5. E7, E11, E19, E20, E31, E49	DEC 7400N	IC 805575	6
6. E28	DEC 8881	IC 805705	1
7. E12, E14, E18, E39, E40, E41, E42, E44, E53	DEC 74H00N	IC 805056	9
8. E13, E15, E24, E34	DEC 74H10N	IC 805057	4
9. E16	DEC 74H20N	IC 805635	1
10. E17, E47, E48	DEC 74H50N	IC 805059	3
11. E21, E51	DEC 7410N	IC 805576	2
12. E22, E46	DEC 8815A	IC 805713	2
13. E27, E30, E45	DEC 8271	IC 805615	2
14. E32, E33, E35, E36	8251B	IC 805594	4
15. E52	DEC 7420N	IC 805577	4
16. C1-C10, C13, C15, C27, C29, C55, C59	01 MFD, 100V, 20%	CAP 805610	52
17. C11, C60	330 MMFD, 100V, 5%	CAP 805023	2
18. C12	15 MMFD, 100V, 5%	CAP 805208	1
19. C14	150 MMFD, 100V, 5%	CAP 805009	1
20. C56, C58, C28	6.8 MFD, 35V, 20%	CAP 805067	4
21. R1	300, 1/4W, 5%	RES 130142	1
22. R2, R4, R7, R10	1000, 1/4W, 5%	RES 130355	4
23. R3	47, 1/4W, 5%	RES 1300202	1
24. R5	470, 1/4W, 5%	RES 1300316	1
25. R6, R9	220, 1/4W, 5%	RES 1300271	2
26. R8	5000 OHM POTENTIOMETER	1321451	1
27. D1	5624 DIODE	130015	1
28. Q1	DEC 3009B TRANSISTOR	1503100	1

NOTES:

- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
- ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
- PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK). THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
- DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
- GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

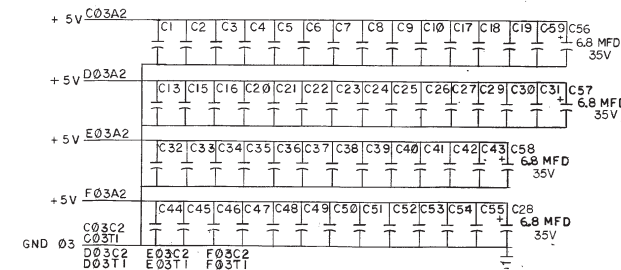
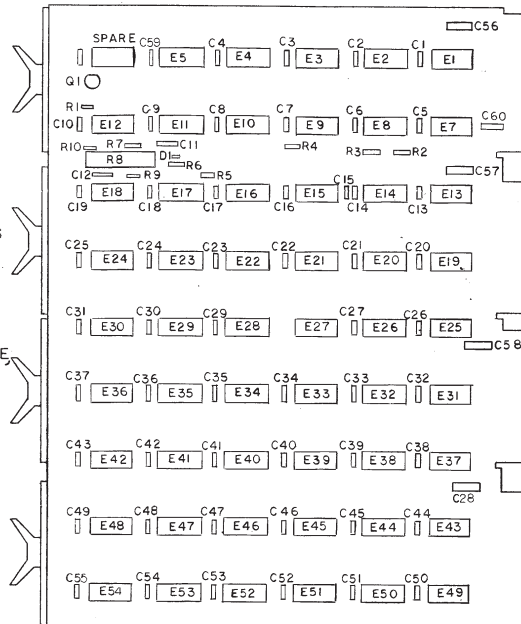
IC TYPE	GND	+5V
DEC 7491	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

- UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT

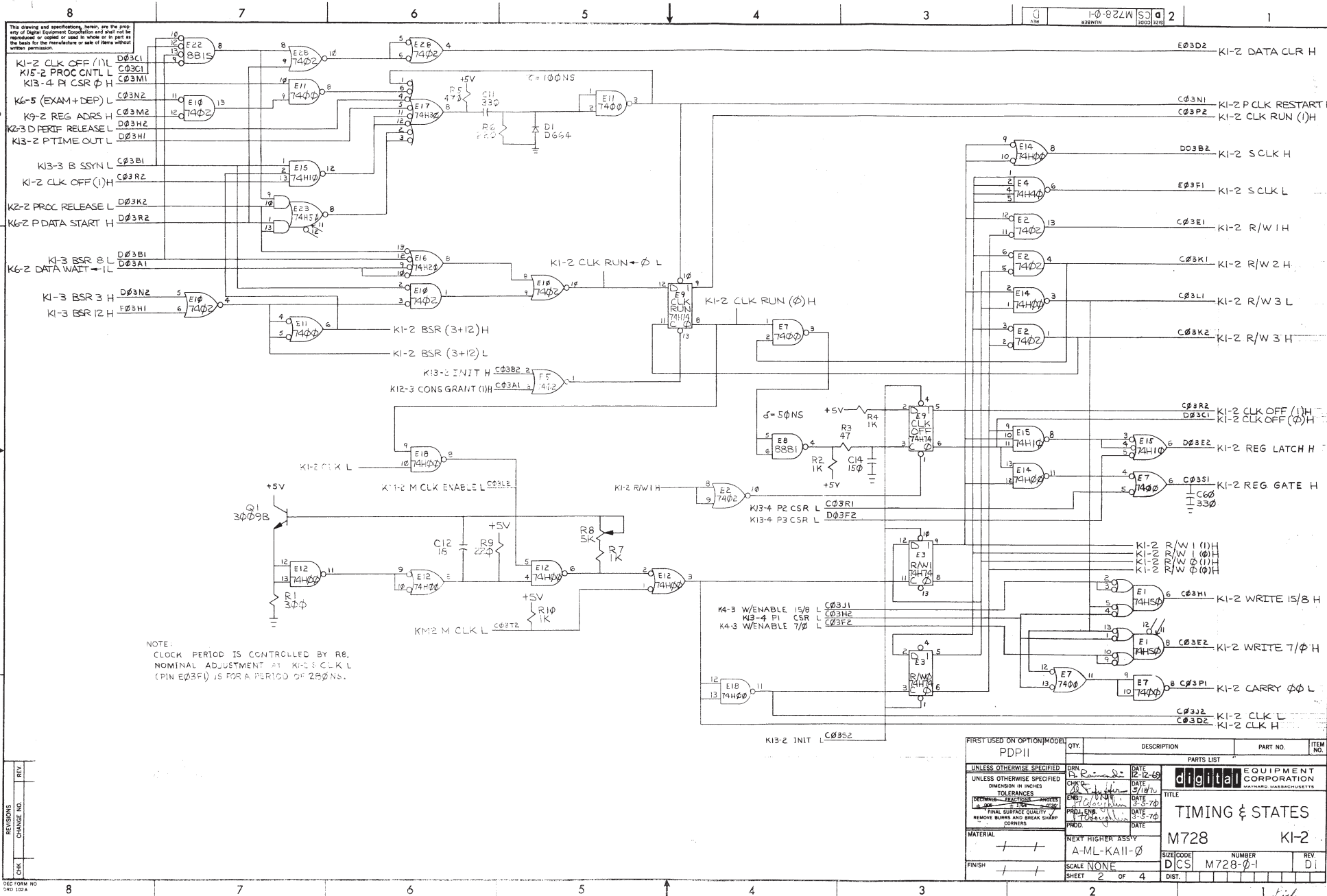
PIN NOMENCLATURE

MODULE PROCESSOR

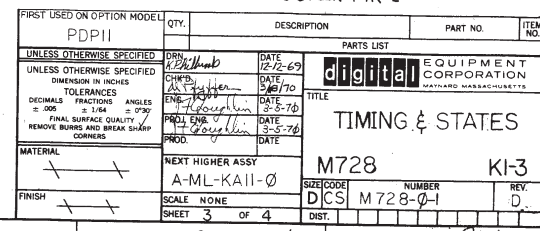


FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11					
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		digital EQUIPMENT CORPORATION			
DIMENSION IN INCHES		DATE 2/17/80			
TOLERANCES		DATE 3-2-80			
DECIMALS FRACTIONS ANGLES		DATE 3-2-80			
± .005 ± .005 ± .005		DATE 3-2-80			
FINAL SURFACE QUALITY		DATE 3-2-80			
REMOVE BURRS AND BREAK SHARP CORNERS		DATE 3-2-80			
MATERIAL		NEXT HIGHER ASSY			
FINISH		A-ML-KAI-0			
SCALE		SIZE CODE			
SHEET 1 OF 4		NUMBER			
ETCH REV B C D		REV. D			
DIST.		1 Pin			

REVISIONS
 1. CHANGE NO. 0001
 2. CHANGE NO. 0002
 3. CHANGE NO. 0003
 4. CHANGE NO. 0004
 5. CHANGE NO. 0005
 6. CHANGE NO. 0006
 7. CHANGE NO. 0007
 8. CHANGE NO. 0008
 9. CHANGE NO. 0009
 10. CHANGE NO. 0010



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPII					
UNLESS OTHERWISE SPECIFIED		DRN	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED		CHK	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DIMENSION IN INCHES		ENR	DATE	TITLE	
TOLERANCES		PROJ. ENR	DATE	TIMING & STATES	
DETERMINED BY		PROJ. ENR	DATE	M728 KI-2	
FINAL SURFACE QUALITY		PROJ. ENR	DATE	SIZE/SCALE	
REMOVE BURRS AND BREAK SHARP CORNERS		PROJ. ENR	DATE	D/C S M728-0-1	
MATERIAL		NEXT HIGHER ASSY	DATE	NUMBER	
FINISH		A-ML-KAII-0	DATE	REV	
		SCALE NONE	DATE	DI	
		SHEET 2 OF 4	DATE	DIST.	

DEC FORM NO.
DRD 102A

A

