

This drawing and specifications, herein, are the property of Digital Equipment Corporation and shall not be used in whole or in part or in any manner for the manufacture of any of items without written permission.

PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1, E2, E3, E4, E5	DEC 7400N	IC 1905575	5
2	E14, E22	DEC 8815A	IC 1909713	3
3	E3, E4	DEC 7402N	IC 1909004	2
4	E10, E30	DEC 7450N	IC 1902580	2
5	E13, E16, E19, E27	DEC 7453N	IC 1905582	2
6	E1, E2	DEC 7410N	IC 1905576	2
7	E1, E2, E3, E4, E5, E6, E7, E8, E9, E10, E11, E12, E13, E14, E15, E16, E17, E18, E19, E20, E21, E22, E23, E24, E25	DEC 7450N	IC 1905580	1
8	E1	DEC 7420N	IC 1905577	1
9	C1, C30	.01MFD 100V 20%	CAP 0001610	30
10	C31, C32	6.8MFD 35V 20%	CAP 0000067	2
11	E17	DEC 74H00N	IC 1909064	1
12	E20	DEC 74H00N	IC 1909056	1
13	E29	DEC 74H50N	IC 1909060	1

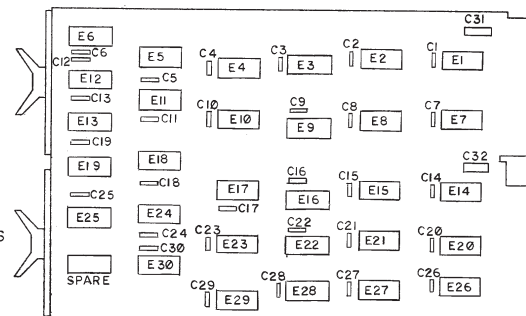
NOTES:

- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE K411 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
- ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED: MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED: MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
- PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
- DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
- GND AND +5V ARE USUALLY PIN 7 AND .PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7401	PIN 10	PIN 4
DEC 7402	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

- UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT



PIN NOMENCLATURE

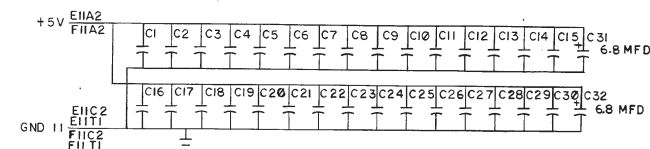
MODULE PROCESSOR

A

E

B

F



FIRST USED ON / OPTION / MODEL PDP 11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES = .000 ± 1/64 ± 0°00' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS DATE 5/28/70 DRAWN BY [Signature] CHECKED BY [Signature] DATE 5/28/70 APPROVED BY [Signature]				
MATERIAL //		NEXT HIGHER ASSY A-ML-K411-0		
FINISH //		SCALE 1/4" = 1"		
SHEET 1 OF 3		ETCH REV B C		
DISTR.		1 Print		

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

REGISTER CNTL

M821 K4-1

DCS M821-0-1 B

