

Digital Equipment Corporation
Maynard, Massachusetts



**Maintenance Manual
Volume II**

RK8 Disk System

**RK8 DISK SYSTEM
MAINTENANCE MANUAL
VOLUME II**

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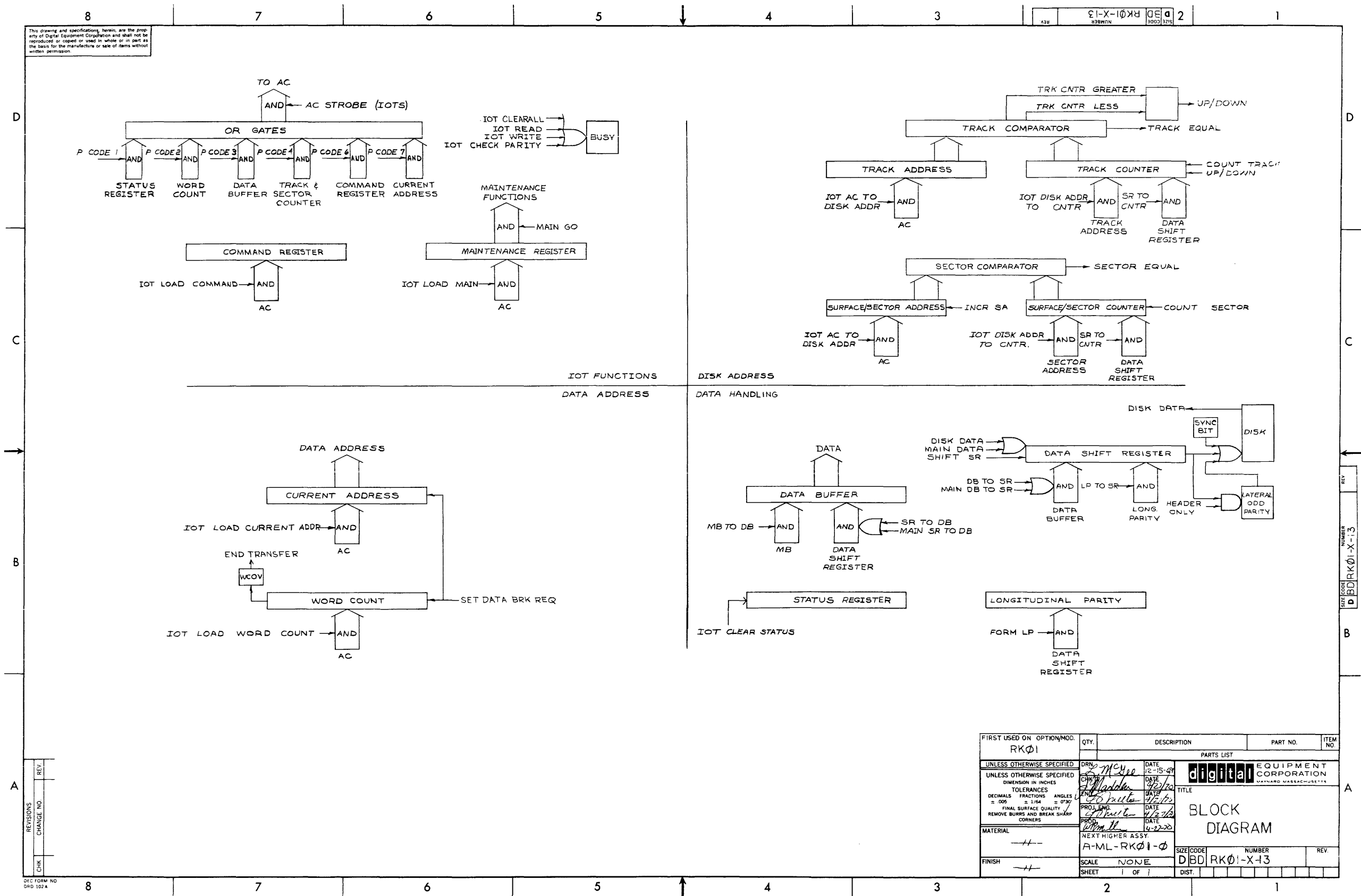


Figure II-2 RK01 Block Diagram

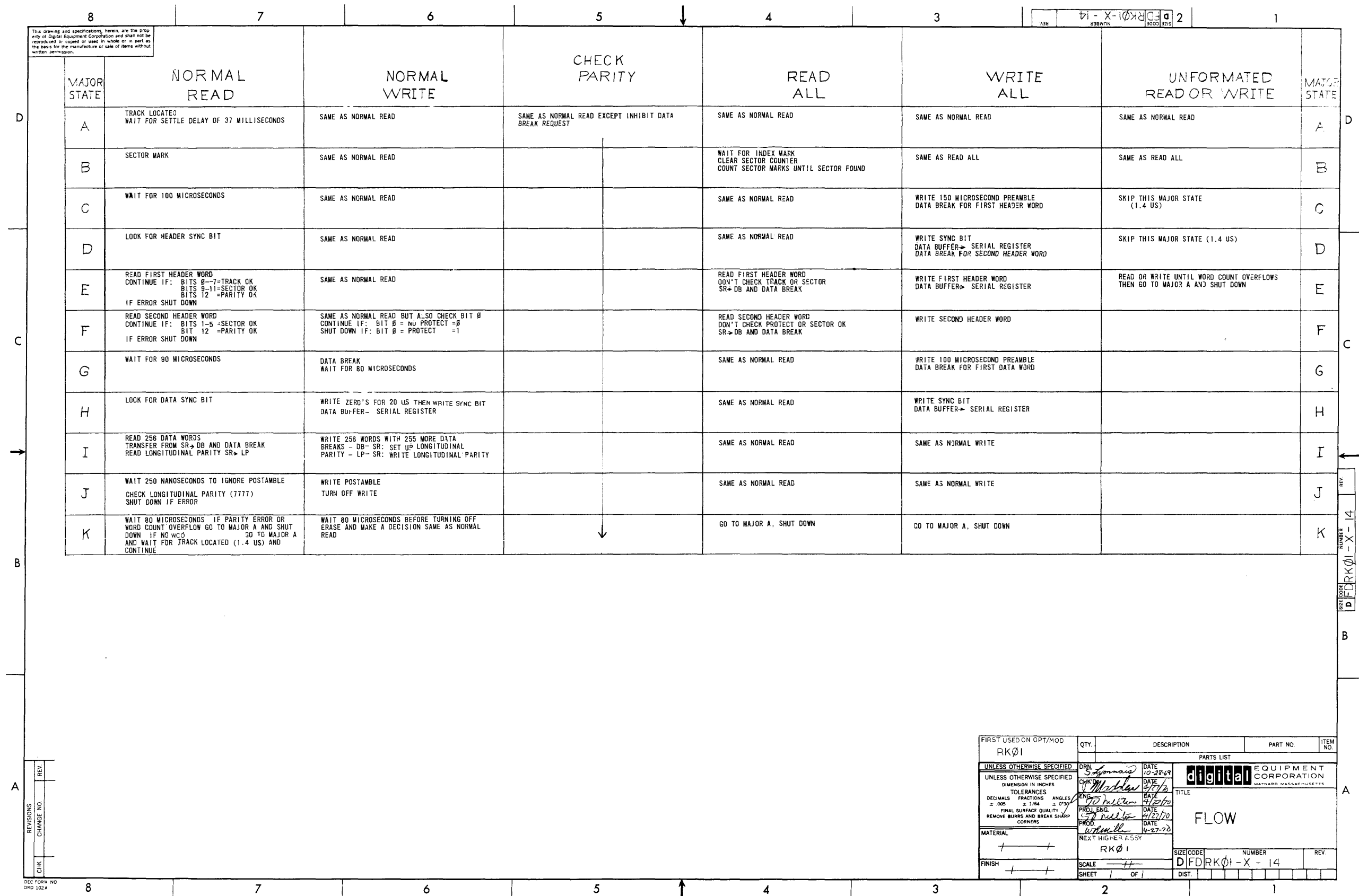


Figure II-3 RK01 Flow Diagram

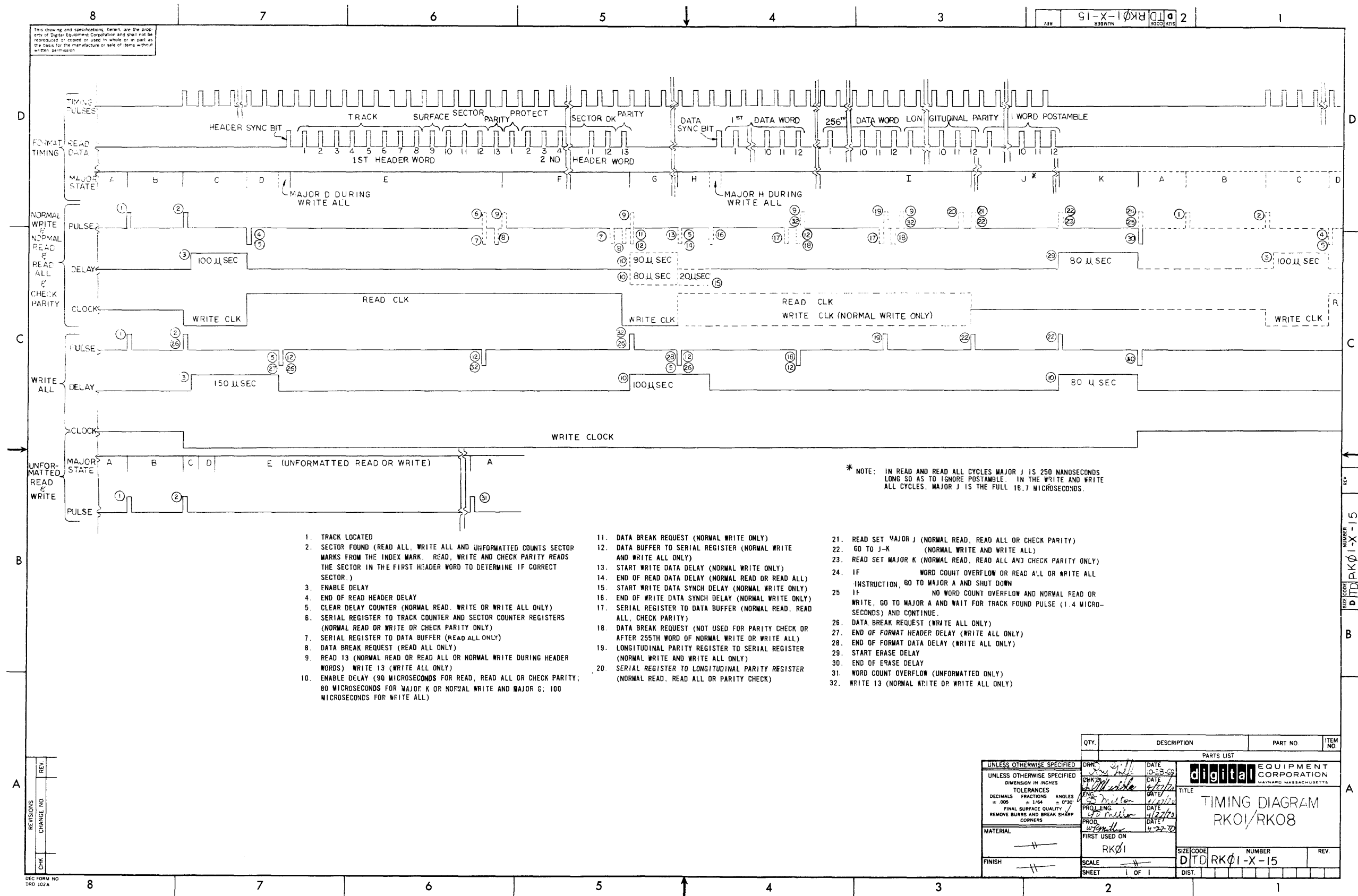


Figure II-4 RK01/RK08 Timing Diagram

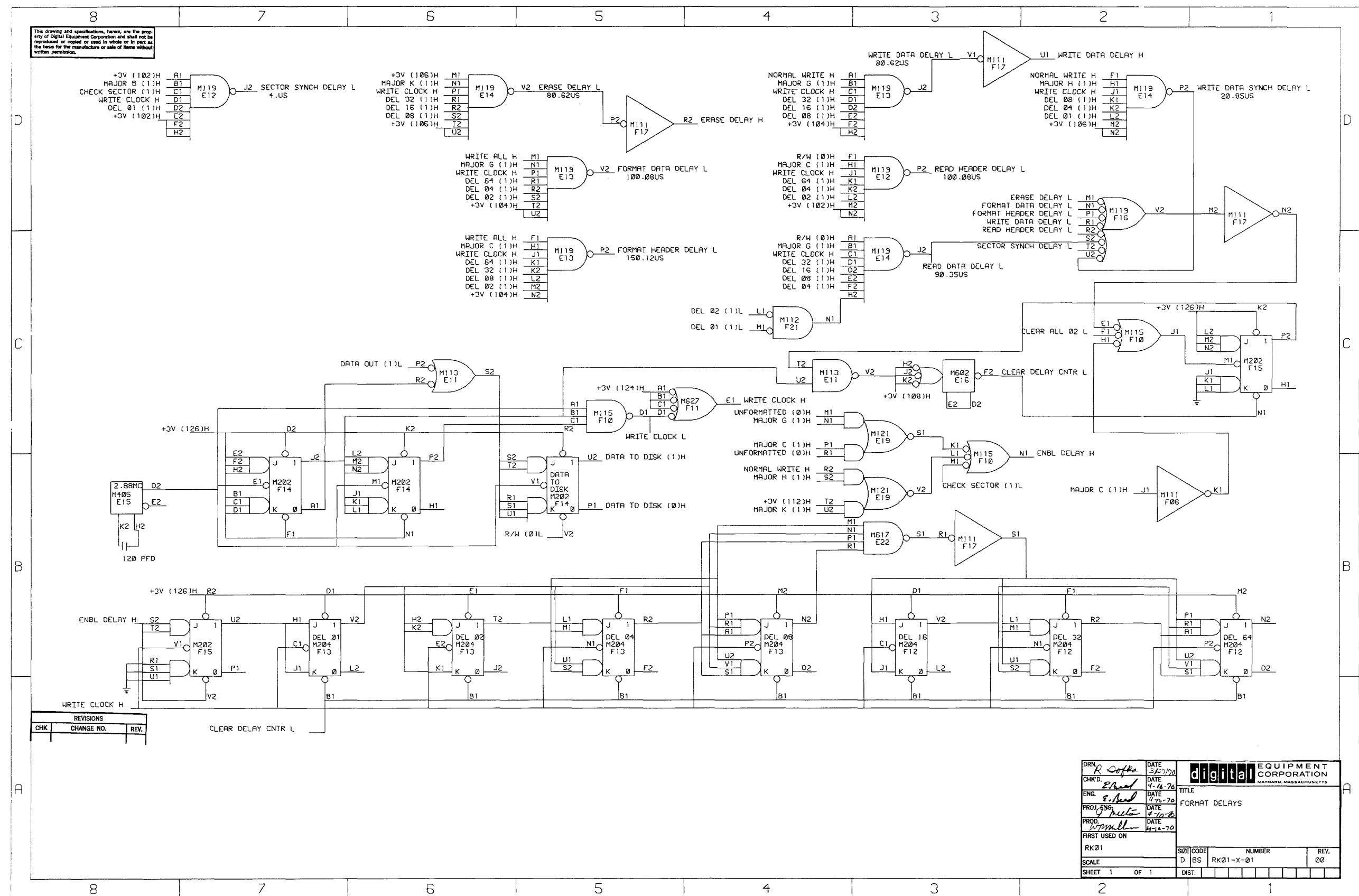


Figure II-5 Format Delays Block Schematic Diagram

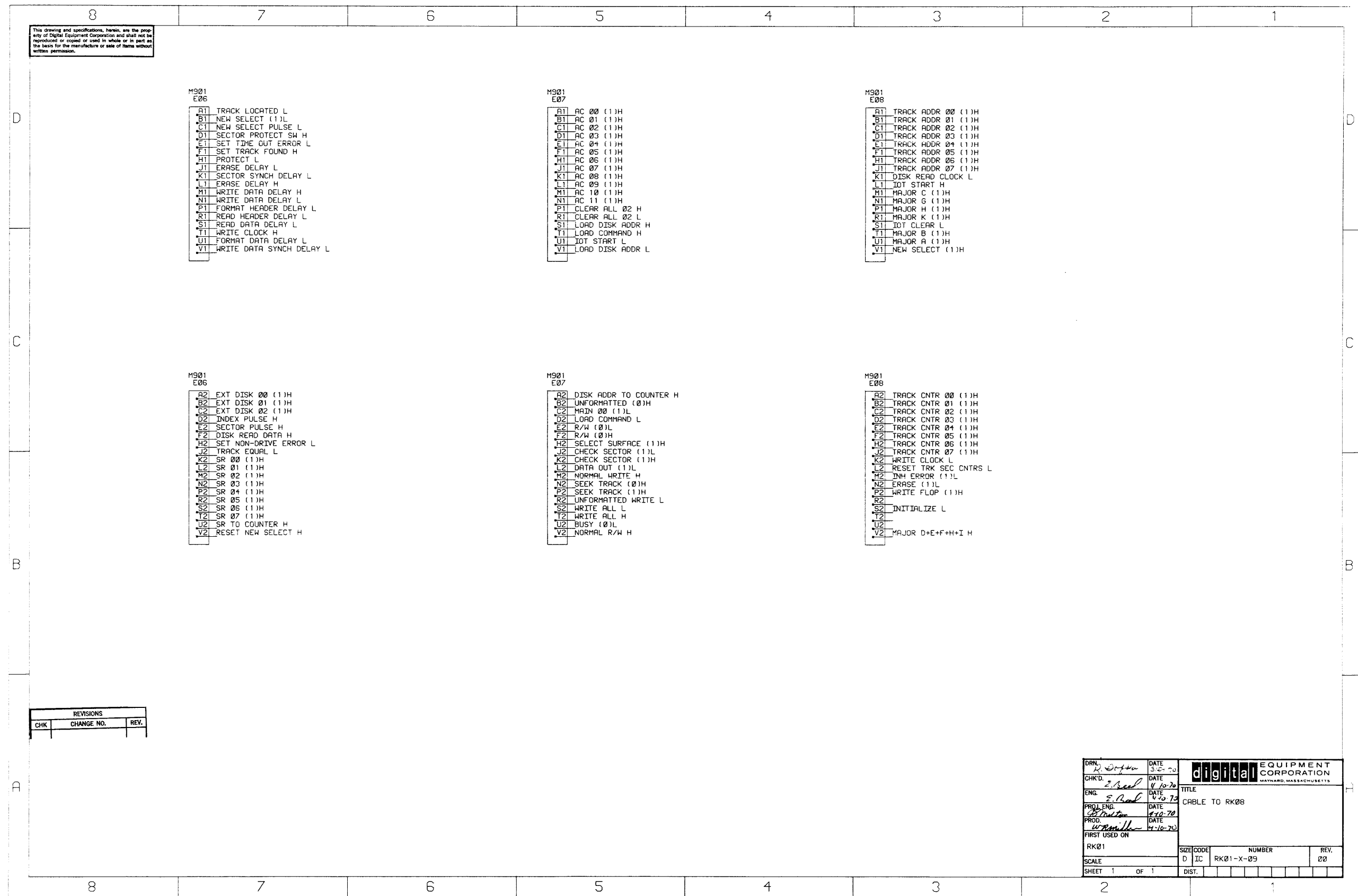


Figure II-6 Cable to RK08 Block Schematic Diagram

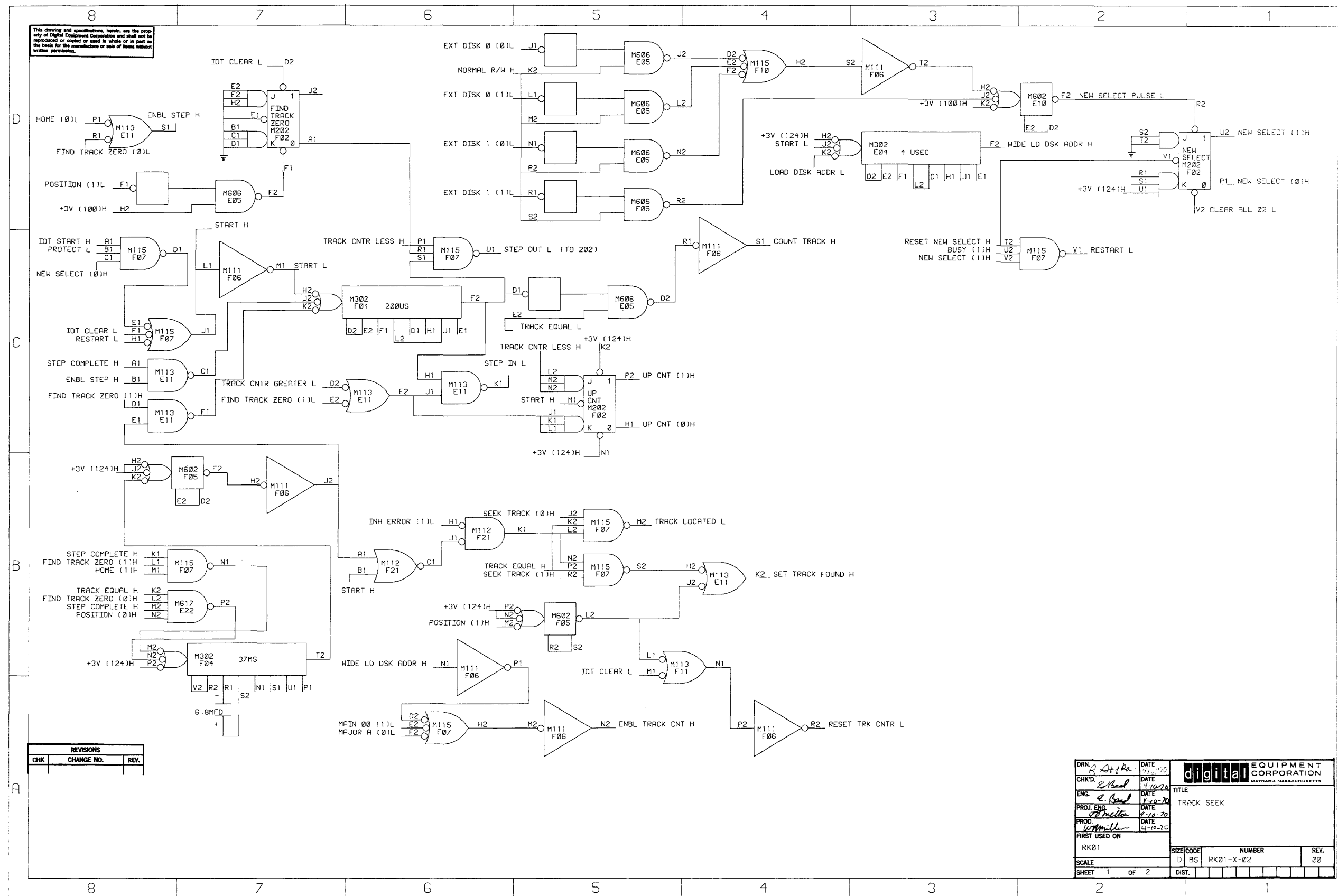


Figure II-7 Track Seek Block Schematic Diagram
(Sheet 1)

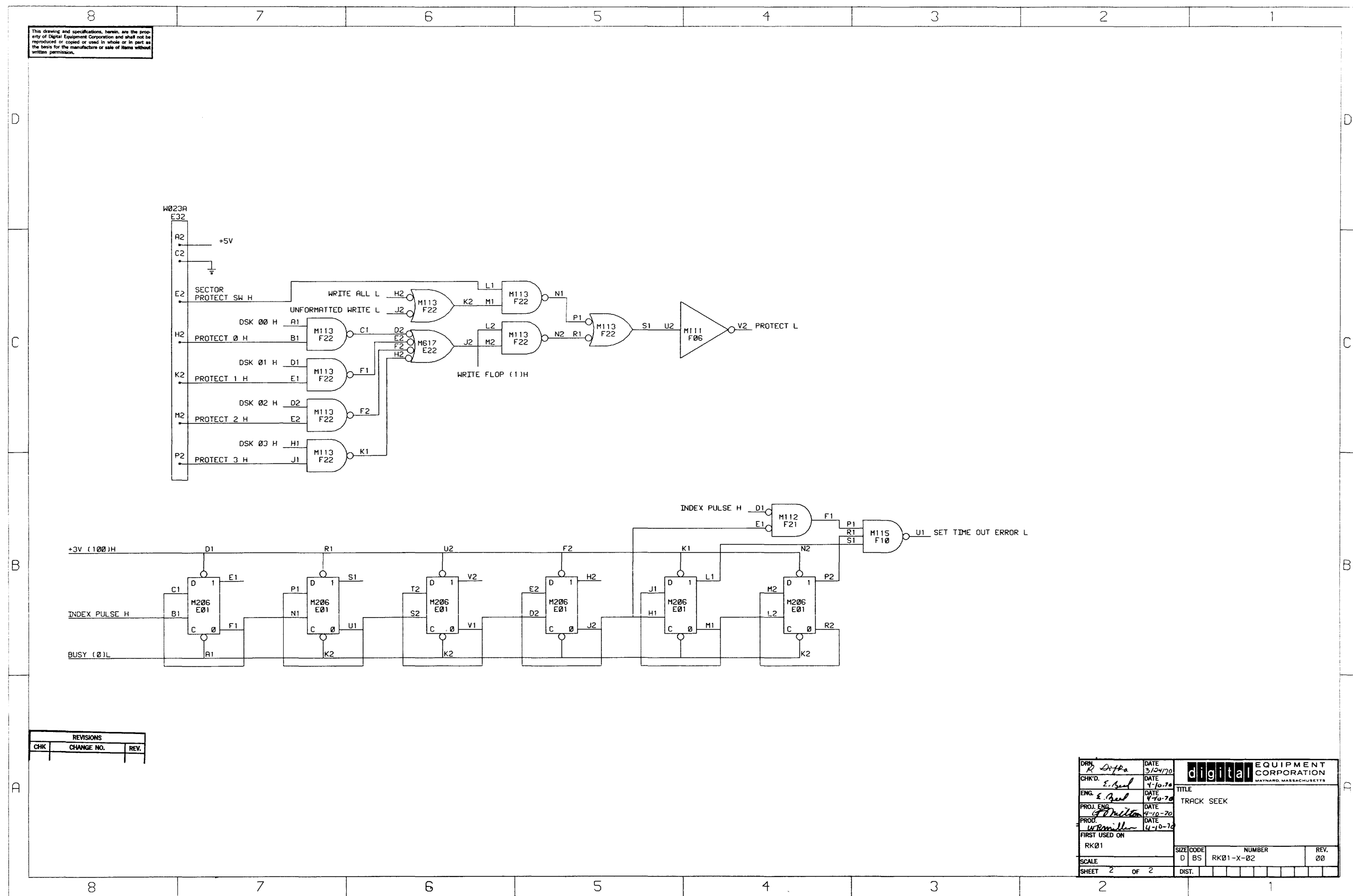


Figure II-7 Track Seek Block Schematic Diagram
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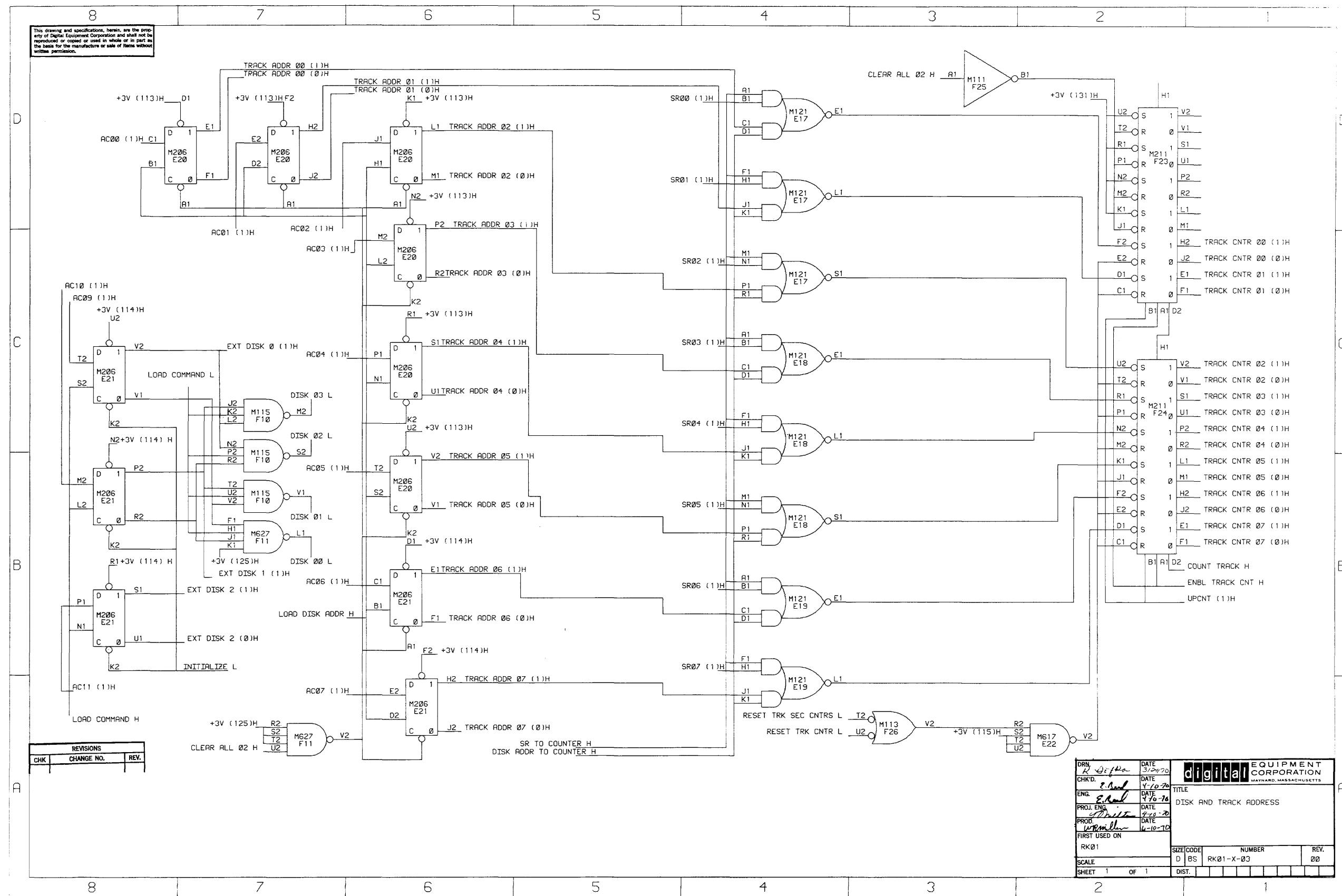


Figure II-8 Disk and Track Address Block Schematic Diagram

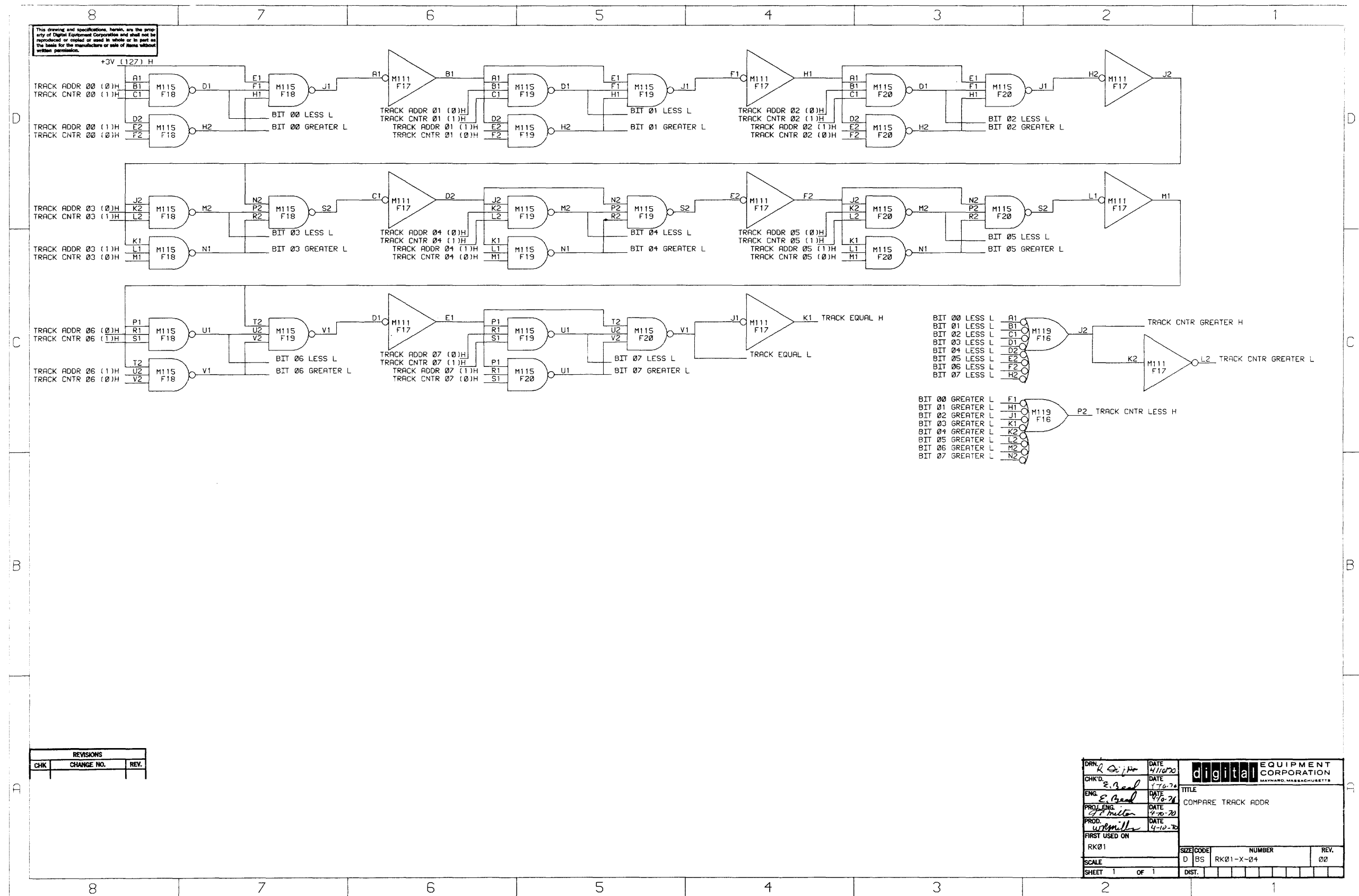
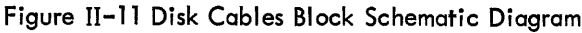


Figure II-9 Compare Track Address Block Schematic Diagram





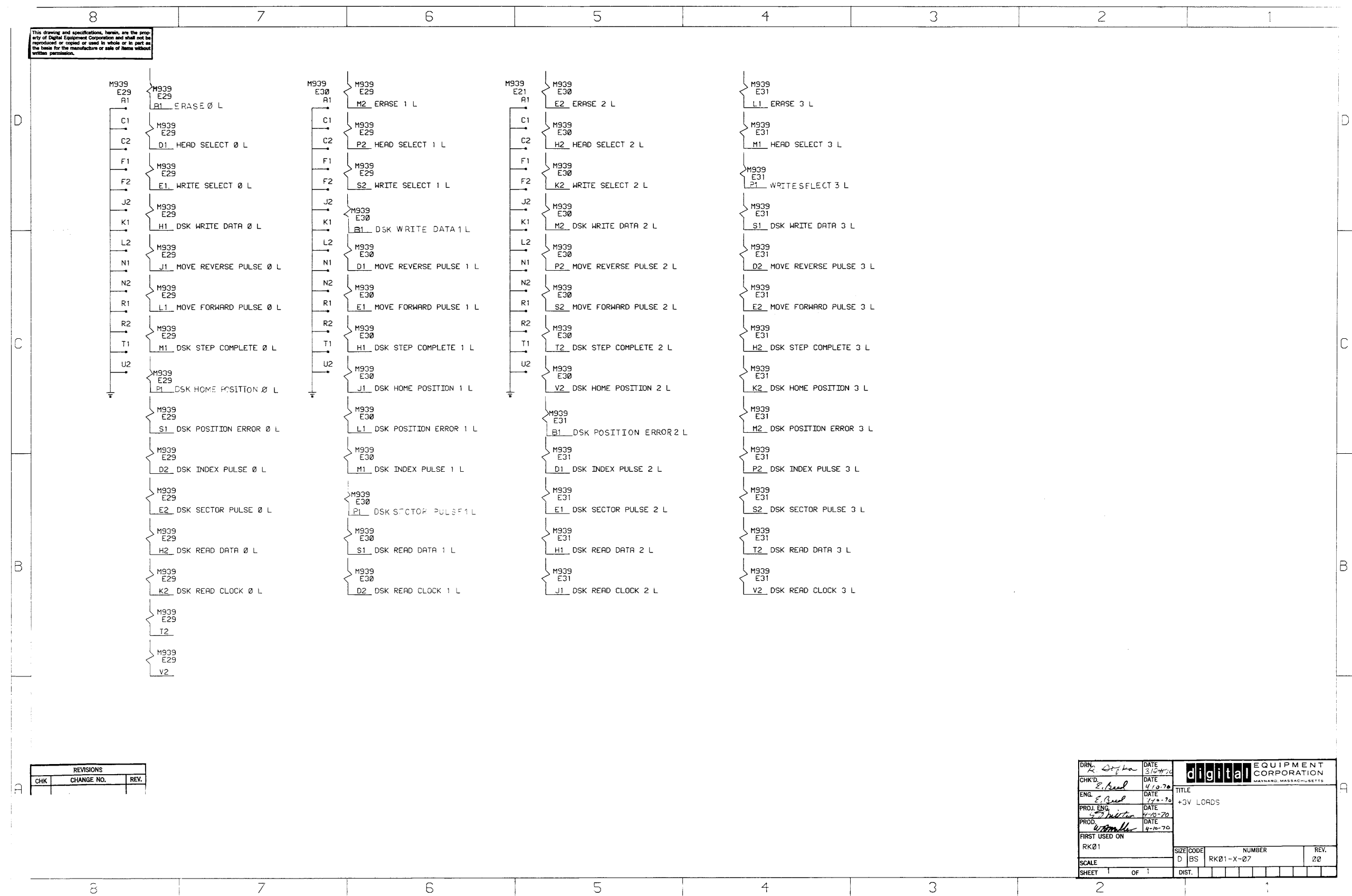


Figure II-12 +3V Loads Block Schematic Diagram

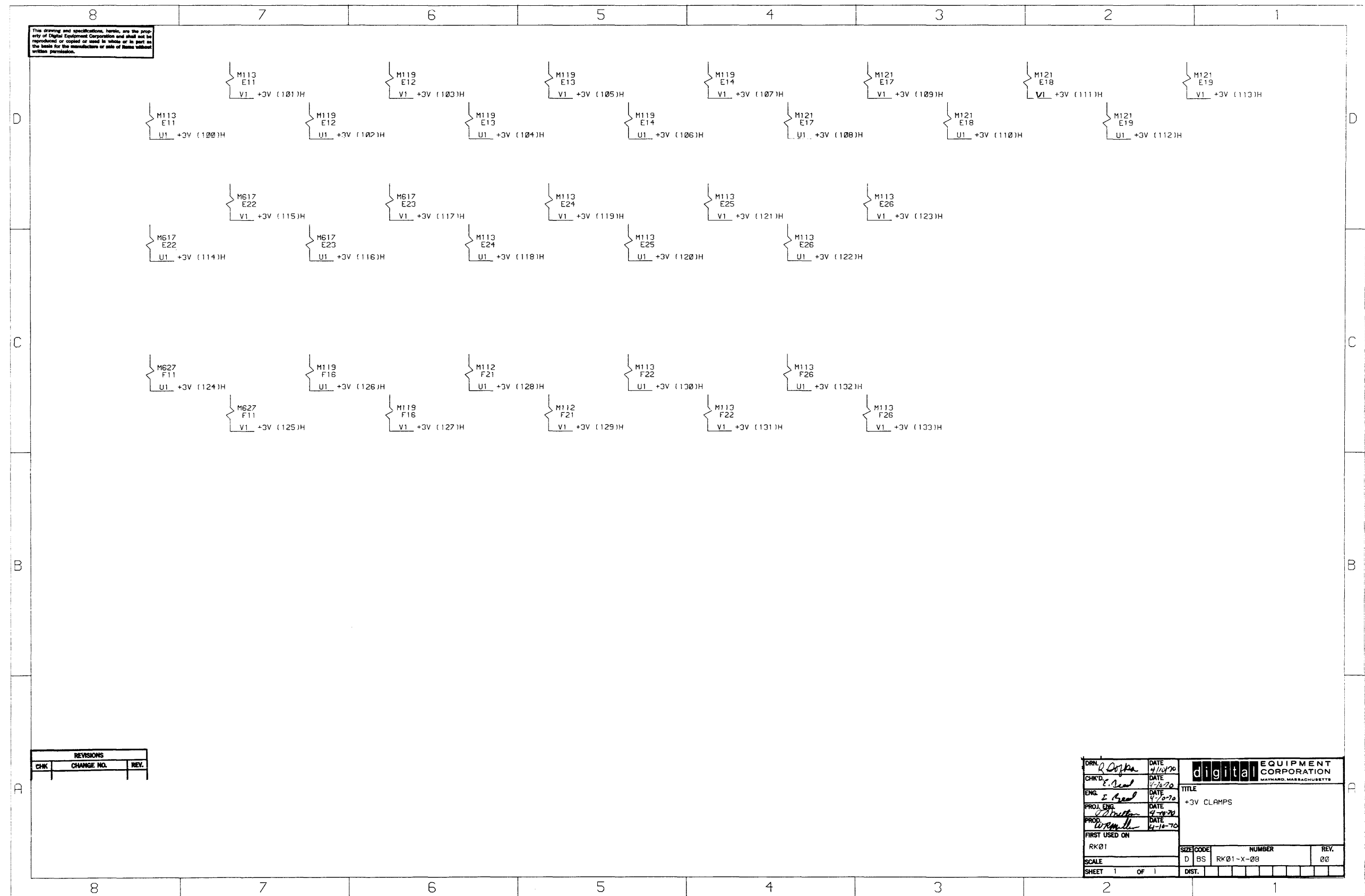
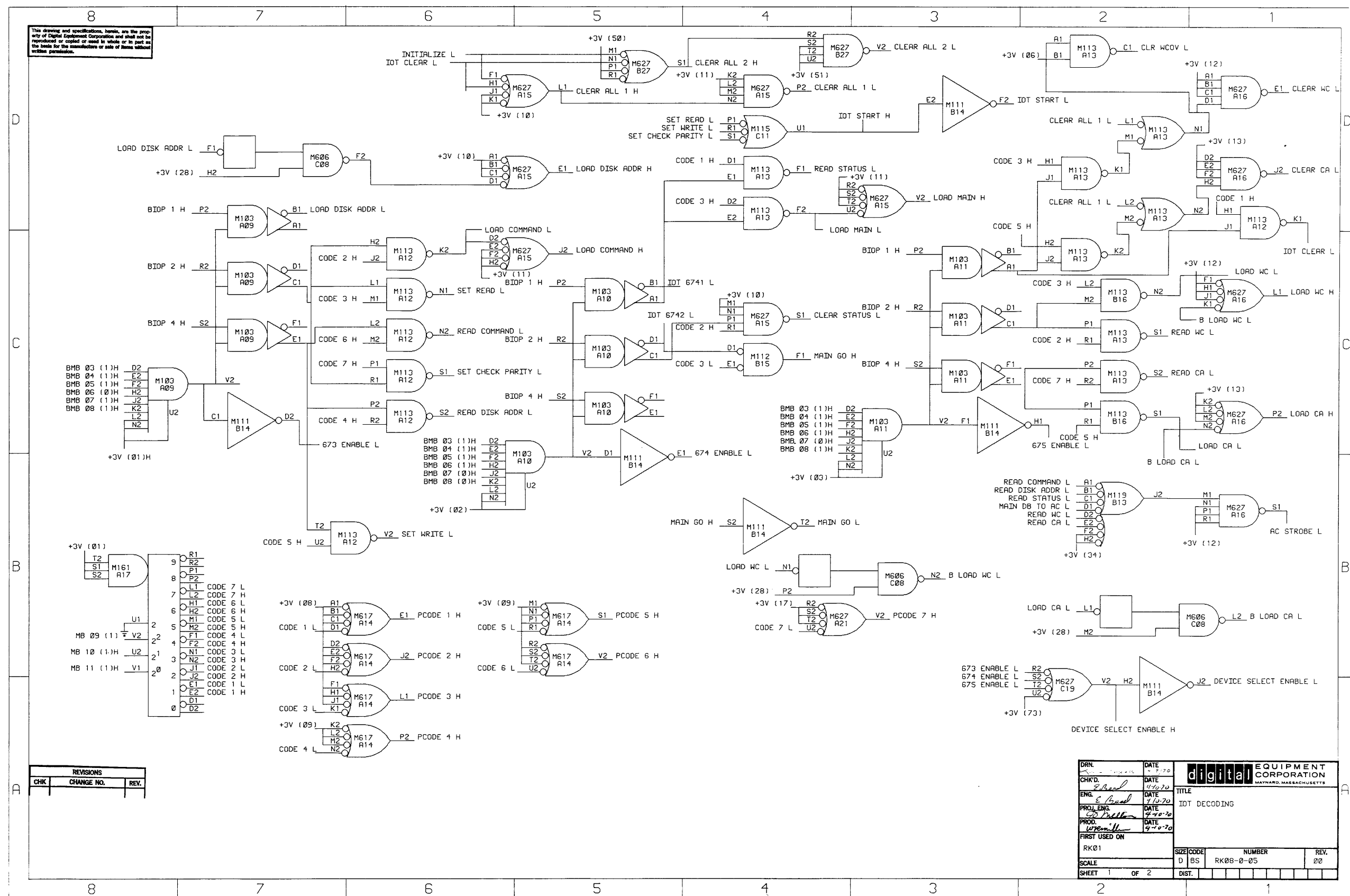


Figure II-13 +3V Clamp Block Schematic Diagram



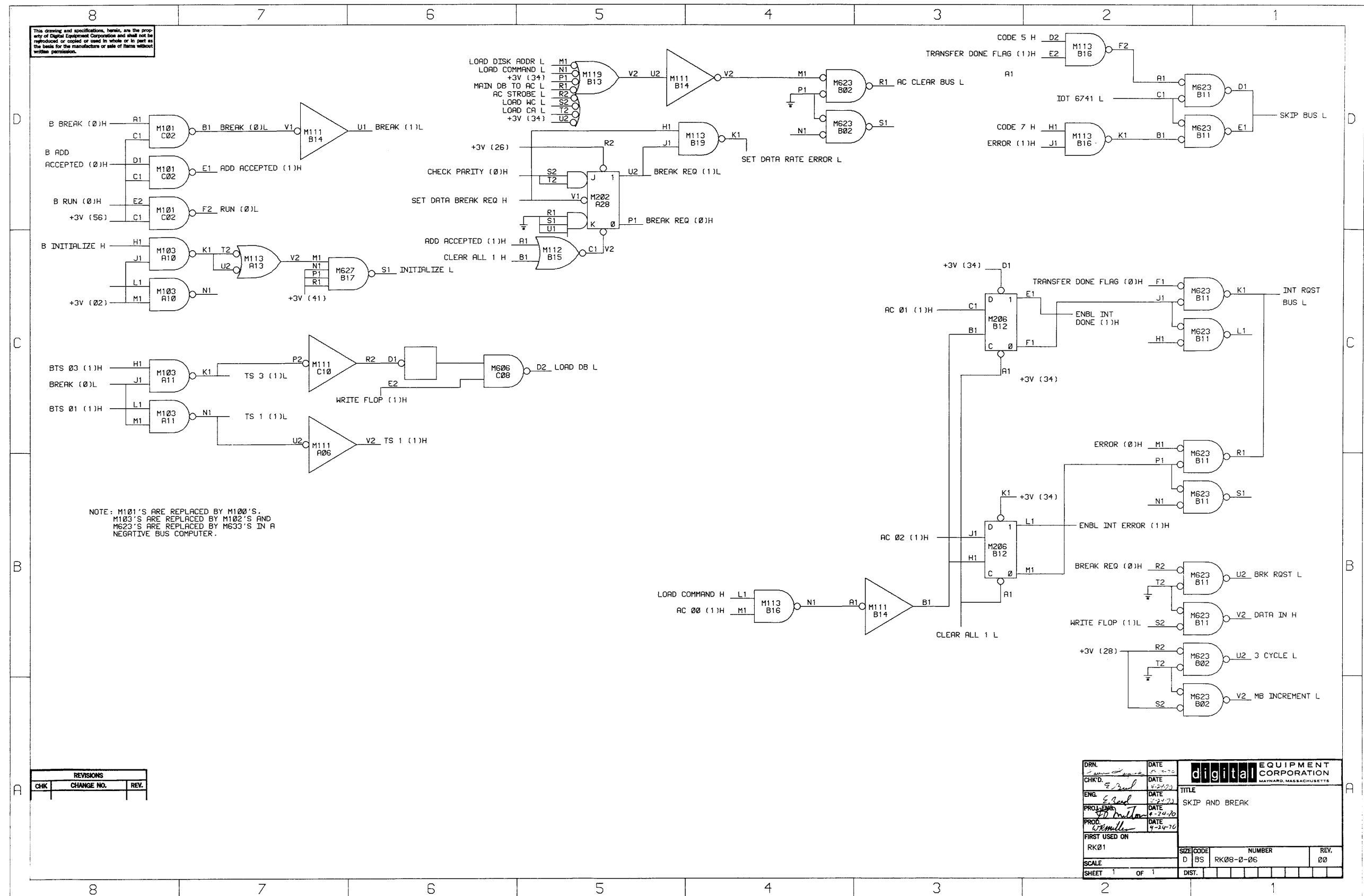


Figure II-16 Skip and Break Block Schematic Diagram

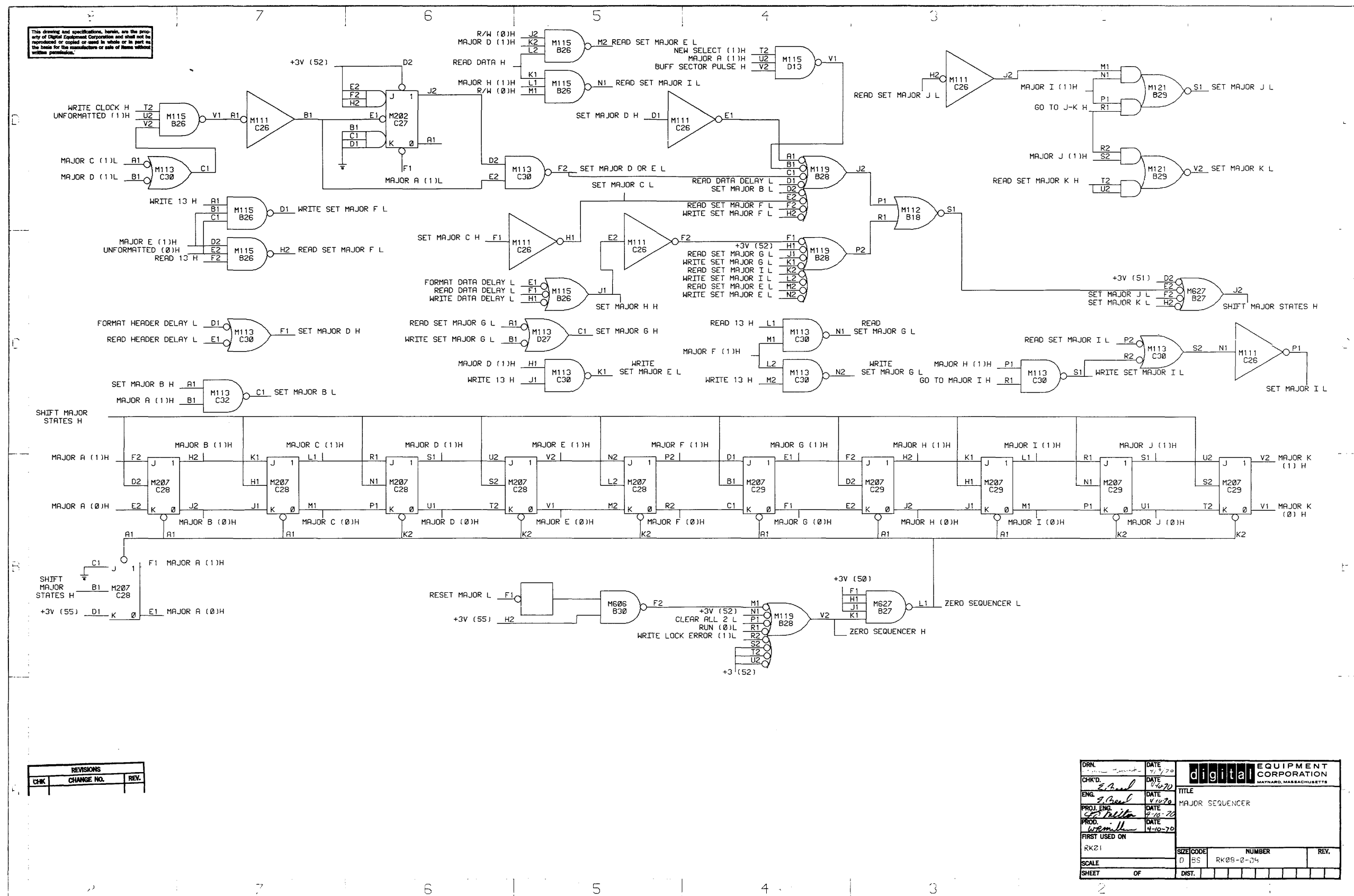


Figure II-18 Major Sequencer Register Block Schematic Diagram

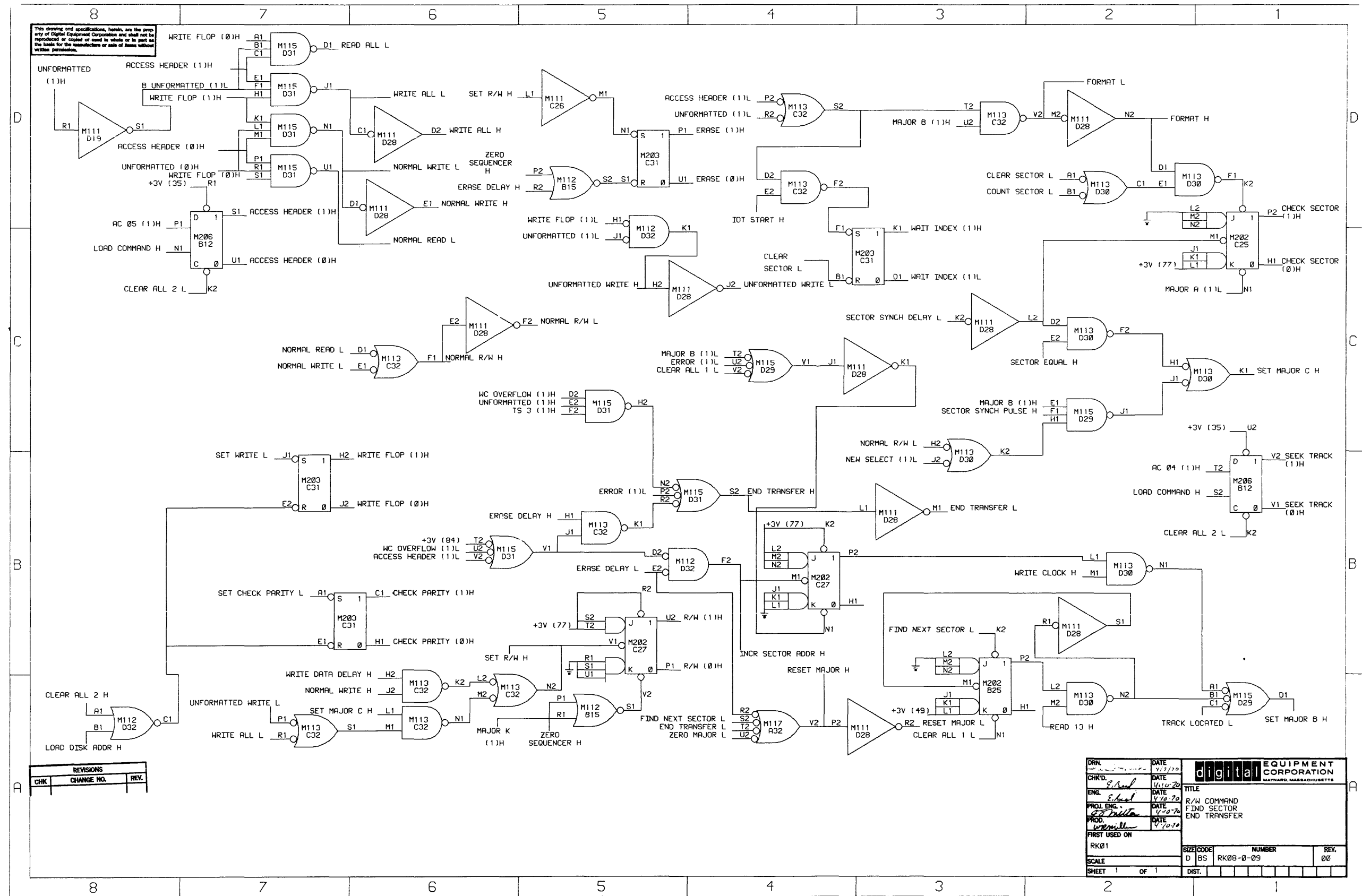


Figure II-19 R/W Command, Find Sector, End transfer
Block Schematic Diagram

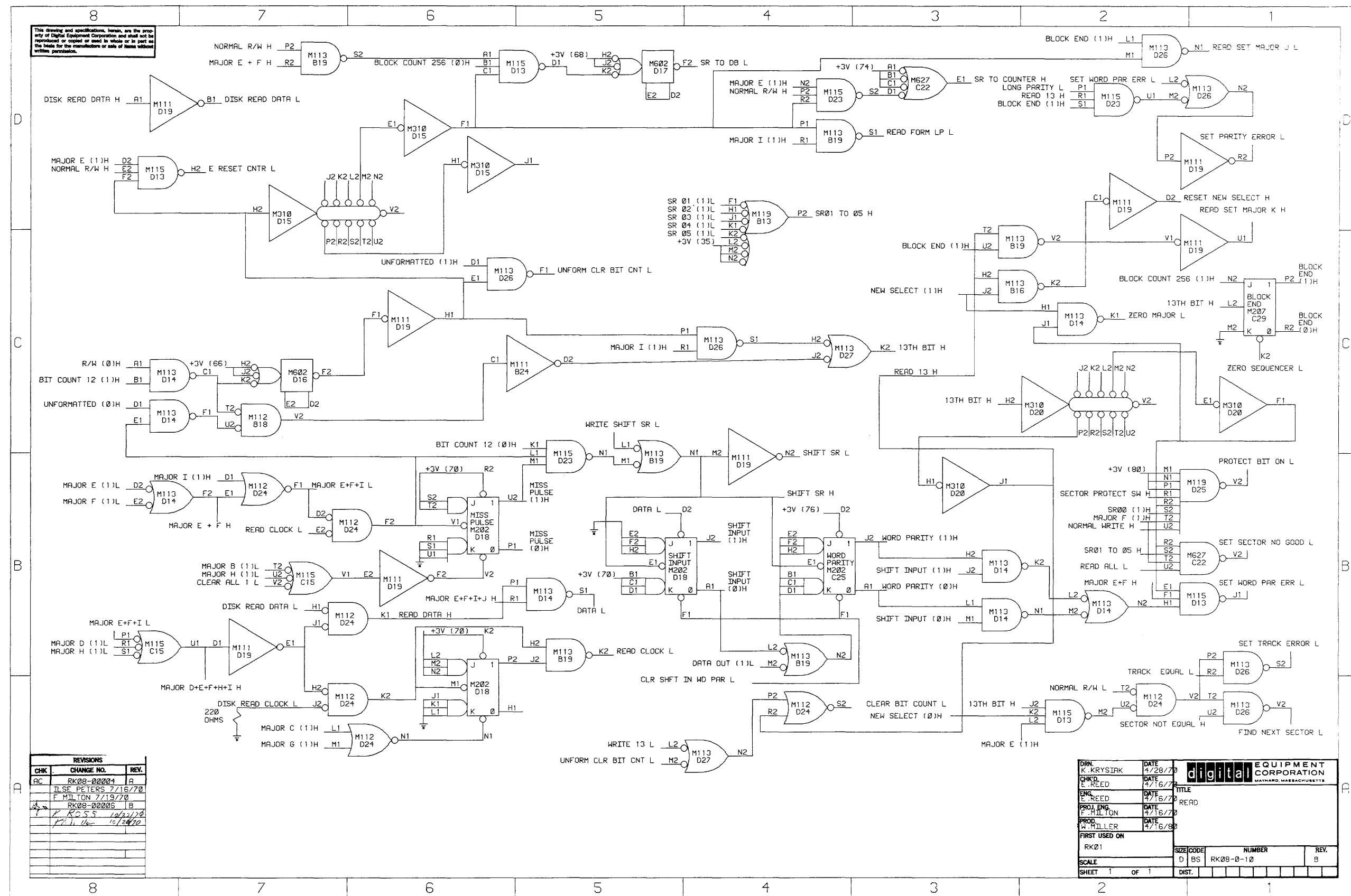


Figure II-20 Read Block Schematic Diagram

Figure II-21 Write Block Schematic Diagram

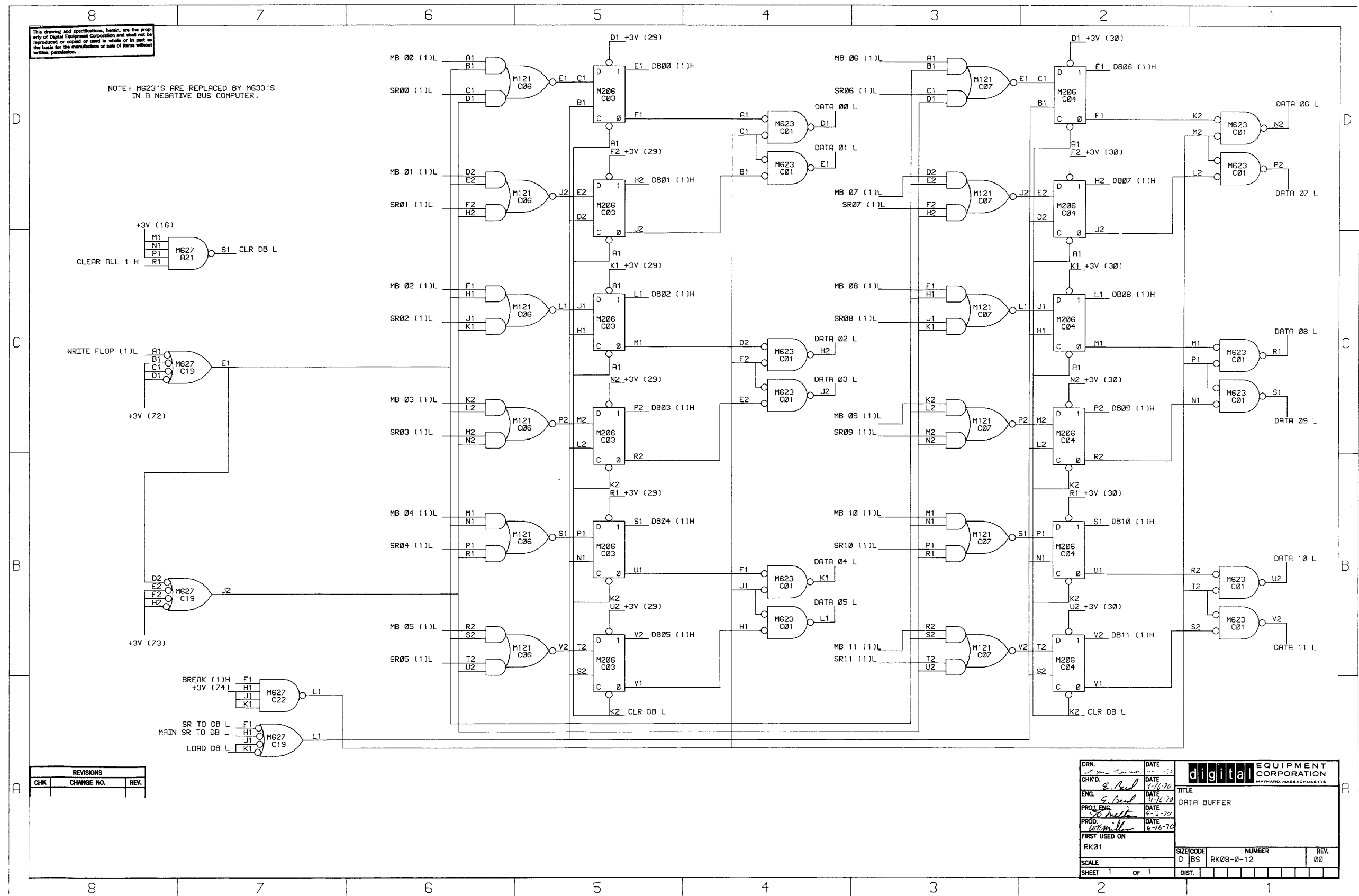


Figure II-22 Data Buffer Block Schematic Diagram

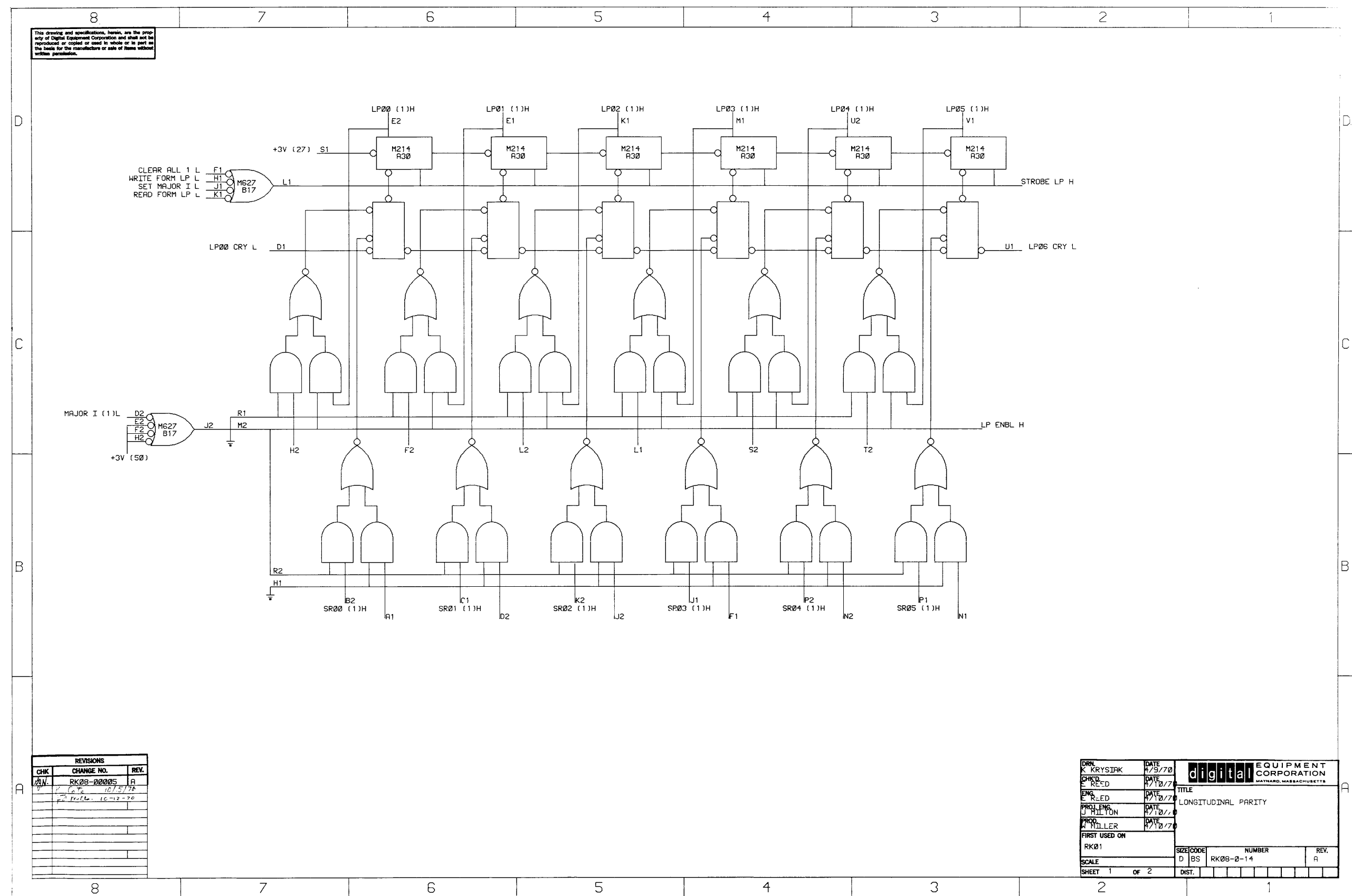


Figure II-24 Longitudinal Parity Block Schematic Diagram
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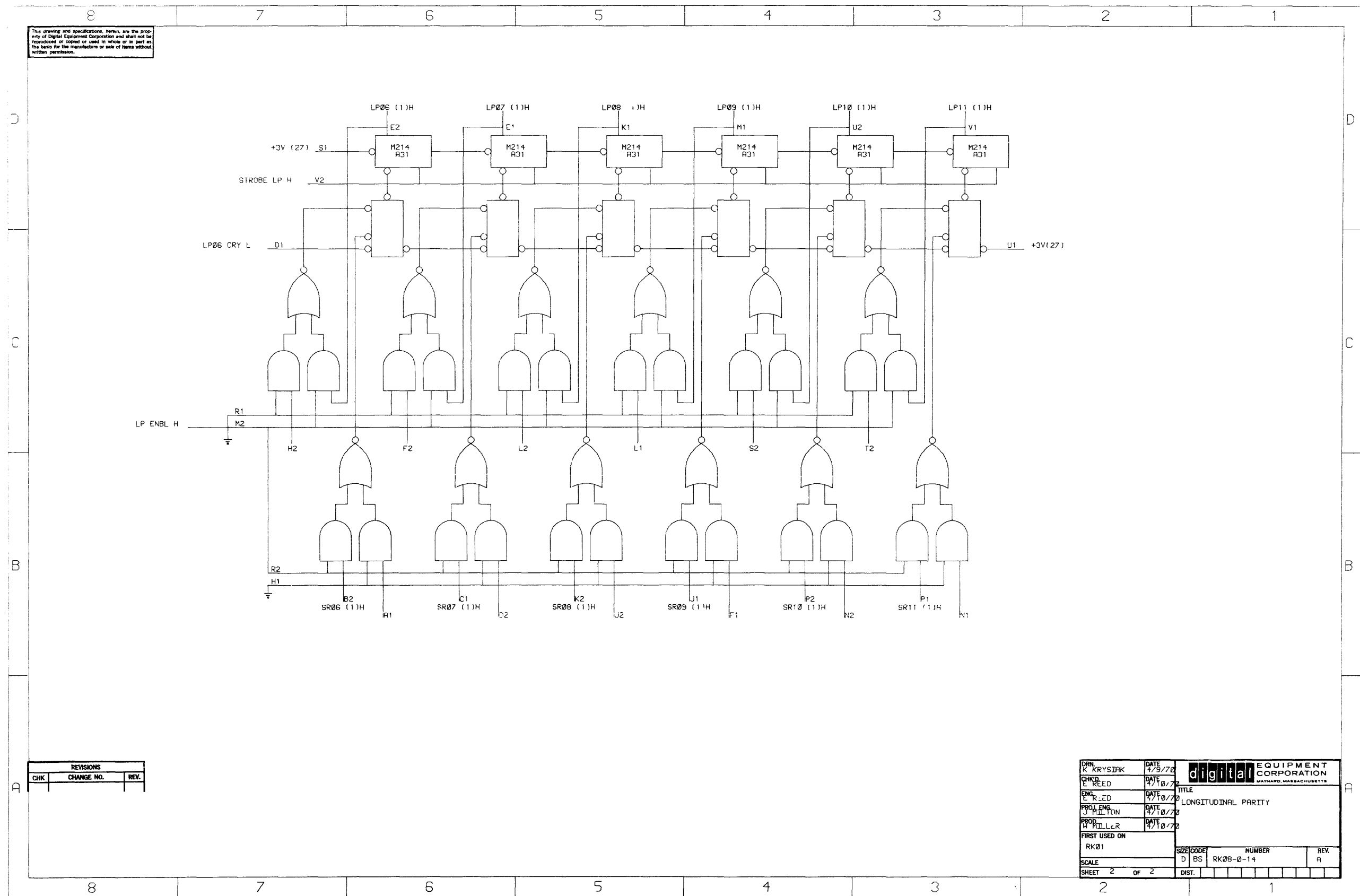
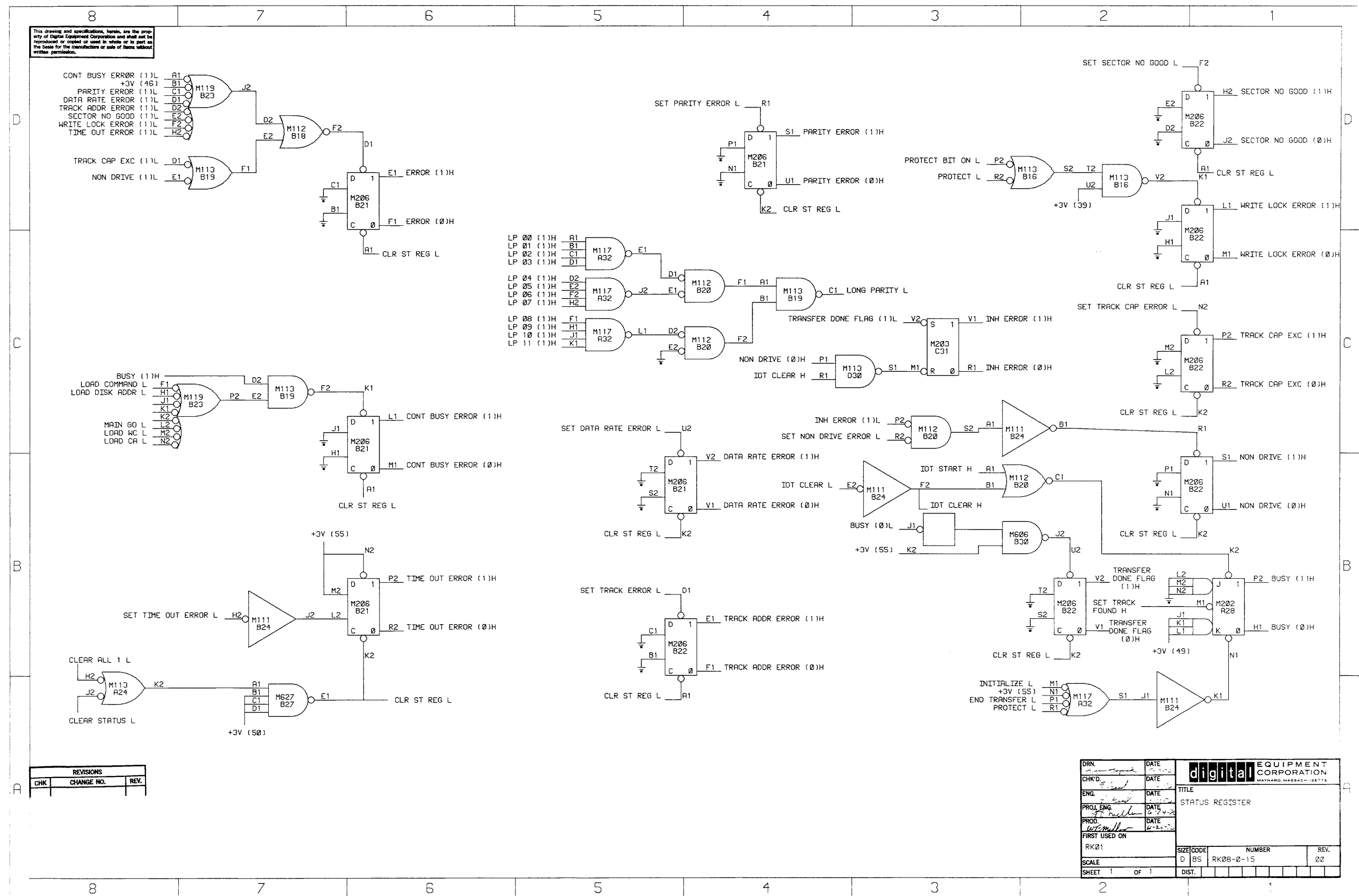


Figure II-25 Longitudinal Parity Block Schematic Diagram
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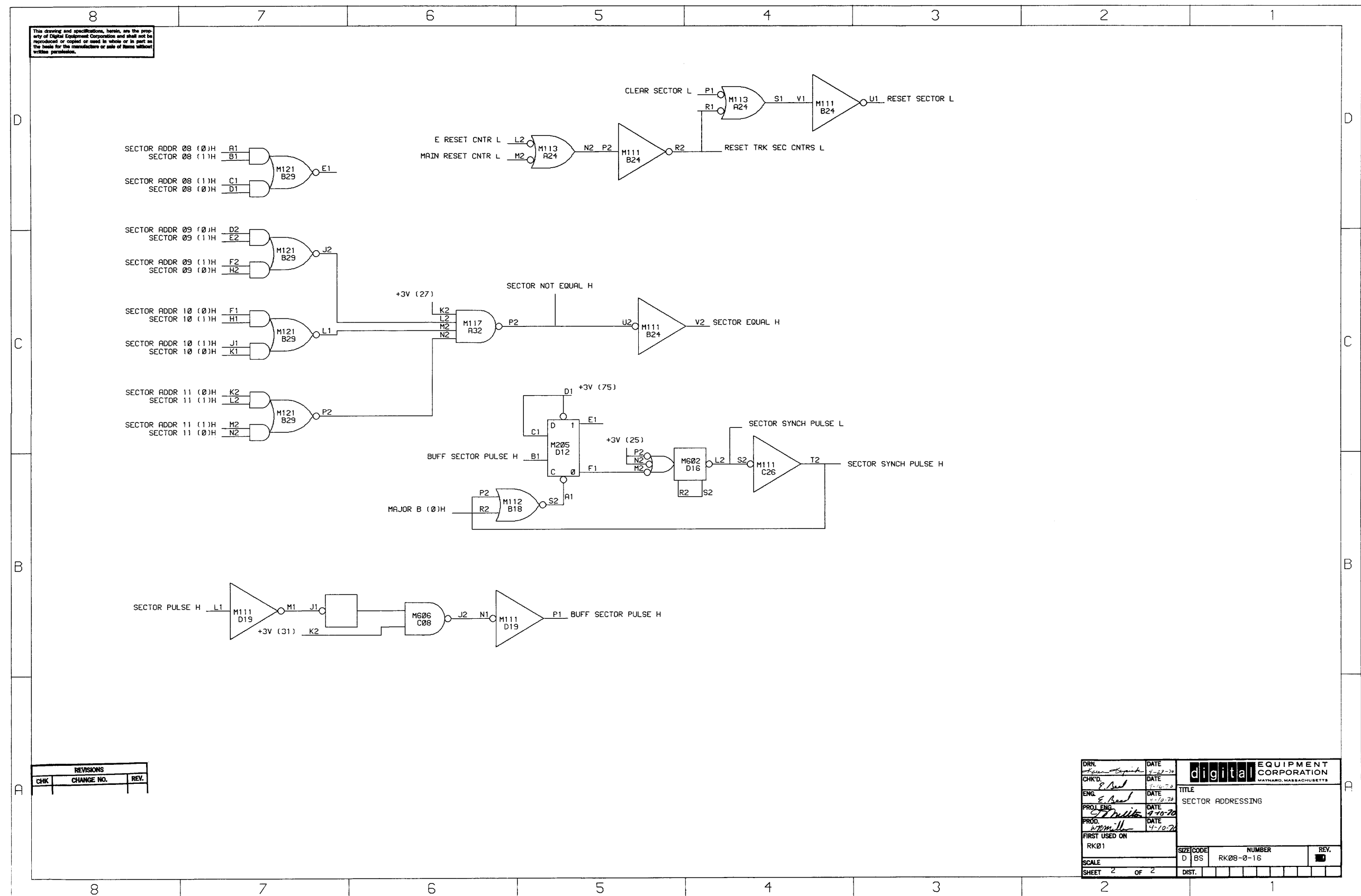


Figure II-28 Sector Addressing Block Schematic Diagram
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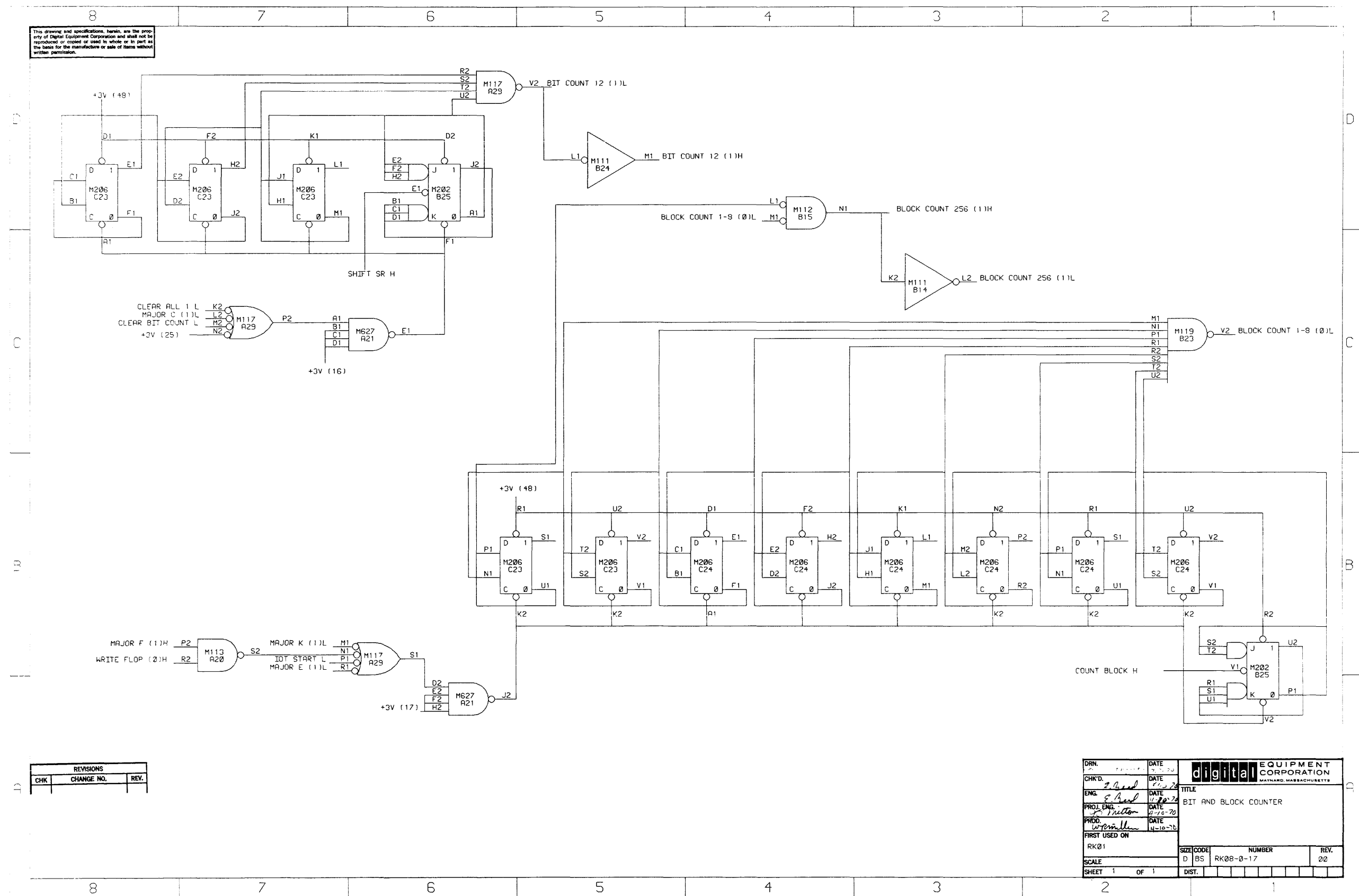


Figure II-29 Bit and Block Counter Block Schematic Diagram

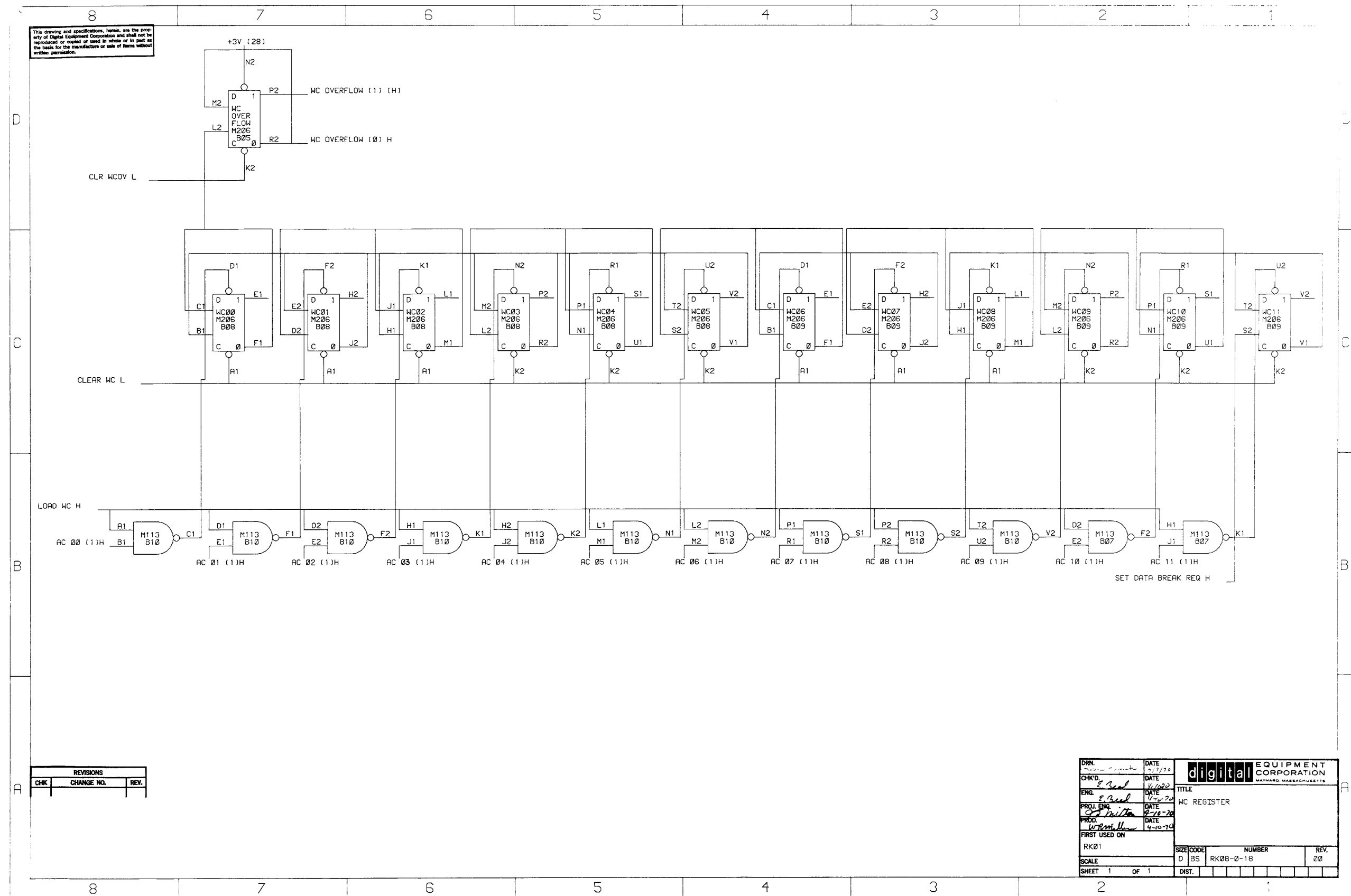


Figure II-30 Word Count (WC) Register Block Schematic Diagram

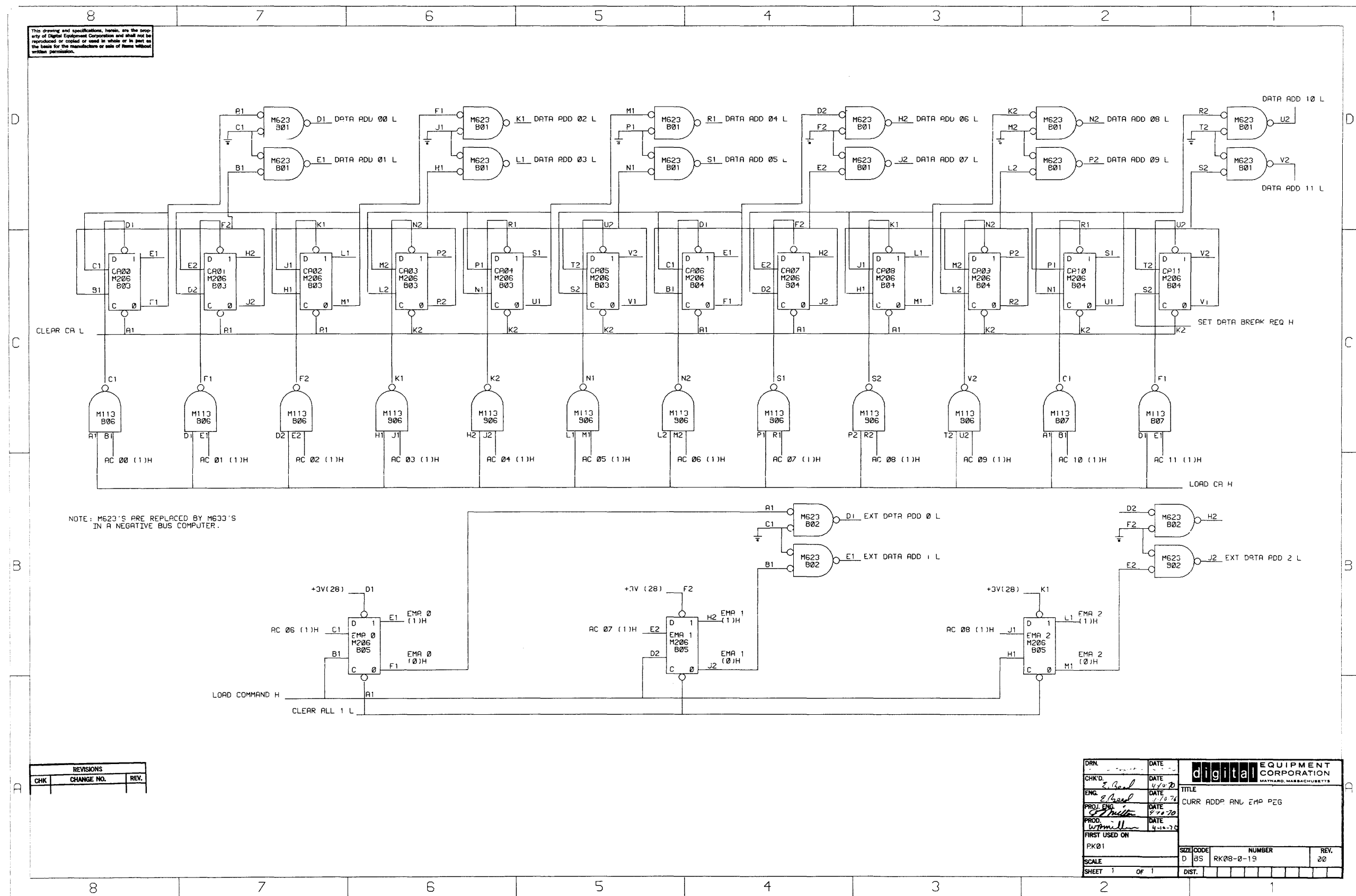


Figure II-31 Current Address and EMA Register Block Schematic Diagram

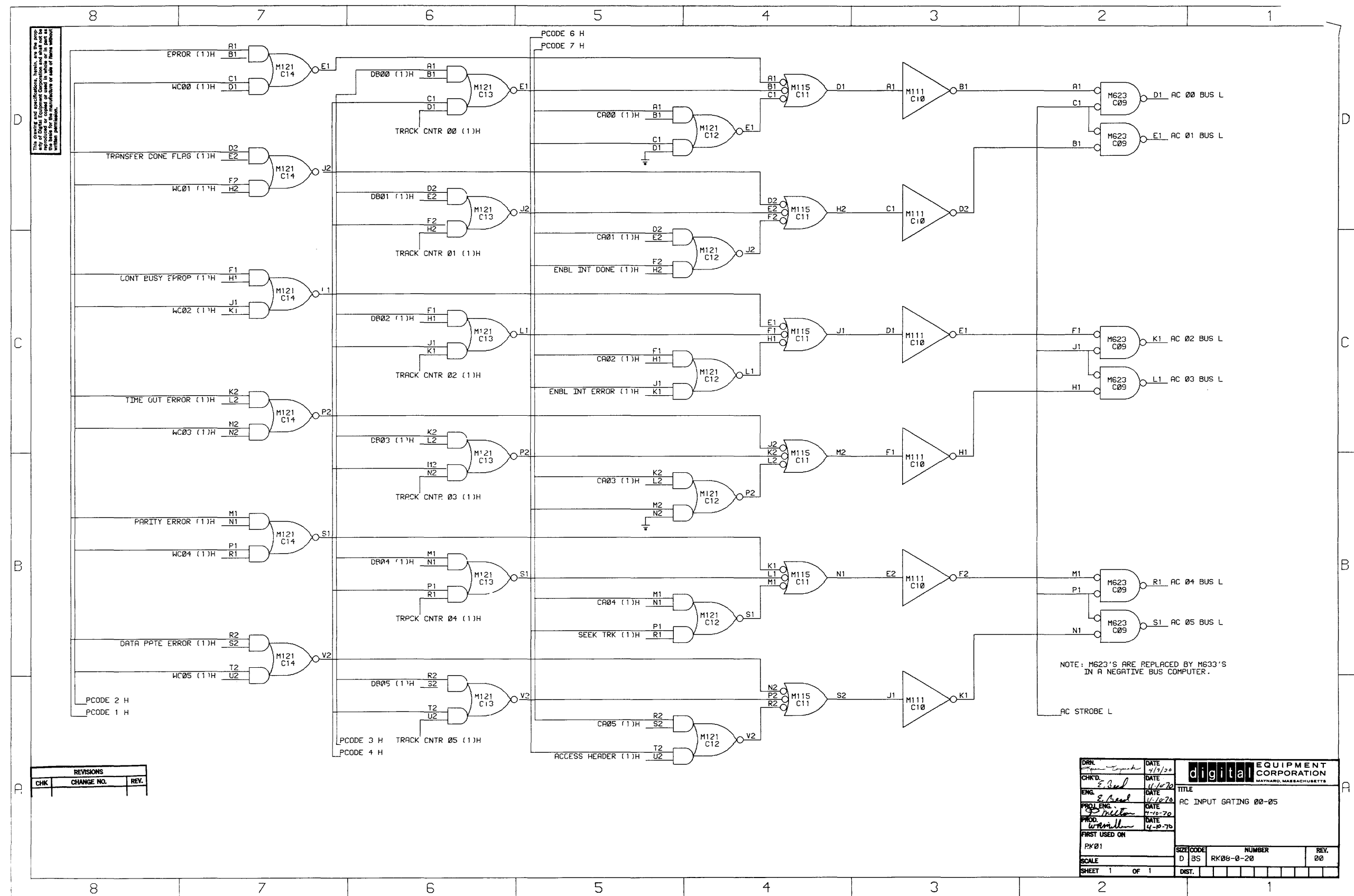


Figure II-32 AC Input Gating 00-05 Block Schematic Diagram

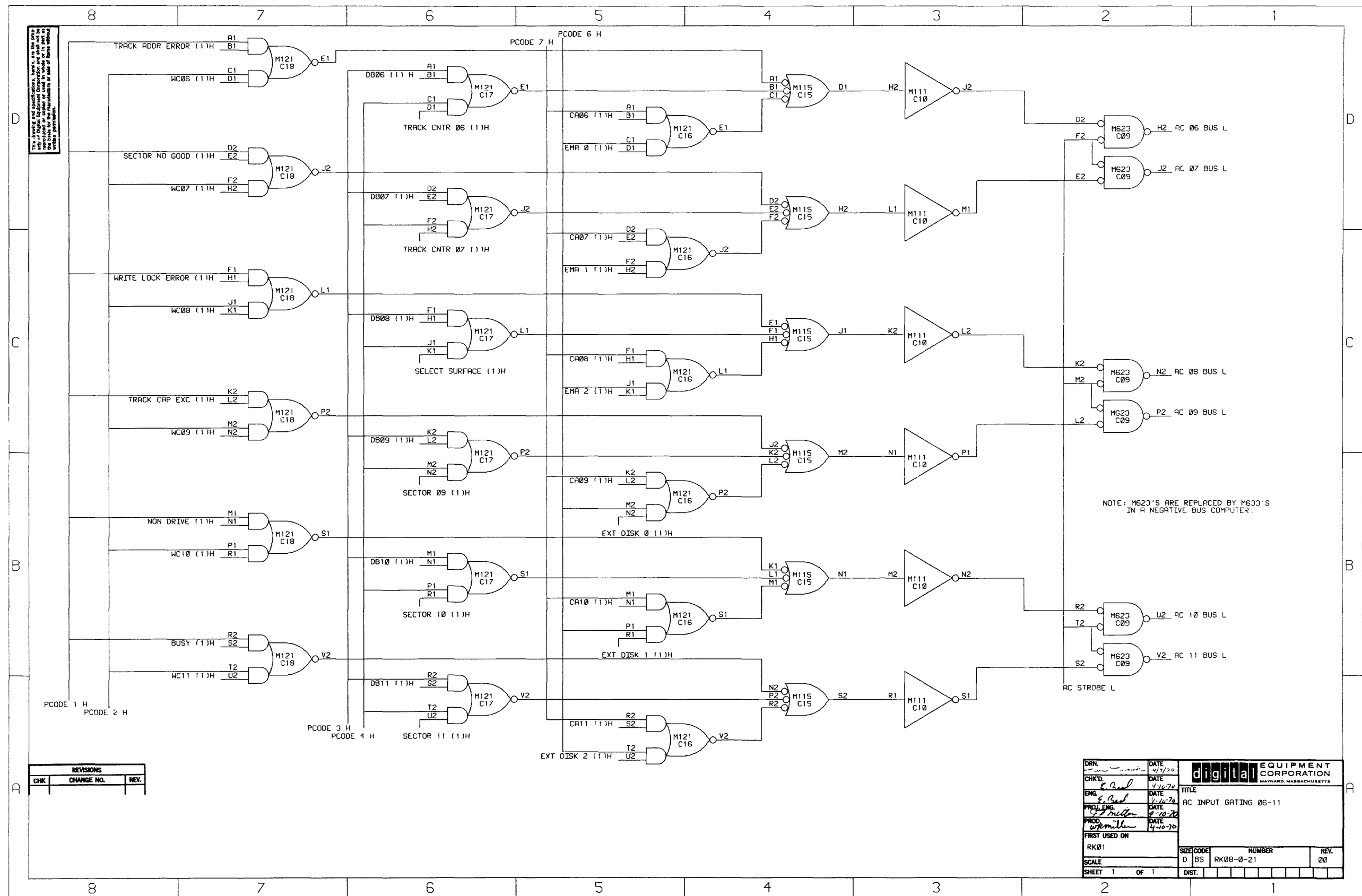


Figure II-33 AC Input Gating 06-11 Block Schematic Diagram

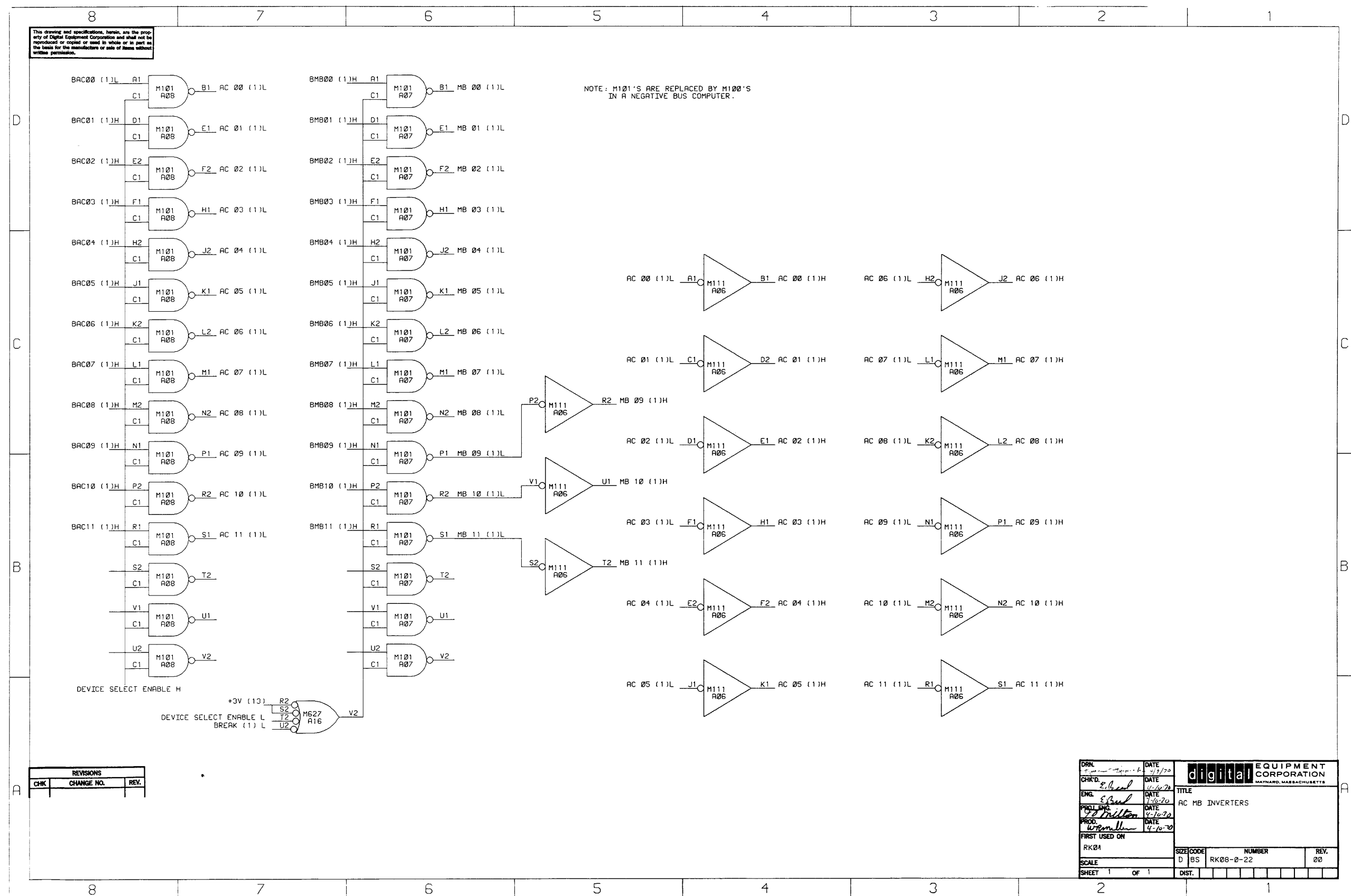
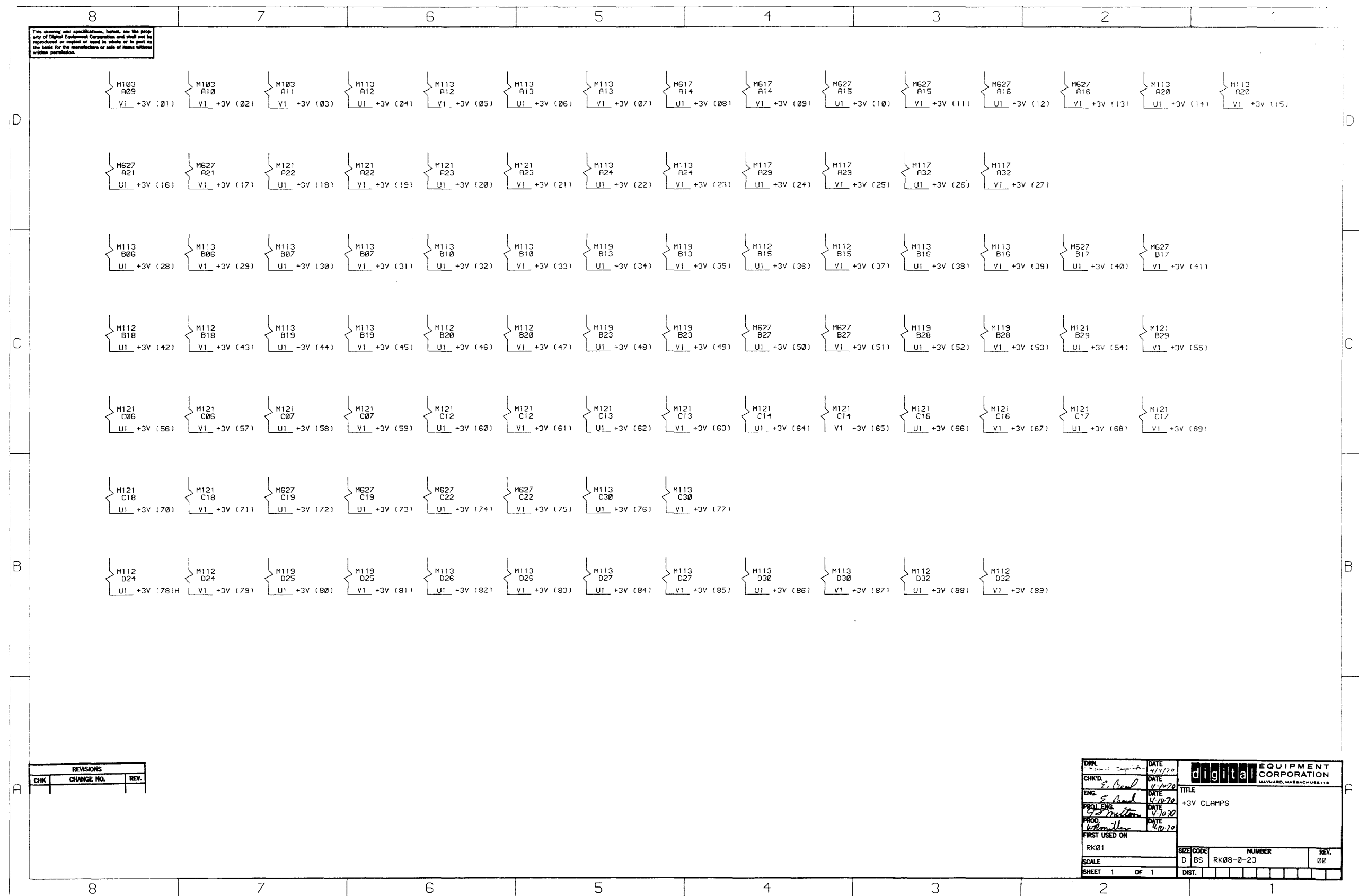


Figure II-34 AC Memory Buffer (MB) Inverters Block Schematic Diagram



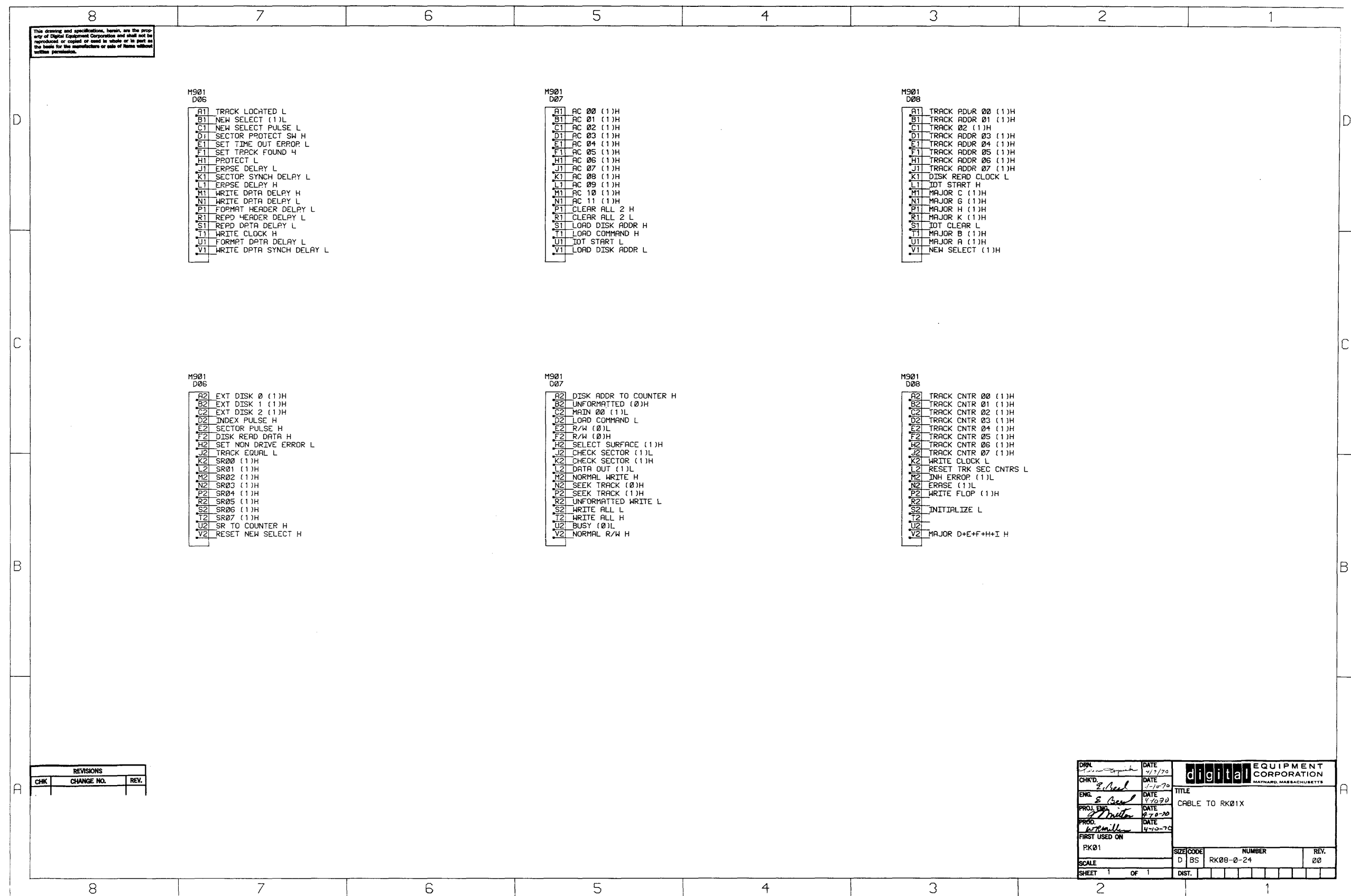


Figure II-36 Cable to RK01-X Block Schematic Diagram

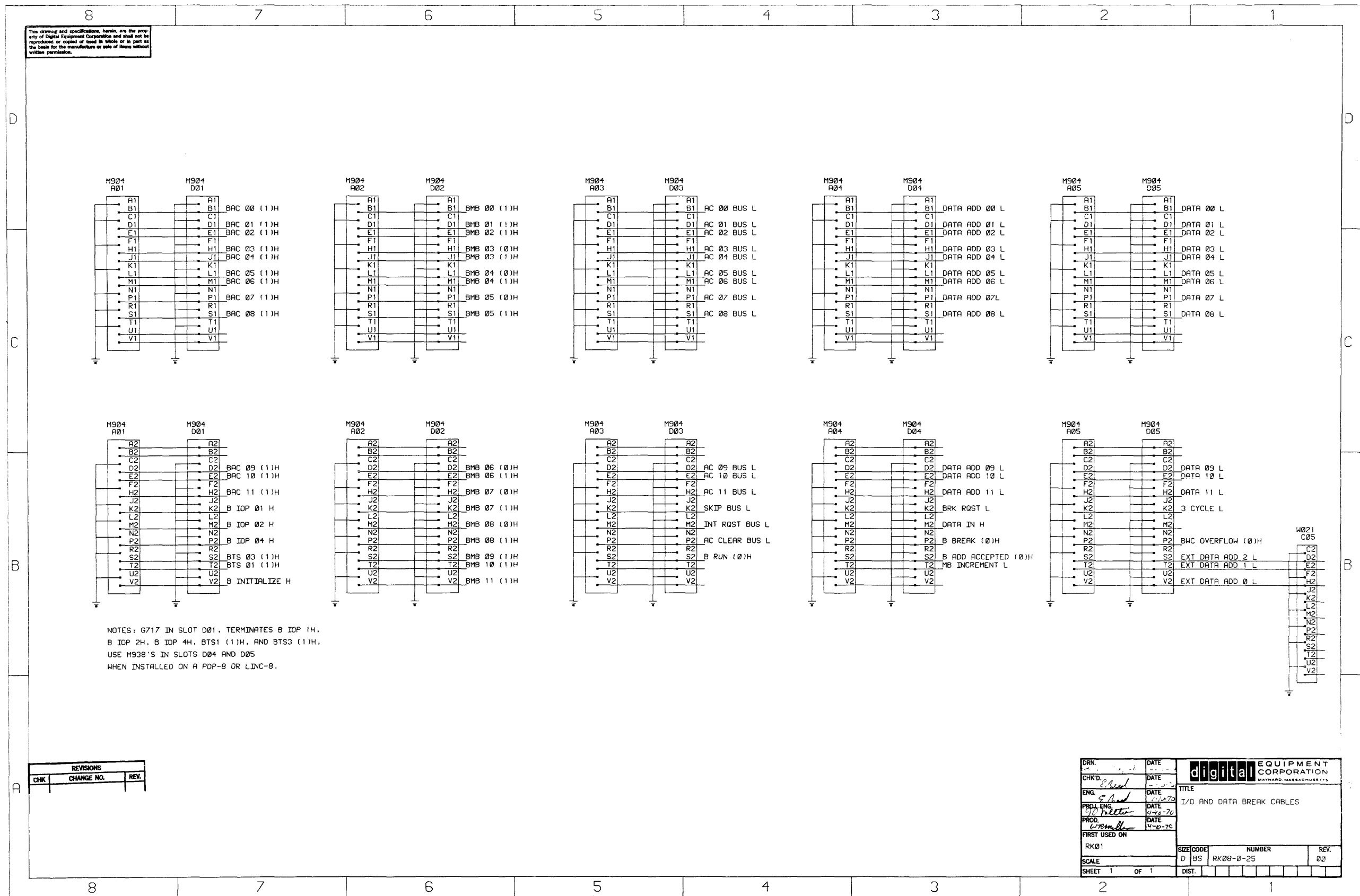


Figure II-37 I/O and Data Break Cables Block Schematic Diagram

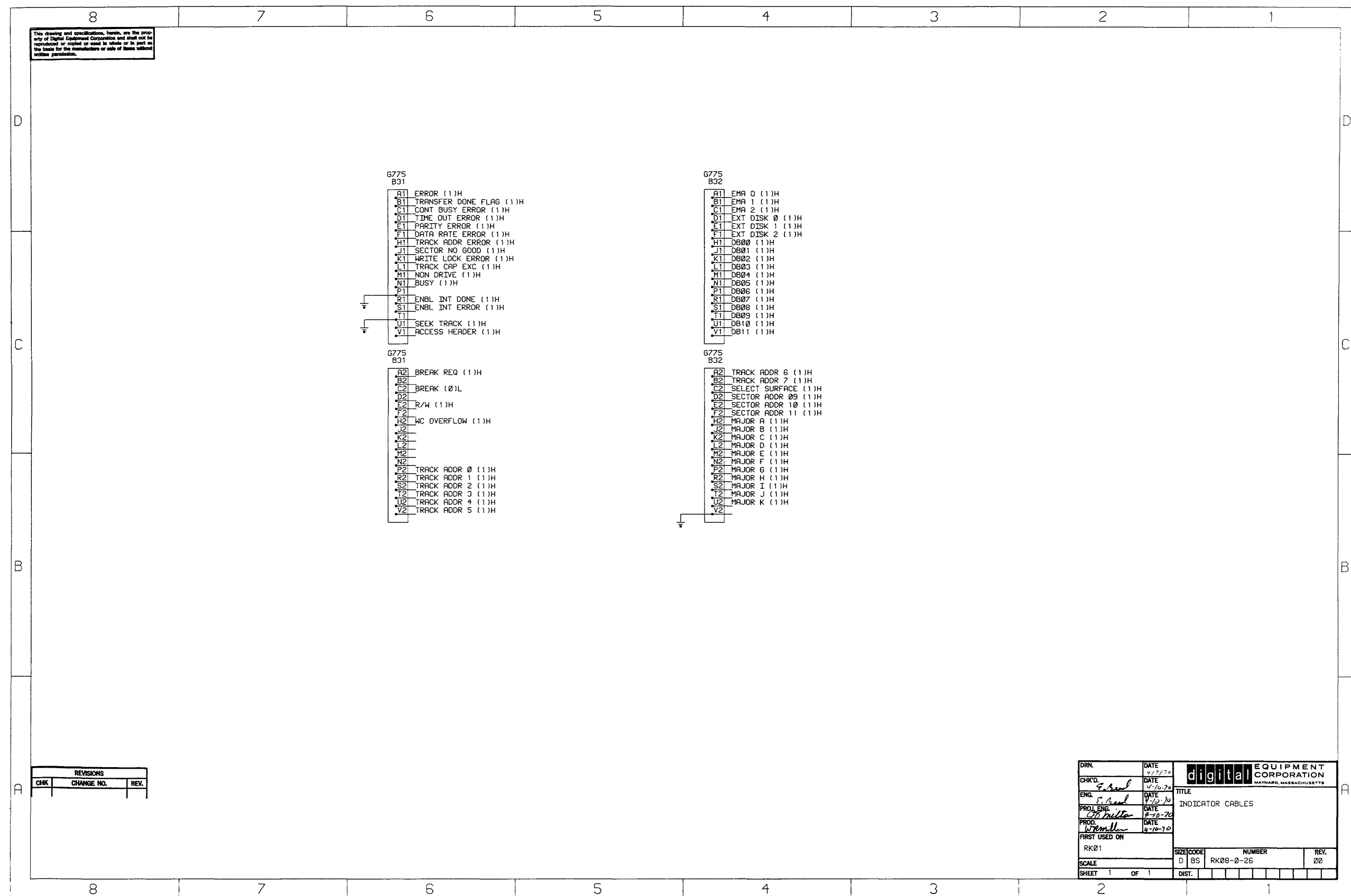


Figure II-38 Indicator Cables Block Schematic Diagram

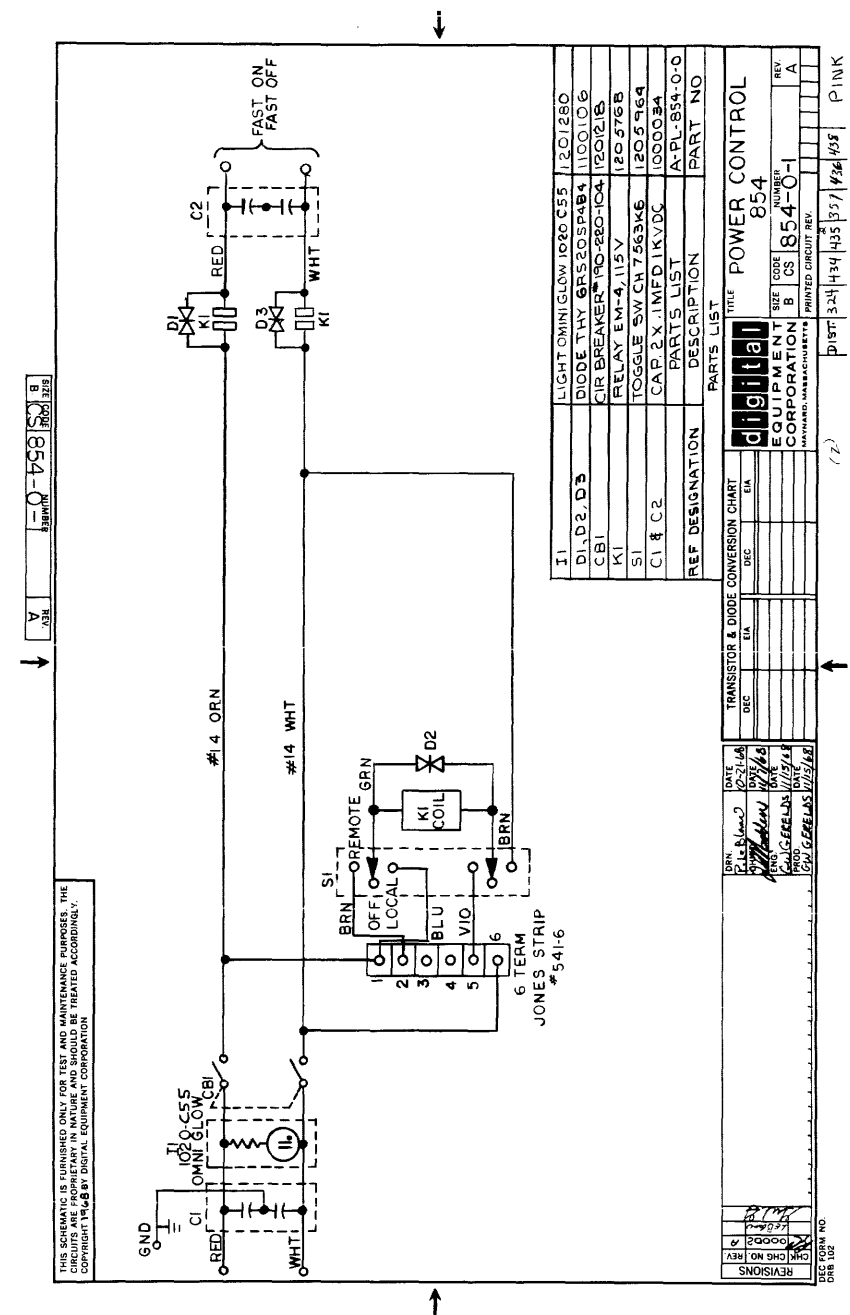
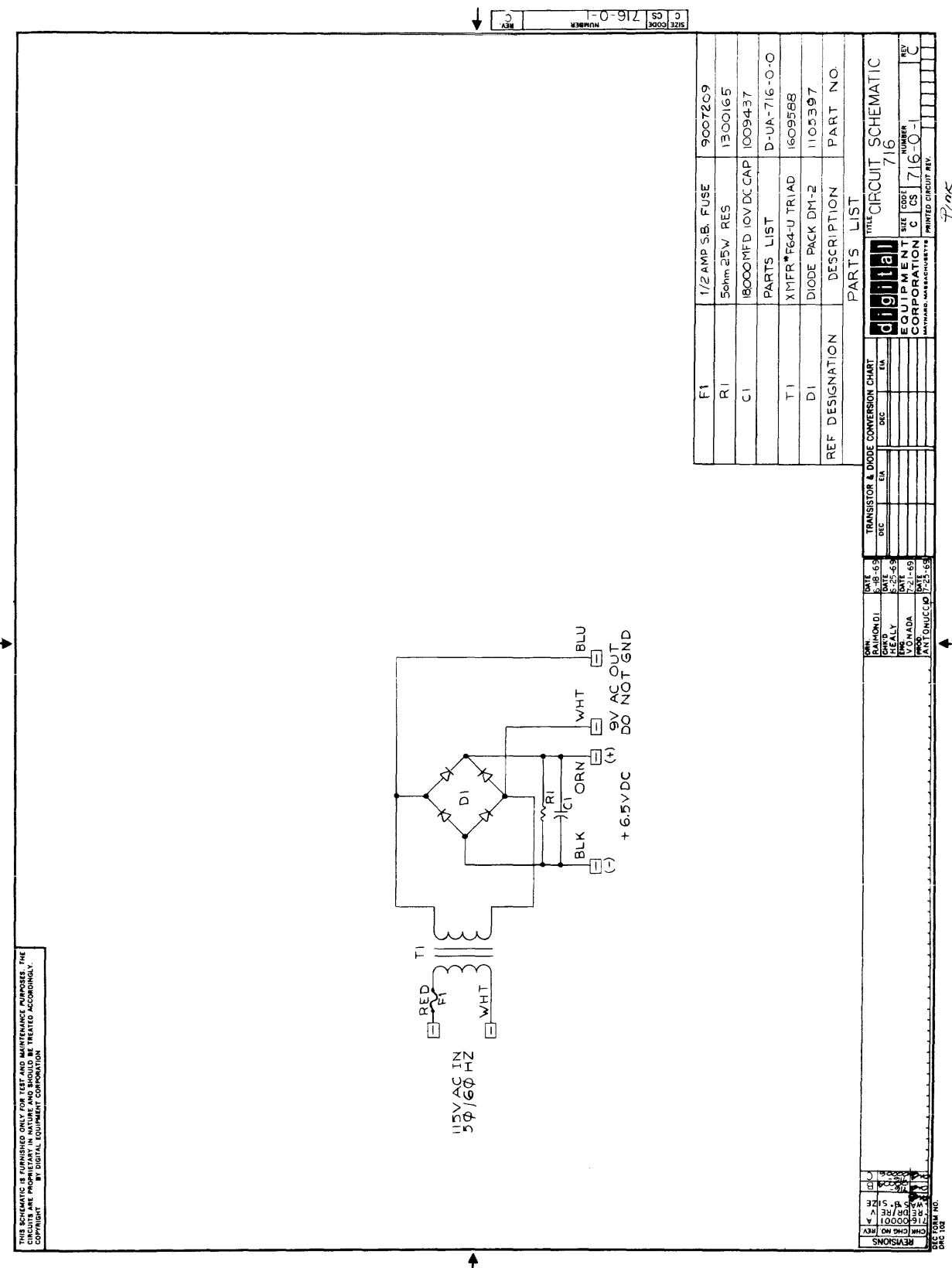


Figure II-39 Type 716 Power Supply Circuit Schematic Diagram

Figure II-40 Type 854 Power Control Panel Wiring Diagram

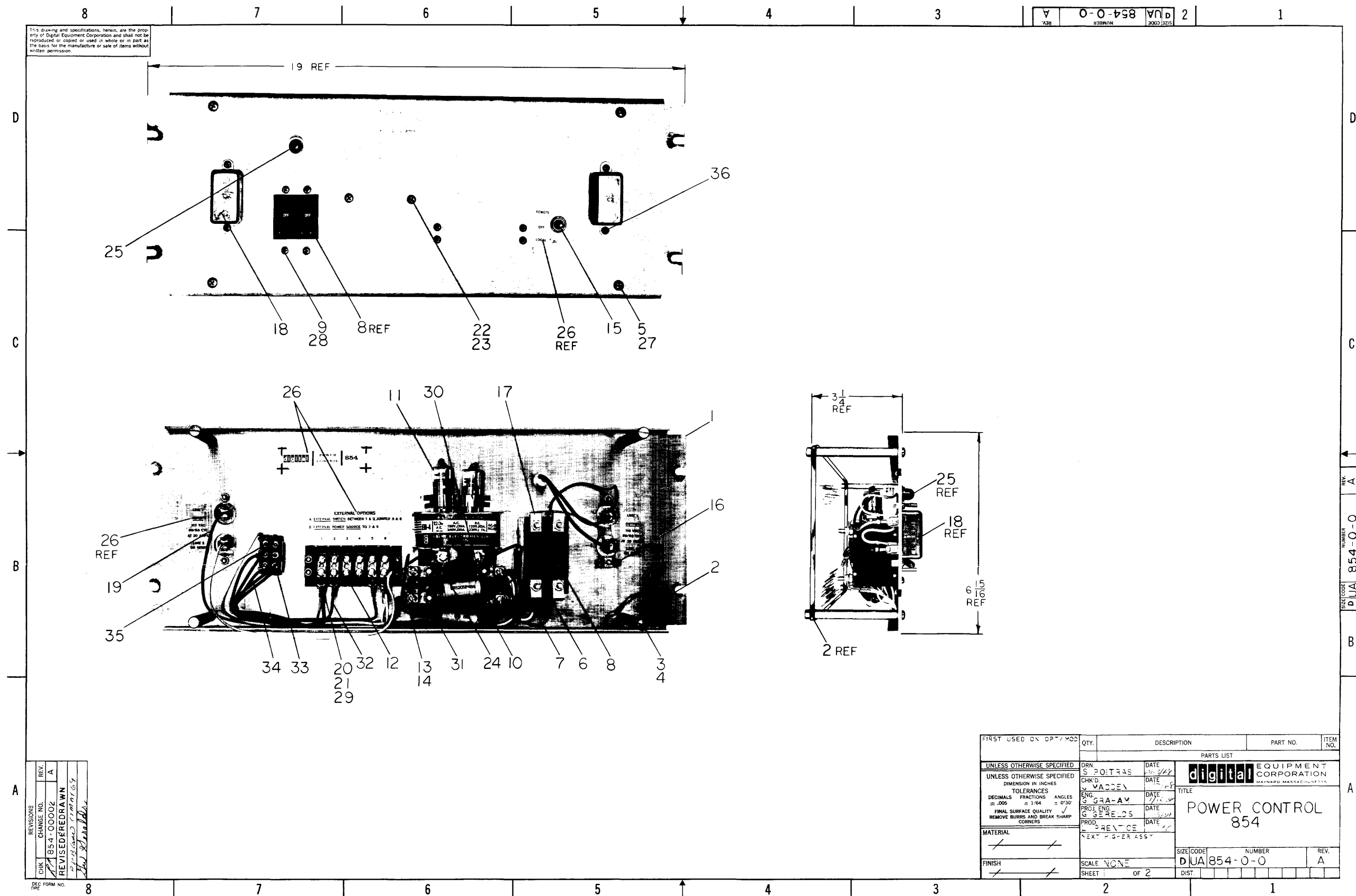


Figure II-41 Type 854 Power Control Panel
Component Pin Location Diagram
(Sheet 1)

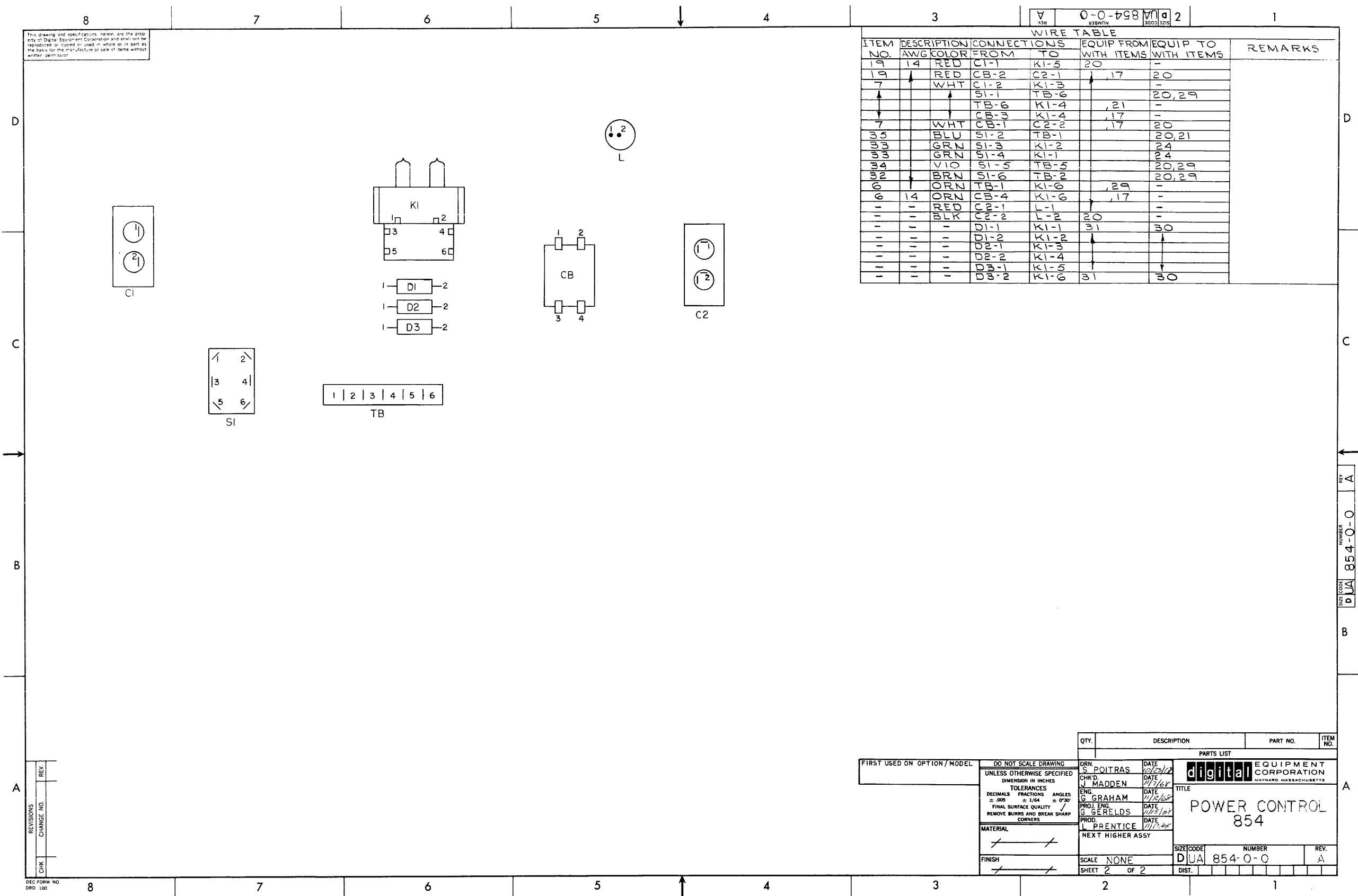


Figure II-41 Type 854 Power Control Panel
Component Pin Location Diagram
(Sheet 2)



Figure II-42 Register Indicator Panel Circuit Schematic Diagram

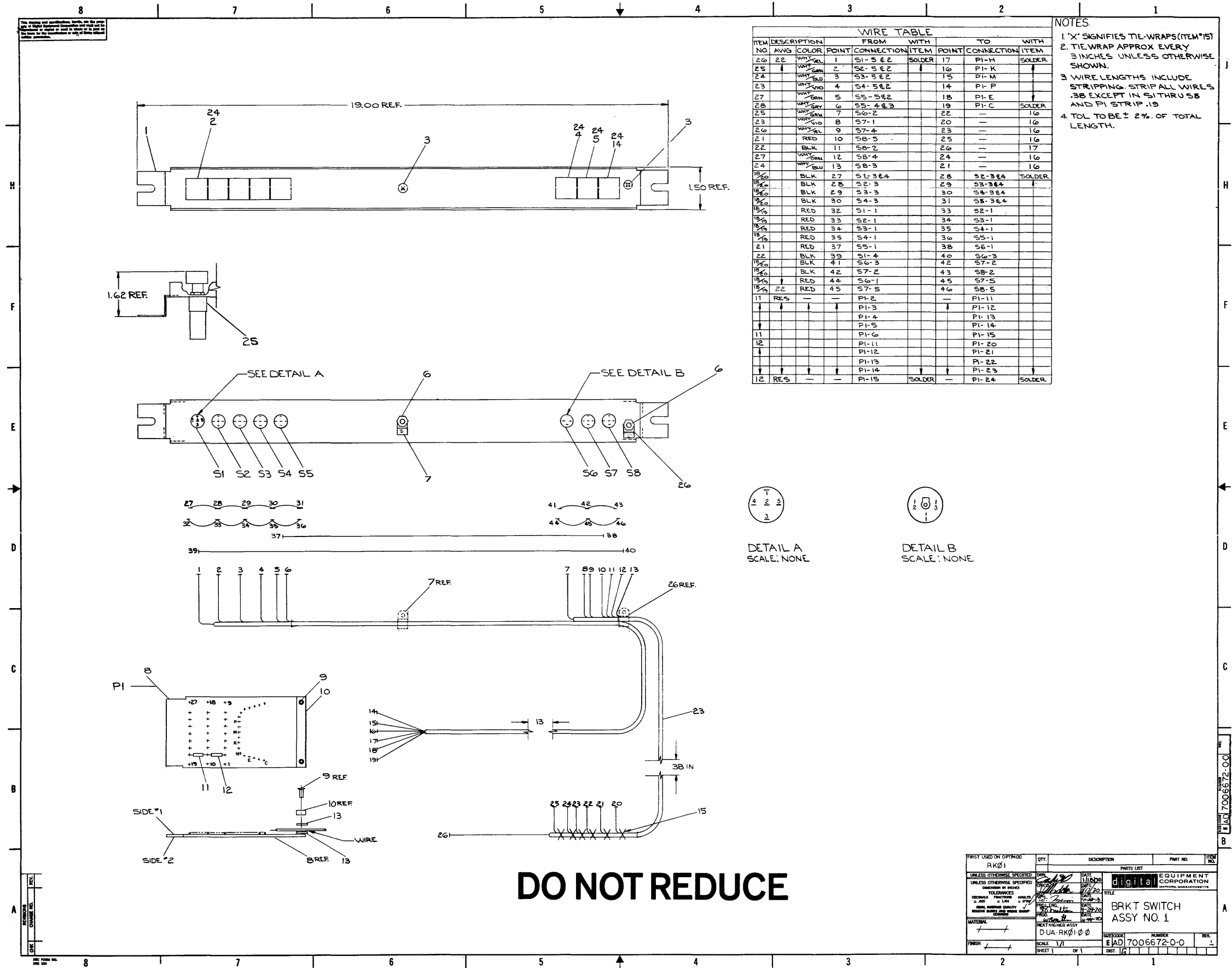


Figure II-43 Control Panel Switch Assembly Diagram



