

IDENTIFICATION

DISPTST

PRODUCT CODE: MAINDEC 12-D6BC-D  
PRODUCT NAME: VR14, VR20 DISPLAY TEST  
DATE CREATED: AUGUST 1, 1971  
MAINTAINER: DIAGNOSTICS GROUP  
AUTHOR: RAYMOND HOOP

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1. ABSTRACT  
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THIS PROGRAM TESTS THE PDP-12 DISPLAY SYSTEM BY GENERATING FOUR (FIVE IF A VR20) DISTINCT PATTERNS ON THE SCOPE, TWO (THREE IF A VR20) WITH THE DIS INSTRUCTION AND TWO WITH THE DSC INSTRUCTION.

2. REQUIREMENTS  
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2.1 EQUIPMENT  
-----

A. PDP-12A OR PDP-12B

2.2 STORAGE  
-----

MOST OF LOCATIONS 4000(8) TO 6000(8)

3. LOADING PROCEDURES  
-----

3.1 METHOD  
-----

LOAD THIS PROGRAM INTO MEMORY BY THE STANDARD LOADING PROCEDURE FOR A BINARY PROGRAM.

4. OPERATOR ACTION  
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SET THE CHANNEL SELECTOR SWITCH ON THE DISPLAY TO THE 1 & 2 POSITION (IF A VR20, SET THE COLOR SWITCH TO THE REMOTE POSITION AND SET SNS 5). PLACE IN LINC-MODE, DEPRESS START 20. UPON STARTING, THE PROGRAM WILL ALTERNATELY DISPLAY THE FOUR (FIVE IF A VR20) PATTERNS, EACH FOR APPROXIMATELY TEN SECONDS.

A. FREEZE ON CURRENT PATTERN,

SETTING OF SENSE SWITCHES 0-4 WILL CAUSE THE PROGRAM TO LOCK ON THAT PATTERN.

B. ALTERNATE BETWEEN FOUR (FIVE IF A VR20) PATTERNS, RESETTING OF SENSE SWITCHES 0-4 WILL DIRECT THE PROGRAM TO ALTERNATE THE DISPLAY BETWEEN THE FOUR(FIVE IF A VR20) PATTERNS. IT SHOULD BE NOTED THAT REQUESTING A FREEZE ON A PATTERN (IE, 4) SETTING OF SNS 0-3 WILL CAUSE IT TO EXECUTE THE LEFT MOST SNS THAT IS SET.

C. RETURN TO DIAL.

TYPE "CTRL D" OF THE CONSOLE TELETYPE TO RETURN TO DIAL;

D. ADJUSTMENT OF THE COLOR DELAYS (SHORT-LONG)

TO SCOPE THE DELAYS START LINC=MODE 400;

5. PROGRAM DESCRIPTION  
-----

A. PATTERN 0

THIS PERMITS CALIBRATION OF THE SCOPE.

B. PATTERN 1

THE PATTERN GENERATED BY THE DSC INSTRUCTION TAKES THE FOLLOWING FORM:

(QUADRANT 2)    CHAN 1    CHAN 2  
                  HALF SIZE    FULL SIZE    (QUADRANT 1)

(QUADRANT 3)    CHAN 1    CHAN 2  
                  FULL SIZE    HALF SIZE    (QUADRANT 4)

THE PATTERN DOES WHAT THE DISPLAY SAYS. ONE HALF OF ONE CHARACTER IS DISPLAYED IN ONE CORNER OF THE SCOPE THEN HALF OF ONE CHARACTER IS DISPLAYED IN THE OPPOSITE CORNER OF THE SCOPE. THE LEFT HALF OF THE CHARACTER IN QUADRANTS 2 AND 4 ARE DISPLAYED FIRST, THEN THE LEFT HALF OF THE CHARACTER IN QUADRANTS 1 AND 3 ARE DISPLAYED. WHEN THE LEFT HALF OF ALL CHARACTERS ON THE SCOPE HAVE BEEN DISPLAYED THE SEQUENCE IS REPEATED FOR THE RIGHT HALF OF THE CHARACTERS.

C. PATTERN 2

DISPLAY AN X PATTERN.

THIS PATTERN IS 2 DIAGONAL LINES FROM TOP LEFT CORNER TO BOTTOM RIGHT CORNER, AND FROM BOTTOM LEFT CORNER TO TOP, RIGHT CORNER. THIS PATTERN IS USED TO ADJUST DEFLECTION AMPLIFIERS OF THE VR14.

D. PATTERN 3

TWO COLOR OVERLAY DISPLAY TEST (VR20)

THIS PATTERN IS USED TO ADJUST THE GAIN CONTROLS OF THE RED AND GREEN AMPLIFIERS. THE PATTERN IS A BOX ON THE OUTER EDGE OF THE SCREEN, AN "X" IN THE CENTER AND THE WORDS "RED" AND "GREEN". THE DOTS SHOULD CONVERGE THE RESULT BEING AN ORANGE COLOR DISPLAY THE WORDS "RED" AND "GREEN" WILL REMAIN UNCHANGED. IF A HALT OCCURS DURING THIS TEST, RUN THE COLOR DELAY ADJUSTMENT ROUTINE.

F. PATTERN 4

POWER SUPPLY REGULATION TEST

THIS PATTERN IS USED TO TEST THE POWER SUPPLY REGULATION OF THE VR20. THE WORD MAINDEC IS DISPLAYED IN THE CORNERS, AND THE MAINDEC NUMBER IS DISPLAYED IN THE CENTER. THE CHARACTERS SHOULD NOT MOVE MORE THAN 1/8 OF AN INCH.

/VR14, VR20 DISPLAY CONTROL AND SCOPE TEST

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SKP=0456

0456

/POINT DISPLAY PATTERN [DISPAT  
/CHARACTER DISPLAY PATTERN [DSCRPT  
/DISPLAY X PATTERN [DISP  
/DISPLAY COLOR [DISCL  
/DISPLAY POWER SUPPLY REGULATION [PSREG

4000  
4010 \*0010  
0000 04BETA, 0  
4011 0000 03BETA, 0  
4012 0000 02BETA, 0  
4013 0000 01BETA, 0

4020 \*20  
4021 0011 CLR  
4022 5430 STC SWITCH  
4023 0077 SET I 17  
4024 7477 -300 INCHB  
4025 6026 JMP DISPAT  
4025 6100 JMP

/TEST INCREMENT THE M.B.  
/OUTPUT THE PATTERNS

4026	1000	INCMB,	LDA	/GET THE RETURN
4027	0000		0000	/ ADDRESS
4030	4066		STC	/SAVE IN EXIT
4031	4000	EXMB	STC	/CLEAR LOG, 0
4032	0160		DIS I	/DISPLAY A POINT
4033	1000		LDA	/GET THE VALUE IN
4034	0000		0000	/ LOC, 0000
4035	1460		SAE I	/IS IT EQUAL TO
4036	0001		0001	/THE EXPECTED
4037	0000		HLT	/NO, INCREMENT THE
4040	0011		CLR	/MB FAILED AFTER
4041	0004		ESF	/A DIS INSTRUCTION
4042	4001		STC	/CLEAR A.C.
4043	1760	1	DSC I	/CLEAR S.F.R.
4044	4477		4477	/CLEAR LOC, 0001
4045	1000		LDA	/DISPLAY A CHARACTER
4046	0001		0001	/LOAD THE A.C. WITH
4047	1460		SAE I	/THE VALUE IN LOC, 0001
4050	0004		0004	/IS IT EQUAL TO
4051	0000		HLT	/THE EXPECTED VALUE?
4052	1020		LDA I	/INCREMENT THE M.B.
4053	0200		0200	/FAILED AFTER A
4054	0004		ESF	/DSC INSTRUCTION (HALF-SIZE)
4055	0011		CLR	/LOAD THE A.C.
4056	4001		STC	/ WITH 0200
4057	1760	1	DSC I	/LOAD S.F.R.
4060	7744		7744	/CLEAR A.C. AND
4061	1000		LDA	/ LOC, 0001
4062	0001		0001	/DISPLAY A CHARACTER
4063	1460		SAE I	/LOAD THE A.C. WITH
4064	0010		0010	/ VALUE IN LOC 1
4065	0000		HLT	/IS IT EQUAL TO
4066	6066	EXMB,	JMP	/THE EXPECTED?
				/NO, INCREMENT THE
				/MB FAILED AFTER
				/A DSC INSTRUCTION (FULL-SIZE)

4100 \*100

/THE SUBROUTINE BELOW WILL GENERATE 5  
 /LINES ACROSS THE SCREEN, THE POINT  
 /SPACING IS 4 UNITS  
 /THE FIRST LEFT HAND POINT IS  
 /0000, THE LAST RIGHT HAND POINT IN  
 /EACH LINE IS 0774.

/A GLITCH IS PLACED AT THE HORIZONTAL  
 /POINTS OF 100,300,500 AND 700 ON  
 /EACH LINE

4100	0077	DISPAT, SET I	17	/LOAD THE CLOCK
4101	7500	JMP	7500	
4102	6667	TST1A,		CLOCK
4103	6105	JMP		TST1B
4104	6447	JMP		DSCPT
4105	1020	TST1B, LDA I		
4106	0010		10	
4107	0004	ESF		
4110	4136	STC		REL
4111	0062	SET I	2	
4112	2000		0	
4113	6137	TST1LP, JMP		LP1
4114	1000	LDA		
4115	0002		2	
4116	1660	BCO I		
4117	0100		100	
4120	1560	BCL I		
4121	7600		7600	
4122	0470	AZE I		TIGL
4123	6210	JMP		
4124	1000	LDA		
4125	0002		2	
4126	1120	ADA I		
4127	0004		4	
4130	1040	STA		
4131	0002		2	
4132	1460	SAE I		
4133	1000		1000	
4134	6113	JMP		TST1LP
4135	6234	JMP		TST2
4136	0000	REL,		/VARIABLE

/THIS IS THE ROUTINE THAT DISPLAYS  
 /FIVE POINTS, ONE ON EACH OF THE  
 /HORIZONTAL LINES  
 LP1,

4137	1000	LDA	
4140	0000	0	
4141	1060	STA I	
4142	0000	0	
4143	1020	LDA I	
4144	0370	370	
4145	2136	ADD	REL
4146	0142	DIS	2
4147	1000	LDA	
4150	0002	2	
4151	0017	COM	
4152	4002	STC	2
4153	1020	LDA I	
4154	0367	367	
4155	2136	ADD	REL
4156	0142	DIS	2
4157	1000	LDA	
4160	0002	2	
4161	0017	COM	
4162	4002	STC	2
4163	1020	LDA I	
4164	0570	570	
4165	2136	ADD	REL
4166	0142	DIS	2
4167	1000	LDA	
4170	0002	2	
4171	0017	COM	
4172	4002	STC	2
4173	1020	LDA I	
4174	0167	167	
4175	2136	ADD	REL
4176	0142	DIS	2
4177	1000	LDA	
4200	0002	2	
4201	0017	COM	
4202	4002	STC	2
4203	1020	LDA I	
4204	0767	767	
4205	2136	ADD	REL
4206	0142	DIS	2
4207	6142	JMP	LP1+3



```

4210 1000 TIGL, LDA /GLITCH GENERATOR
4211 0000 0
4212 1060 STA I
4213 0000 0
4214 1020 LDA I
4215 0020 20
4216 4136 STC
4217 6137 JMP
4220 0011 CLR
4221 1020 LDA I
4222 7774 7774
4223 1200 LAM
4224 0136 REL
4225 1460 SAE I
4226 7774 7774
4227 6217 JMP
4230 1020 LDA I
4231 0010 10
4232 4136 STC
4233 6213 JMP TIGL+3
4234 0011 CLR
4235 4136 STC
4236 6271 JMP LP2A
4237 6310 JMP LP2B
4240 1000 LDA
4241 0136 REL
4242 1660 BCO I
4243 0100 100
4244 1560 BCL I
4245 7600 7600
4246 0470 AZE I
4247 6334 JMP GL2
4250 1020 LDA I
4251 0004 4
4252 1140 ADM
4253 0136 REL
4254 1460 SAE I
4255 1000 1000
4256 6237 JMP TST2LP

```

THIS ROUTINE GENERATES 5 VERTICAL LINES  
 AT HORIZONTAL LOCATIONS 0,177,377,577,777,  
 GLITCHES ARE DISPLAYED AT VERTICAL LOCATIONS  
 177,377,500,700 ON THE LINES.

```

4234 0011 CLR
4235 4136 STC
4236 6271 JMP LP2A
4237 6310 JMP LP2B
4240 1000 LDA
4241 0136 REL
4242 1660 BCO I
4243 0100 100
4244 1560 BCL I
4245 7600 7600
4246 0470 AZE I
4247 6334 JMP GL2
4250 1020 LDA I
4251 0004 4
4252 1140 ADM
4253 0136 REL
4254 1460 SAE I
4255 1000 1000
4256 6237 JMP TST2LP

```

/DONE ALL POINTS YET

/DISPLAY UPPER LEFT AND LOWER RIGHT DOTS

4257	0011	CLR	Q4BETA
4260	4010	STC	K377
4261	3426	ADD	Q4BETA
4262	0150	DIS	Q4BETA
4263	0070	SET I	Q4BETA
4264	0777	777	
4265	1020	LDA I	
4266	0400	400	
4267	0150	DIS	Q4BETA
4270	6102	JMP	TST1A

/SET UP INDEX REGISTERS

4271	1000	LP2A,	LDA	
4272	0000	0		
4273	1060	STA I		
4274	0000	0000		
4275	0002	SET I	2	
4276	0000	0		
4277	0003	SET I	3	
4300	0177	177		
4301	0064	SET I	4	
4302	0377	377		
4303	0065	SET I	5	
4304	0577	577		
4305	0066	SET I	6	
4306	0777	777		
4307	6274	JMP	LP2A+3	

/ACTUALLY DISPLAY THE 5 POINTS

4310	1000	LP2B,	LDA	
4311	0000	0		
4312	1060	STA I		
4313	0000	0		
4314	1000	LDA		
4315	0136	REL		
4316	0142	DIS	2	
4317	0017	COM		
4320	0146	DIS	6	
4321	1000	LDA		
4322	0136	REL		
4323	1120	ADA I		
4324	0200	200		
4325	0143	DIS	3	
4326	0017	COM		
4327	0145	DIS	5	
4330	1000	LDA		
4331	0136	REL		
4332	0144	DIS	4	
4333	6313	JMP	LP2B+3	

/DISPLAY THE GLITCHES ON THE VERTICAL LINES

4334	1000	LDA	
4335	0000	0	
4336	1060	SYA I	
4337	2000	0	
4340	0075	SET I	15
4341	7772	=5	
4342	1020	LDA I	
4343	0767	767	GL2V
4344	4352	STC	7
4345	0067	SET I	7
4346	7772	-5	
4347	0070	SET I	10
4350	0001	1	
4351	1020	LDA I	
4352	0767	767	GL2V
4353	1170	ADM I	10
4354	0227	XSK I	7
4355	6351	JMP	1=4
4356	6310	JMP	LP2B
4357	1020	LDA I	
4360	0004	4	
4361	4352	STC	GL2V
4362	0235	XSK I	15
4363	6345	JMP	GL2V=5
4364	6271	JMP	LP2A
4365	6337	JMP	GL2+3

/RESET HORIZONTAL POSITION  
/GO BACK

4400 \*400

```

4400 1020 DORED, LDA I
4401 0014 14
4402 0004 ESF
4403 0011 CLR
4404 4015 STC
4405 0447 SKPRD
4406 0000 HLT
4407 0467 SKPGR
4408 0496 SKP
4409 0000 HLT
4410 0446 SKPVND
4411 0000 HLT
4412 0446 SKPVND
4413 0000 HLT
4414 2466 SKPVRD
4415 0456 SKP
4416 6423 JMP
4417 0235 XSK I
4420 6414 JMP
4421 0000 HLT
4422 6400 JMP DORED

```

/CHANGE TO RED

```

/SKIP ON RED FAILED
/SKIP ON RED GREEN
/SKIP ON GREEN
/SHOULD NOT SKIP
/SKIPPED IN ERROR
/SKIP ON COLOR NOT DONE
/SKIP ON COLOR NOT DONE FAILED
/SKIP ON COLOR DONE
/NOT YET
/YES
/DELAY
/TRY AGAIN
/SKIP ON COLOR DONE FAILED

```

```

4423 1020 DOGRN, LDA I
4424 0010 10
4425 0004 ESF
4426 0011 CLR
4427 4015 STC
4430 0407 SKPGR
4431 0000 HLT
4432 0447 SKPRD
4433 0456 SKP
4434 0000 HLT
4435 0446 SKPVND
4436 0000 HLT
4437 0466 SKPVRD
4440 0456 SKP
4441 0446 JMP
4442 0235 XSK I
4443 0437 JMP
4444 0000 HLT
4445 0423 JMP
4446 0400 JMP

```

/CHANGE TO GREEN

15

```

/SKIP ON GREEN
/SKIP ON GREEN FAILED
/SKIP ON RED
/SHOULD NOT SKIP
/SKIPPED IN ERROR
/SKIP ON COLOR NOT DONE
/SHOULD SKIP
/SKIP ON DONE
/NOT DONE YET
/YES
/DELAY
/TRY AGAIN
/SKIP ON COLOR DONE FAILED
/TRY AGAIN

```

15

15

15

DOGRN  
DORED

```

4447 0077 DSCPT, SET I 17
4450 0000
4451 6667 DSCPTA, JMP CLOCK
4452 6454 JMP DSCPAT
4453 7155 JMP DISPX

4454 0075 DSCPAT, SET I 15
4455 0740 Q1GRID=1
4456 1035 LDA I 15
4457 4013 STC Q1BETA
4460 1035 LDA I 15
4461 4012 STC Q2BETA
4462 1035 LDA I 15
4463 4011 STC Q3BETA
4464 1035 LDA I 15
4465 4010 STC Q4BETA

/HAFFLG=0 WHEN DISPLAYING LEFT HALF OF PATTERN
/HAFFLG=4 WHEN DISPLAYING RIGHT HALF
STC HAFFLG

4466 4733
4467 0067 SET I 7
4470 0734 RCHNG=1
4471 0074 SET I 14
4472 7773 "4

/IN RIGHT HALF PASS NOP BELOW WILL BE REPLACED BY ADA I 7
/LEFT AND RIGHT HALF SEQUENCES ARE STAGGERED BY A CONSTANT
/20 FOR FULL SIZE CHARACTERS, 10 FOR HALF SIZE
RHI, LDA I 15
NOP
STA I 15
LDA I 15
STA I 15
XSK I 14
JMP RH1-1
STC LNFLG

4473 1035
4474 0016
4475 1075
4476 1035
4477 1075
4500 0234
4501 6473
4502 4734

4503 0075
4504 7771
4505 0004
4506 2752
4507 4001
4510 2754
4511 1772
4512 1020

/PUT GRID PATTERN ADDR FOR
/EACH QUAD IN 4 RETAS

/INITIALIZE ARGUMENTS
/THERE ARE
/4 QUADRANTS
/IN RIGHT HALF PASS NOP BELOW WILL BE REPLACED BY ADA I 7
/LEFT AND RIGHT HALF SEQUENCES ARE STAGGERED BY A CONSTANT
/20 FOR FULL SIZE CHARACTERS, 10 FOR HALF SIZE
/PTN FOR HORIZ COORD
RHI, LDA I 15
NOP
STA I 15
LDA I 15
STA I 15
XSK I 14
JMP RH1-1
STC LNFLG

/HORIZ ARGUMENT
/PTN FOR VERT COORD
/VERT ARGUMENT
/DOONE ALL QUADRANTSJ
/NO
/20 WHEN DOING LN 2 IN
/1, 0 WHEN DOING LN 1
/THERE ARE 6 CHAR ON LN 1

/ENABLE HALF SIZE CHARS
/SELECT CHAN 0 AND
/SET HORIZ COORD
/VERT COORD TO AC
/DSC IN QUAD 2
/BUMP HORIZ COORD TO

```

```

4513 2010 BH02, 10 ADD
4514 2001 ADD
4515 4752 STC
4516 2762 STC
4517 1620 BSE I
4520 4000
4521 4001 STC I
4522 2764 ADD
4523 1770 DSC I
4524 1020 LDA I
4525 0010 BH04, 10 ADD
4526 2001 ADD
4527 4762 STC
4530 0235 XSK I
4531 6506 JMP
4532 2734 ADD
4533 0470 AZE I
4534 6560 JMP
4535 0075 SET I
4536 7766 P11
4537 0011 CLR
4540 4734 STC
4541 2751 ADD
4542 2733 ADD
4543 4752 STC
4544 2733 ADD
4545 1120 ADA I
4546 7737 BV02, P40
4547 4754 STC
4550 2761 ADD
4551 2733 ADD
4552 4762 STC
4553 2763 ADD
4554 1120 ADA I
4555 7737 BV04, P40
4556 4764 STC
4557 6506 JMP
4560 0075 FULSIZ, SET I
4561 7771 P6

```

/SET HORIZ COORD

/DSC IN QUAD 4  
/BUMP HORIZ COORD

/DONE A LN?  
/NO

/DONE 2 LNS?  
/YES GO TO FULL SIZE CHARS  
/THERE ARE 11  
/CHARS IN LN 2  
/SET LNFLG  
/TO EXIT ON NEXT CHK  
/RESET HORIZ  
/AND VERT  
/COORD  
/FOR LN 2

/DOO LN 2  
/SET CTR  
/FOR LN 1

```

4562 0076
4563 7737
4564 0236
4565 6564

4566 1020
4567 0210
4570 0004
4571 4734
4572 2746
4573 1620
4574 4000
4575 4001
4576 2750
4577 1773
4600 1020
4601 0020
4602 2001
4603 4746
4604 2756
4605 4001
4606 2740
4607 1771
4610 1020
4611 0020
4612 2001
4613 4756
4614 0235
4615 6572
4616 2734
4617 0470
4620 6646
4621 0075
4622 7766
4623 0011
4624 4734
4625 2745
4626 2733
4627 2733
4630 4746
4631 2747
4632 1120
4633 7737

/DELAY, SIZE CHANGE NEXT
SET I 16
XSK I 16
JMP :#1

LDA I
210
ESF
STC LNFLG
Q1HOR
LOOP2,
ADD
BSE I
4000
STC I
ADD
DSC I
LDA I
20
ADD 1
STC
ADD
STC 1
ADD
DSC I
LDA I
20
ADD
STC
XSK I
JMP LOOP2
AZE I
JMP HAFCHK
SET I 15
-11
CLR
STC
ADD
ADD
ADD
STC
ADD
ADA I
-40

/ENABLE
/FULL SIZE
/CHAR
/SET FLAG FOR LN 1
/HORIZ COORD

/QUAD 1
/BUMP HORIZ

/HORIZ COORD
/CHAN 0
/QUAD 3

/DONE A LN?
/NO

/DONE 2 LNS?
/YES CHK FOR 2ND HALF OF PATTERN
/NO SET FOR LN 2

/SET LNFLG FOR
/EXIT TO HAFCHK
/RESET COORDINATES

LNFLG
Q1HOR
HAFFLG
HAFFLG
Q1HOR
KQ1VER

LNFLG
KQ1HOR
HAFFLG
HAFFLG
Q1HOR
KQ1VER

```



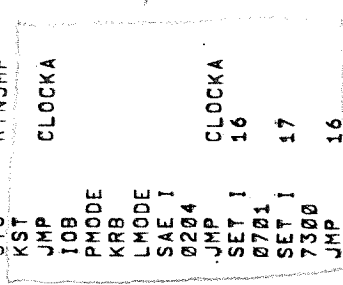
4634	4750	STC	Q1VER		
4635	2755	ADD	KQ3HOR		
4636	2733	ADD	HAFFLG		
4637	2733	ADD	HAFFLG		
4640	4756	STC	Q3HOR		
4641	2757	ADD	KQ3VER		
4642	1120	ADA I			
4643	7737	-40			
4644	4760	STC	Q3VER		
4645	6572	JMP	LOOP2		
4646	1000	JMP			
4647	0733	LDA	HAFCHK, LDA		
4650	0450	HAFFLG			
4651	6603	AZE			
4652	1020	JMP	DSCEND		
4653	0804	LDA I			
4654	4733	4			
4655	1020	STC	HAFFLG		
4656	1127	LDA I			
4657	4474	ADA I			
4660	0075	STC	RH1		
4661	0744	SET I	15		
4662	6467	KQ1HOR+1			
		JMP	RH1=5		
4663	1020	DSCEND, LDA I			
4664	0016	NOP			
4665	4474	STC	RH1		
4666	6451	JMP	DSCPTA		

/DO LN 2  
 /DONE BOTH  
 /LEFT AND RIGHT  
 /SEQUENCES?  
 /YES EXIT  
 /NO SET FOR  
 /DSC RIGHT SEQ.  
 /SET HAFFLG FOR EXIT  
 /ENABLE INST TO ADD A  
 /CONSTANT FOR  
 /RIGHT HALF SEQ.  
  
 /DO RIGHT HALF SEQ.  
 /RESTORE NOP  
 /FOR NEXT LEFT HALF SEQ.

```

4667 1000      CLOCK, LDA
4670 0000      STC
4671 4732      RTNJMP
4672 0415      KST
4673 6706      JMP
4674 0500      IOB
4675 6036      PMODE
1460 1460      KRB
1467 0204      LMODE
1700 6706      SAE I
1701 0076      0204
1702 0701      JMP
1703 0077      SET I
1704 7300      0701
1705 6016      SET I
1706 0460      7300
1707 6105      JMP
1710 0461      CLOCK, SNS I
1711 6454      JMP
1712 0402      SNS I
1713 7162      JMP
1714 0403      SNS I
1715 7221      JMP
1716 0444      SNS
1717 6723      JMP
1720 0077      SET I
1721 1770      1770
1722 7474      JMP
1723 0237      CLOCKB, XSK I
1724 6732      JMP
1725 1000      LDA
1726 0732      RTNJMP
1727 1120      ADA I
1730 0001      1
1731 4732      STC
1732 2000      RTNJMP, 0
1733 0000      HAFFLG, 0
1734 0000      LNFLAG, 0

```



TO

/TICK CLOCK AND /REFRESH SCOPE

```

4735 0010 RCHNG, 10
4736 0004 4
4737 0010 10
4740 0004 4
4741 0764 Q1GRID, Q4VER
4742 1022 Q2GRID, Q4VER+36
4743 1060 Q3GRID, Q4VER+74
4744 1116 Q4GRID, Q4VER+132
4745 0450 KQ1HOR, 450
4746 0000 Q1HOR, 0
4747 0340 KQ1VER, 340
4750 0000 Q1VER, 0
4751 0010 KQ2HOR, 10
4752 0020 Q2HOR, 0
4753 0340 KQ2VER, 340
4754 0000 Q2VER, 0
4755 0010 KQ3HOR, 10
4756 0000 Q3HOR, 0
4757 7477 KQ3VER, =300
4760 0000 Q3VER, 0
4761 0600 KQ4HOR, 600
4762 0000 Q4HOR, 0
4763 7477 KQ4VER, =300
4764 0000 Q4VER, 0

```

/ADDR =1 OF GRID PATTERNS

/GRID PATTERNS  
/QUAD 1 LEFT HALF

```

4765 4136 /C
4766 1077 /H
4767 4677 /A
4770 3077 /N
4771 0000 /SPACE
4772 4523 /2
4773 4477 /F
4774 0177 /U
4775 0177 /L
4776 0177 /L
4777 0000 /SPACE
5000 5121 /S
5001 7741 /I
5002 4543 /Z
5003 4577 /E

5004 2241 /C
5005 7710 /H
5006 7744 /A
5007 7706 /N
5010 0000 /SPACE
5011 2151 /2
5012 4044 /F
5013 7701 /U
5014 2301 /L
5015 0301 /L
5016 0000 /SPACE
5017 4651 /S
5020 0041 /I

```

/RIGHT HALF

5021	6151	6151	/Z
5022	4145	4145	/E
5023	4136	4136	/C
5024	1077	1077	/H
5025	4477	4477	/A
5026	3077	3077	/N
5027	0000	0	/SPACE
5030	2101	2101	/I
5031	1077	1077	/H
5032	4477	4477	/A
5033	0177	0177	/L
5034	4477	4477	/F
5035	0000	0	/SPACE
5036	5121	5121	/S
5037	7741	7741	/I
5040	4543	4543	/Z
5041	4577	4577	/E
/RIGHT HALF			
5042	2241	2241	/C
5043	7710	7710	/H
5044	7744	7744	/A
5045	7706	7706	/N
5046	0000	0	/SPACE
5047	0177	0177	/I
5050	7710	7710	/H
5051	7744	7744	/A
5052	0301	0301	/L
5053	4044	4044	/F
5054	0000	0	/SPACE
5055	4651	4651	/S
5056	0041	0041	/I
5057	6151	6151	/Z
5060	4145	4145	/E
/QUAD 3 LEFT HALF			
5061	4136	4136	/C
5062	1077	1077	/H
5063	4477	4477	/A
5064	3077	3077	/N
5065	0000	0	/SPACE
5066	2101	2101	/I
5067	4477	4477	/F
5070	0177	0177	/U
5071	0177	0177	/L
5072	0177	0177	/L
5073	0000	0	/SPACE
5074	5121	5121	/S
5075	7741	7741	/I
5076	4543	4543	/Z
5077	4577	4577	/E

5100	2241	/RIGHT HALF	/C
5101	7710		/H
5102	7744		/A
5103	7706		/N
5104	0000		/SPACE
5105	0177		/1
5106	4044		
5107	7701		
5110	0301		
5111	0301		
5112	0000		
5113	4651		
5114	0041		
5115	6151		
5116	4145		

/QUAD 4 LEFT HALF

5117	4136		
5120	1077		
5121	4477		
5122	3077		
5123	0000		
5124	4523		
5125	1077		
5126	4477		
5127	0177		
5130	4477		
5131	0000		
5132	5121		
5133	7741		
5134	4543		
5135	4577	Q4EL,	4577

/RIGHT HALF

5136	2241		
5137	7710		
5140	7744		
5141	7706		
5142	0000		
5143	2151		
5144	7710		
5145	7744		
5146	0301		
5147	4044		
5150	0000		
5151	4651		
5152	0041		
5153	6151		
5154	4145	Q4ER,	4145

/A
/L
/F
/SPACE
/S
/1
/Z

THIS ROUTINE DISPLAYS X PATTERN

5155	0077	DISPX,	SET I	17
5156	1300	GOA,	JMP	CLOCK
5157	6667		JMP	GO
5160	7162		JMP	DISCL
5161	7212		LDA I	
5162	1020	GO,	210	
5163	0210		ESF	
5164	0004		SET I	13
5165	0073		377	
5166	0377		SET I	14
5167	0074		7377	
5170	7400		SET I	15
5171	0075		7001	
5172	7001		SET I	1
5173	0061		0	
5174	0000		XPATRN,	LDA I
5175	1020		71	
5176	7776		ADM	
5177	1140		13	
5200	0013		DIS I	1
5201	0161		LDA I	
5202	1020		1	
5203	0001		ADM	
5204	1140		14	
5205	0014		DIS	1
5206	0141		XSK I	13
5207	0235		JMP	XPATRN
5210	7175		JMP	GOA
5211	7157			

/COLOR ROUTINE (VR20)  
 / EXECUTE THIS TEST ONLY IF SNS 5=1

0446 SKPVND=0446 /SKIP ON COLOR NOT DONE  
 0466 SKPVRD=0466 /SKIP ON COLOR DONE  
 0447 SKPRD=0447 /SKIP ON RED  
 0467 SKPGR=0467 /SKIP ON GREEN

5212 0445 DISCL, SNS 5  
 5213 7467 JMP PSREG  
 5214 0077 SET I 17  
 5215 0000 0  
 5216 6667 JMP CLOCK  
 5217 7221 JMP DRW  
 5220 7467 JMP PSREG

/DRAW OUTER BOX

5221 0011 DRW, CLR  
 5222 4010 STC Q4BETA  
 5223 3427 ADD K400  
 5224 0150 DIS Q4BETA  
 5225 0011 CLR  
 5226 3425 ADD K10  
 5227 2010 ADD Q4BETA  
 5230 1460 SAE I  
 5231 1000 1000  
 5232 7222 JMP DRW+1

5233 0011 DRWA, CLR  
 5234 4010 STC Q4BETA  
 5235 3426 ADD K377  
 5236 0150 DIS Q4BETA  
 5237 0011 CLR  
 5240 3425 ADD K10  
 5241 2010 ADD Q4BETA  
 5242 1460 SAE I  
 5243 1000 1000  
 5244 7234 JMP DRWA+1

5245 0070 SET I Q4BETA  
 5246 0777 777  
 5247 3426 ADD K377  
 5250 0150 DIS Q4BETA

5251	0011	DRWB,	CLR		
5252	4010		STC	Q4BETA	
5253	3427		ADD	K400	
5254	0146		DIS	6	
5255	3425		ADD	K10	
5256	1460		SAE I		
5257	1400		1400		
5260	7254		JMP	14	
5261	0066		SET I	6	
5262	0777		777		
5263	0011		CLR		
5264	3427		ADD	K400	
5265	0150		DIS	Q4BETA	
5266	3425		ADD	K10	
5267	1460		SAE I		
5270	1400		1400		
5271	7265		JMP	14	
5272	0093		SET I	13	
5273	0200		200		
5274	0094		SET I	14	
5275	0600		600		
5276	0095		SET I	15	
5277	7700		7700		
5300	0061		SET I	1	
5301	0220		220		
5302	1020	DRWC,	LDA I		
5303	7773		74		
5304	1140		ADM		
5305	0013		13		
5306	0161		DIS I	1	
5307	1020		LDA I		
5310	0004		4		
5311	1140		ADM		
5312	0014		14		
5313	0141		DIS	1	
5314	1020		LDA I		
5315	0002		2		
5316	1140		ADM		
5317	0001		1		
5320	0235		XSK I	15	
5321	7302		JMP	DRWC	



```

5322 0011 CLR
5323 3430 ADD SWITCH
5324 0450 AZE GREEN
5325 7367 JMP GREEN
5326 1020 LDA I RED,
5327 7070 STC SWITCH
5330 5430 LDA I
5331 1020 LDA I
5332 0014 14
5333 0004 ESF
5334 0011 CLR
5335 4015 STC 15
5336 0447 SKPRD
5337 0000 HLT
5340 0467 SKPGR
5341 0456 SKP
5342 0000 HLT
5343 0446 SKPVND
5344 0000 HLT
5345 0466 SKPVRD
5346 0456 SKP
5347 7354 JMP :+5
5350 0235 XSK I 15
5351 7345 JMP :+4
5352 0000 HLT
5353 7326 JMP RED
5354 0011 CLR
5355 0061 SET I 1
5356 0200 200
5357 0070 SET I 04BETA
5360 1430 TXI1-1
5361 0071 SET I 03BETA
5362 7765 -12
5363 1770 DSC I
5364 0231 XSK I
5365 7363 JMP :+2
5366 7216 JMP DISCLA

/SKIP ON RED
/SKIP ON RED FAILED
/SKIP ON GREEN
/SKIP ON GREEN IN ERROR
/SKIP ON COLOR NOT DONE
/SKIP ON COLOR NOT DONE FAILED
/NOT YET
/YES
/DELAY
/SKIP ON COLOR DONE FAILED
/TRY AGAIN

```

5367	0011	GREEN,	CLR			
5370	5430		STC	SWITCH		
5371	1020		LDA I			
5372	0010		10			
5373	0004		ESF			
5374	0011		CLR			
5375	4015		STC	15		
5376	0467		SKPCR			/SKIP ON GREEN
5377	0000		HLT			/SKIP ON GREEN FAILED
5400	0447		SKPRD			/SKIP ON RED
5401	0456		SKP			/SHOULD NOT SKIP
5402	0000		HLT			/SKIP ON RED SKIPPED IN ERROR
5403	0466		SKPYRD			/SKIP ON COLOR DONE
5404	0456		SKP			/NOT YET
5405	7412		JMP	+5		/YES
5406	0235		XSK I	15		/DELAY
5407	7403		JMP	+4		/TRY AGAIN
5410	0000		HLT	GREEN		/SKIP ON COLOR DONE FAILED
5411	7367		JMP			/TRY AGAIN
5412	0011		CLR			
5413	0061		SET I	1		
5414	0470		470			
5415	0070		SET I	Q4BETA		
5416	1444		TXT2=1			
5417	0071		SET I	Q3BETA		
5420	7755		-22			
5421	1770		DSC I	Q4BETA		
5422	0231		XSK I	Q3BETA		
5423	7421		JMP	+2		
5424	7216		JMP	DISCLA		
5425	0010		K10,	10		
5426	0377		K377,	377		
5427	0400		K400,	400		
5430	0000		SWITCH,	0		

5431	4477	TXT1,	4477
5432	3146		3146
5433	0000		0
5434	0000		0
5435	4577		4577
5436	4145		4145
5437	0000		0
5440	0000		0
5441	4177		4177
5442	3641		3641
5443	0000		0
5444	0000		0
5445	4136	TXT2,	4136
5446	2645		2645
5447	0000		0
5450	0000		0
5451	4477		4477
5452	3146		3146
5453	0000		0
5454	0000		0
5455	4577		4577
5456	4145		4145
5457	0000		0
5460	0000		0
5461	4577		4577
5462	4145		4145
5463	0000		0
5464	0000		0
5465	3077		3077
5466	7706		7706

POWER SUPPLY REGULATION TEST

```

5467 0077 PSREG, SET I 17
5470 1770 PSRECA, JMP CLOCK
5471 6667 PSREGB, JMP PSREGB
5472 7474 JMP DISPAT
5473 6100

5474 0076 PSREGB, SET I 16
5475 7000 LDA I
5476 1020 LDA I
5477 0210 210
5500 0004 ESF
5501 0061 PSREGC, SET I 1
5502 0000
5503 1020 LDA I
5504 0377 LDA I
5505 7546 JMP OPTIT
5506 0061 SET I 1
5507 0000
5510 1020 LDA I
5511 7400 "377
5512 7546 JMP OPTIT
5513 0061 SET I 1
5514 0440 440
5515 1020 LDA I
5516 0377 377
5517 7546 JMP OPTIT
5520 0061 SET I 1
5521 0440 440
5522 1020 LDA I
5523 7400 "377
5524 7546 JMP OPTIT
5525 0236 XSK I
5526 7501 JMP PSREGC
5527 0076 PSREGD, SET I 16
5530 0000
5531 0061 PSREGE, SET I 1
5532 0200 200
5533 0070 SET I 04BETA
5534 1611 TXT4--1
5535 0071 SET I 03BETA
5536 7745 -32
5537 0011 CLR
5540 1770 DSC I
5541 0231 XSK I
5542 7540 JMP I-2
5543 0236 XSK I
5544 7531 JMP PSREGE
5545 7471 JMP PSRECA

```

5546 0046 OPTIT, SET 6  
 5547 0000  
 5550 0070 SET I Q4BETA  
 5551 1557 TX13-1  
 5552 0071 SET I Q3BETA  
 5553 7745 -32  
 5554 1770 DSC I Q4BETA  
 5555 0231 XSK I Q3BETA  
 5556 7554 JMP :=2  
 5557 6006 JMP 6

/MAINDEC

5560 3077 TX13, 3077  
 5561 7730 7730  
 5562 0000 0  
 5563 0000 0  
 5564 4477 4477  
 5565 7744 7744  
 5566 0000 0  
 5567 0000 0  
 5570 7741 7741  
 5571 0041 0041  
 5572 0000 0  
 5573 0000 0  
 5574 3077 3077  
 5575 7706 7706  
 5576 0000 0  
 5577 0000 0  
 5600 4177 4177  
 5601 3641 3641  
 5602 0000 0  
 5603 0000 0  
 5604 4577 4577  
 5605 4145 4145  
 5606 0000 0  
 5607 0000 0  
 5610 4136 4136  
 5611 2241 2241

/12-D6BC

5612	2101	TXT4,	2101
5613	0177		0177
5614	0000		0
5615	0000		0
5616	4523		4523
5617	2151		2151
5620	0000		0
5621	0000		0
5622	0404		0404
5623	0404		0404
5624	0000		0
5625	0000		0
5626	4177		4177
5627	3641		3641
5630	0000		0
5631	0000		0
5632	1506		1506
5633	4225		4225
5634	0000		0
5635	0000		0
5636	5177		5177
5637	2651		2651
5640	0000		0
5641	0000		0
5642	4136		4136
5643	2241		2241

/END

e

0000  
0100  
0200  
0300  
0400  
0500  
0600  
0700  
  
1000  
1100  
1200  
1300  
1400  
1500  
1600  
1700  
  
2000  
2100  
2200  
2300  
2400  
2500  
2600  
2700  
  
3000  
3100  
3200  
3300  
3400  
3500  
3600  
3700





BH01	4601	OPTIT	5546
BH02	4513	PSREG	5467
BH03	4611	PSREGA	5471
BH04	4525	PSREGB	5474
BV01	4633	PSREGC	5501
BV02	4546	PSREGD	5527
BV03	4643	PSREG E	5531
BV04	4555	Q1BETA	4013
CLOCK	4667	Q1GRID	4741
CLOCKA	4706	Q1HOR	4746
CLOCKB	4723	Q1VER	4750
DISCL	5212	Q2BETA	4012
DISCLA	5216	Q2GRID	4742
DISPAT	4100	Q2HOR	4752
DISPX	5155	Q2VER	4754
DOGRN	4423	Q3BETA	4011
DOR ED	4400	Q3GRID	4743
DRW	5221	Q3HOR	4756
DRWA	5233	Q3VER	4760
DRWB	5251	Q4BETA	4010
DRWC	5302	Q4EL	5135
DSCEND	4663	Q4ER	5154
DSCPAT	4454	Q4GRID	4744
DSCPT	4447	Q4HOR	4762
DSCPTA	4451	Q4VER	4764
EXNB	4066	REL	5326
FULSIZ	4560	REL	4136
GL2	4334	RH1	4474
GL2V	4352	RHCHNG	4735
GO	5162	RTNJMP	4732
GOA	5157	SKP	0456
GREEN	5367	SKPGR	0467
HAFCHK	4646	SKPRD	0447
HAFFLG	4733	SKPVND	0446
INCMB	4026	SKPVRO	0466
K10	5425	SWITCH	5430
K377	5426	T1GL	4210
K400	5427	TST1A	4102
K01HOR	4745	TST1B	4105
K01VER	4747	TST1LP	4113
K02HOR	4751	TST2	4234
K02VER	4753	TST2LP	4237
K03HOR	4755	TXT1	5431
K03VER	4757	TXT2	5445
K04HOR	4761	TXT3	5560
K04VER	4763	TXT4	5612
LNFLG	4734	XPATRN	5175
LOOP1	4506		
LOOP2	4572		
LP1	4137		
LP2A	4271		
LP2B	4310		

/VR14, VR20 DISPLAY CONTROL AND SCOPE TEST DIAL10 V003 16-AUG-71 13130 PAGE 25-4

ERRORS DETECTED: 0

LINKS GENERATED: 0

RUN-TIME: 7 SECONDS

3K CORE USED