

DECUS NO.

8-286

TITLE

TWO PATCHES FOR DISASSEMBLER WITH SYMBOLS

AUTHOR

Gary Coleman

COMPANY

The Taft School Watertown, Connecticut

DATE

February 25, 1970

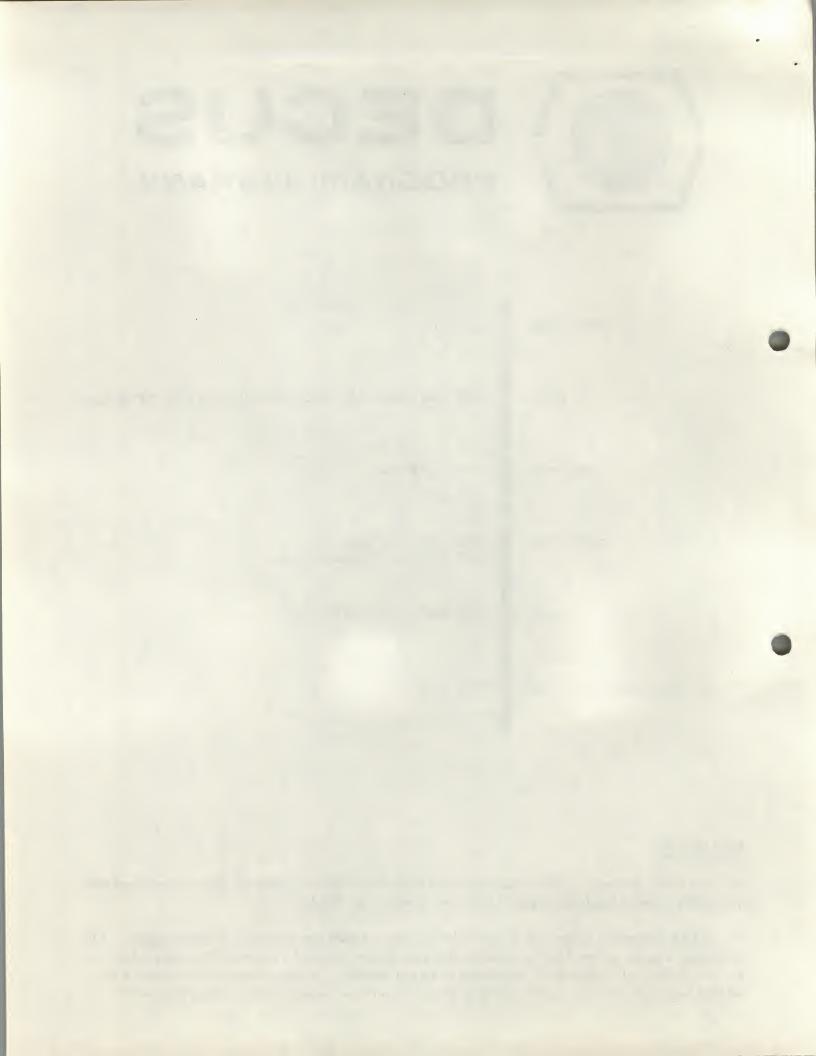
SOURCELANGUAGE

PAL III

ATTENTION

This is a USER program. Other than requiring that it conform to submittal and review standards, no quality control has been imposed upon this program by DECUS.

The DECUS Program Library is a clearing house only; it does not generate or test programs. No warranty, express or implied, is made by the contributor, Digital Equipment Computer Users Society or Digital Equipment Corporation as to the accuracy or functioning of the program or related material, and no responsibility is assumed by these parties in connection therewith.



TWO PATCHES FOR DISASSEMBLER WITH SYMBOLS

DECUS Program Library Write-up

DECUS No. 8-286

ABSTRACT

This patch for DECUS No. 5/8-18C (Disassembler With Symbols) allows the user to get a cross reference table of addresses which have been defined on the symbol table. It does not enter items on the cross reference table that are not on the symbol table, thus cutting down on the garbage that can be produced by entering all addresses on the table. It provides the user with the capability to trace a single address (or several addresses) through a program without having to sift through many pages of other addresses. All the user gets is a cross reference table of the symbols that he entered; if no symbols are entered, no cross reference table will be produced. Also included is a patch that allows the disassembler to run on a PDP-8/S by replacing the illegal operate instructions with a legal instruction. This allows a PDP-8/S to accept 6 lettered symbols as opposed to 2 lettered symbols without this patch.

REQUIREMENTS

Same as disassembler, except that a symbol table must be entered.

USAGE

Patch is in binary loader format.

LOAD

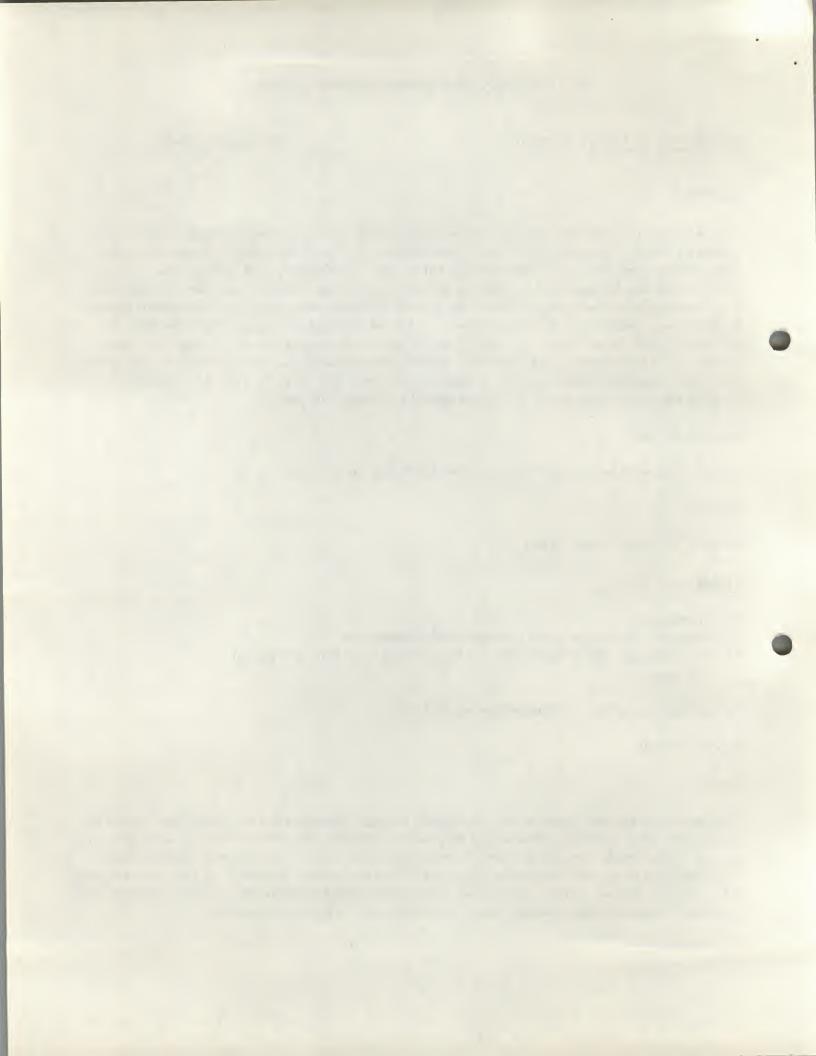
- A) Disassembler
- B) Cross reference table patch provided with disassembler
- C) Any other patches (DECUS No. 8-179 EAE patch, or PDP-8/S patch)
- D) This patch

Follow the instructions in disassembler manual.

RESTRICTIONS

None.

This patch can be used to trace certain symbols through a binary program. Without this patch a complete cross reference table would be produced showing all references of all locations. If a large symbol table were used, the cross reference table would overflow and dump several times using up much time and paper. This patch will not enter a reference on the cross reference table unless a symbol is being referenced. This means that the frequency of cross reference table overflow is considerably lowered, making the tracing of a symbol much easier.



EXAMPLE

With the symbol table below:

P4ØØ Ø167 P511 Ø511 OUT Ø226 M377 ØØ34

And the following listing:

*200 START TAD P400 DCA 15 TAD I 15 TAD M377 SNA CLA JMP .-3 TAD P400 DCA P511 JMP OUT

Will produce this:

ØØ34	M377,	Ø2Ø3	
Ø167	P4ØØ,	Ø2ØØ	Ø2Ø6
Ø226	OUT,	Ø21Ø	
Ø511	P511,	Ø2Ø7	

In the case above, the address 15 was not output in the cross reference table because it had not been defined as a symbol.

METHODS

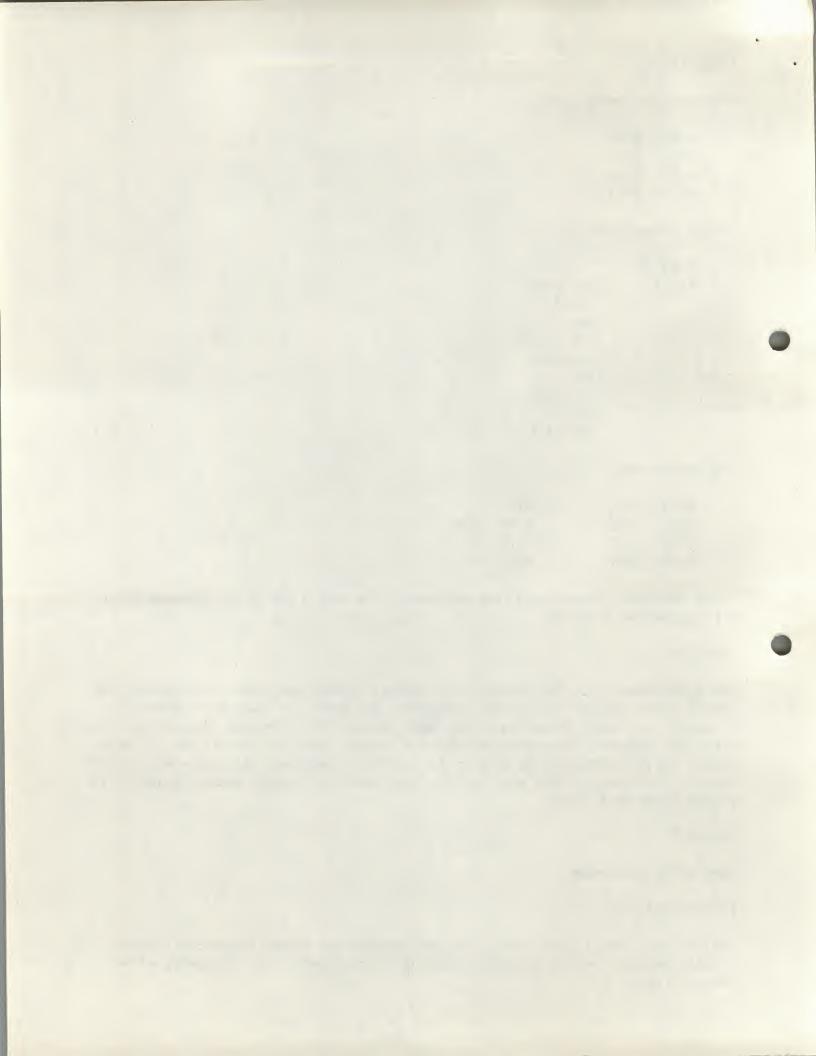
During the reading time the computer reads one word (2 paper tape frames) and assembles the address (if the instruction is a memory reference). This patch then takes over. Where the disassembler would put the address on the cross reference table right away, the patch searches the symbol table and if the address referenced is a symbol, then the entry is made. If not a symbol, the patch returns to the program leaving the cross reference table unaltered. At printout time, which occurs either when the table overflows or the program reaches the trailer, the program prints out as usual.

FORMAT

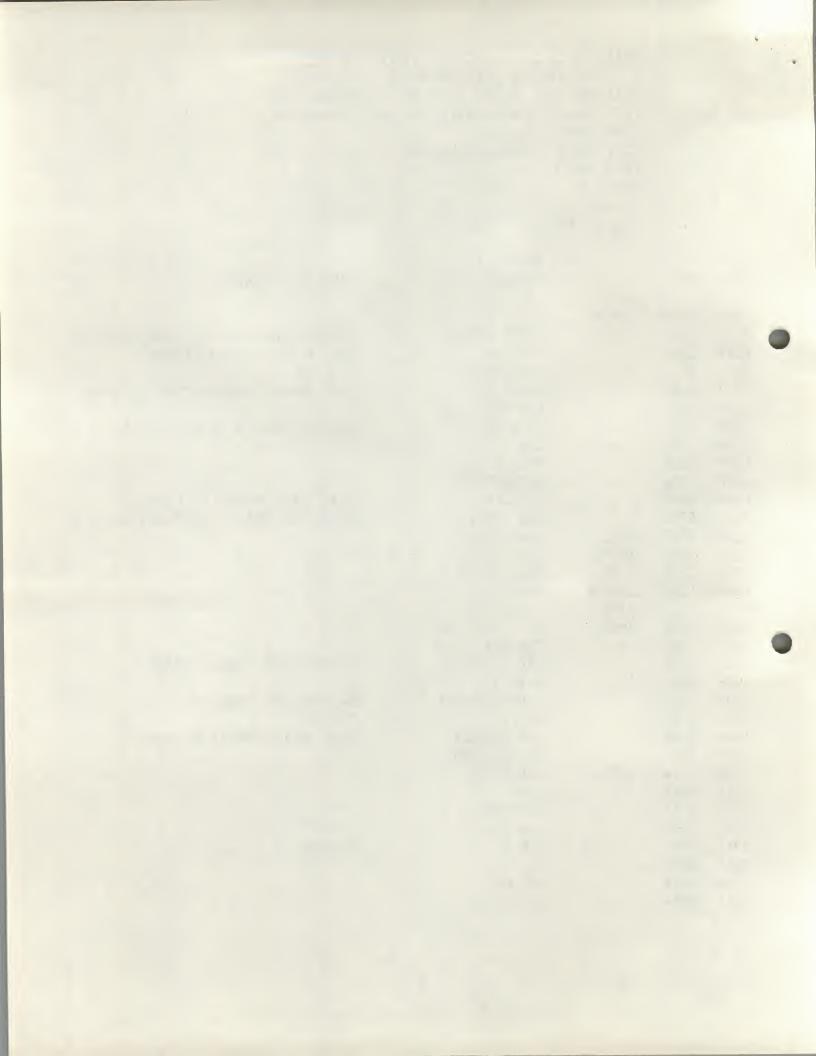
Same as the disassembler.

EXECUTION TIME

The program is slowed down a little in the reading phase but is faster at print-out because it has less material and the cross reference table will not overflow as often causing a time consuming dump.



```
/PATCH FOR DECUS PROGRAM NO. 5/8-18C
            / DISASSEMBLER WITH SYMBOLS
            ALLOWS USER TO GET A CROSS REFERENCE TABLE
            OF ONLY THE ADDRESSES THAT ARE DEFINED BY
            THE SYMBOL TABLE.
            DEFINITIONS FOR ASSEMBLER
            RETURN=161
            ADD=171
            PC=143
            COUNT=41
            *5Ø7
Ø5Ø7
      471Ø
                        JMS 1 .+1
Ø51Ø
      Ø267
                        PUTON
                                             /LINK TO CODING
            *267
Ø267
      ØØØØ
            PUTON,
Ø27Ø
     47Ø6
                        JMS I LSMSR2
                                             SEARCH SYMBOL TABLE FOR ADDRESS
Ø271
      53Ø3
                        JMP OUT
                                             /NOT FOUND; NO REFERENCE
Ø272
      1171
                       TAD ADD
                                             FOUND!
Ø273
      3416
                       DCA 1 16
                                             PUT ADDRESS REFERENCED ON TABLE
Ø274
      1143
                       TAD PC
Ø275
      3416
                       DCA 1 16
                                             /PUT PROGRAM COUNTER ON TABLE
Ø276
      71ØØ
                       CLL
Ø277
      1016
                       TAD 16
Ø3ØØ
     13Ø4
                       TAD M7576
Ø3Ø1
      763Ø
                       SZL CLA
                                             REACHED TOP OF TABLE SPACE?
Ø3Ø2
                       JMP I OVER
     57Ø5
                                             CROSS REF TABLE OVERFLOW MESSAGE
Ø3Ø3
      5161
            OUT,
                       JMP RETURN
Ø3Ø4
     Ø2Ø2
            M7576,
                       -7576
Ø3Ø5
     1331
            OVER,
                       1331
Ø3Ø6
      12ØØ
            LSMSR2,
                       SMSR2
            *1200
      ØØØØ
                       Ø
12ØØ
            SMSR2,
1201
      73ØØ
                       CLA CLL
1202
     163Ø
                       TAD I LSYMTB
                                             ADDRESS OF SYMBOL TABLE
1203
      3014
                       DCA 14
1204
      1631
                       TAD I LSMCNT
                                             NUMBER OF SYMBOLS
1205
      745Ø
                                             /ZERO?
                       SNA
1206
      56ØØ
                       JMP I SMSR2
                                             YES; NO SYMBOLS IN TABLE
1207
      3Ø41
                       DCA COUNT
121Ø
      1414
            LOP2,
                       TAD 114
1211
      7Ø41
                       CIA
1212
      1171
                       TAD ADD
1213
      765Ø
                       SNA CLA
1214
      5226
                       JMP B
                                             FOUND!
1215
      2014
                       ISZ 14
1216
      2014
                       ISZ 14
1217
      2014
                       ISZ 14
```



		1, 2, 1		
122Ø 1221 1222 1223 1224 1225 1226 1227 123Ø 1231	2041 2041 2041 2041 521,0 56,00 22,00 56,00 0575 0576	B, LSYMTB, LSMCNT,	ISZ COUNT ISZ COUNT ISZ COUNT ISZ COUNT JMP LOP2 JMP I SMSR2 ISZ SMSR2 JMP I SMSR2 575 576	/NOT FOUND YET. /NOT FOUND IN TABLE AT ALL
			ALLOW DSAS TO W	ORK ON PDP-8/S
ØØØI	7775	M3, *552	-3	
Ø552	1,00	*14Ø6	TAD M3	
1406	1øø1		TAD M3	
ADD		Ø171		

Ø171 1226 В CHECK2 1220 COUNT ØØ41 LOP2 1210 1231 LSMCNT LSMSR2 Ø3Ø6 LSYMTB . 123Ø **M3** ØØØI Ø3Ø4 M7576 Ø3Ø3 . OUT OVER Ø3Ø5 PC Ø143 Ø267 Ø161 12ØØ PUTON RETURN SMSR2

