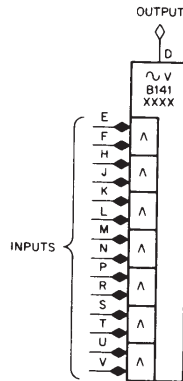


## B141 DIODE GATE

Standard Size FLIP CHIP Module, 18 Pins



The B141 is a 2 mA diode gate used for performing AND/OR functions and register comparisons. It is typically used for register loading and driving I/O bus data lines. The B141 will operate from standard DEC 35 ns or 40 ns negative pulses, provided that adequate fall times are maintained. It will also operate with standard DEC levels of ground and -3 V. (Pulses 70 ns or wider are considered levels.) Repetition rate may be limited to less than 10 MHz by slow fall times.

**INPUTS:** Input load is 2 mA per input pair shared by the grounded inputs. When any pair is not being used, at least one of the two inputs must be grounded.

**OUTPUT:** Standard levels of -3 V and ground. The output can drive 26 mA at ground (30 mA if slower rise and fall is adequate). Rise TTT is 15 ns or less; fall TTT is 50 ns or less. Fall delay is 20 ns or less. No clamped load is provided.

### POWER:

Pin	Voltage	Margin Range	Current
A	+10 V	0 V to 20 V	1.3 mA
B	-15 V	-10 V to -20 V	19 mA
C	ground		