



# FLIP CHIP MODULES TEST SPECS

TYPE: B 171

DIODE GATE

TEST	CONDITIONS	MINIMUM	MAXIMUM
<u>DC TESTS</u> * EXTRA 3K LOAD TO -15V.  MAKE ALL TESTS WITH -2.5V OR -0.5V IN AS APPROPRIATE.	$V_{CE}$ ( SATURATION )		175 MV
	LOWER LEVEL	3.2 V	3.9 V
	LOAD CURRENT	9 MA	11 MA
DIODE LEAKAGE	CONNECT SELECTED INPUT TO -4V WITH METER. GND OTHER INPUTS.		1.0 $\mu$ A
TTT	RISE PIN D		60 NS
	FALL PIN D		60 NS
	RISE PINS E,F		70 NS
	FALL PINS E,F		100 NS

**TECHNICAL INFORMATION**

Instruction literature and technical bulletins are available on all Digital products, if you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

**MAINTENANCE INFORMATION**

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting. Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.

ELEC. TESTER:		DATE: NOV 1 1965
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