

2.10 G790 SIGNAL SIMULATOR GENERATOR

The G790 card is on the RS09 side of the head-simulator cable. The card consists of 4 transformer networks that accept the outputs of the maintenance flip-flops and convert their transitions to signals that resemble the signals from the read heads of the disk assembly (see Figure 2-19).

2.11 G821 REGULATOR CONTROL

The G821 Regulator Control is a single-height board serving as a voltage regulator that supplies +5 Vdc to the TTL logic of the timing track writer. It is capable of supplying a maximum current of 6A, provided it is supplied with an air circulation of at least 200 cfm. Without cooling, the G821 is rated at 2A. This module was designed to be used with the PDP-15 memory and incorporates several features specifically serving the PDP-15 memory. For example, it supplies a 1A driver that is enabled when a memory OK signal is returned from memory. However, this feature is not used by the timing track writer.

Inputs:	Ordinarily, the inputs are rated at 8 Vdc, 11 Vdc, and -15 Vdc supplied by the PDP-15 power supply. However, the timing track writer uses +10 Vdc and -15 Vdc.
Outputs:	The timing track writer uses the regulated +5 Vdc output. This output can be varied between 4.5 and 5.5 Vdc by a potentiometer on the module. At 5 Vdc the regulation is 2 percent with a ripple of 25 mV peak-to-peak. The circuit schematic is given in Figure 2-20.