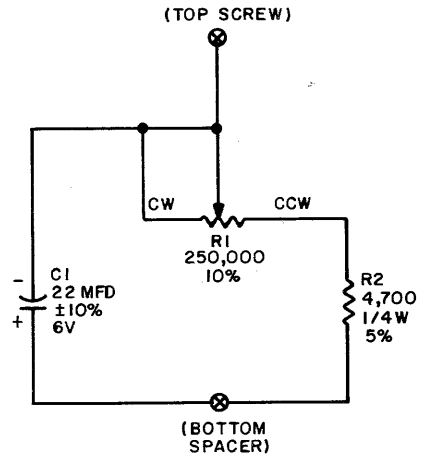


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1967 BY DIGITAL EQUIPMENT CORPORATION



UNLESS OTHERWISE INDICATED:  
 R1 IS ALLEN-BRADLEY TSRA2541  
 MODIFIED CLOCKWISE LOG TAPER  
 C1 IS SPRAGUE I50D226X9006A2 OR EQUIV.

REVISIONS CHK CHG NO. REV. 00001 A	DRN. <i>M. Waller</i>	DATE <i>5-4-67</i>	TRANSISTOR & DIODE CONVERSION CHART				<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE CALIBRATED TIMER CONTROL K376			
	CHK'D <i>[Signature]</i>	DATE <i>5/5/67</i>	DEC	EIA	DEC	EIA		SIZE B	CODE CS	NUMBER K376-0-1	REV. A
	ENG. <i>[Signature]</i>	DATE <i>5/22/67</i>									
	PROD.	DATE									
PRINTED CIRCUIT REV.							B	C			