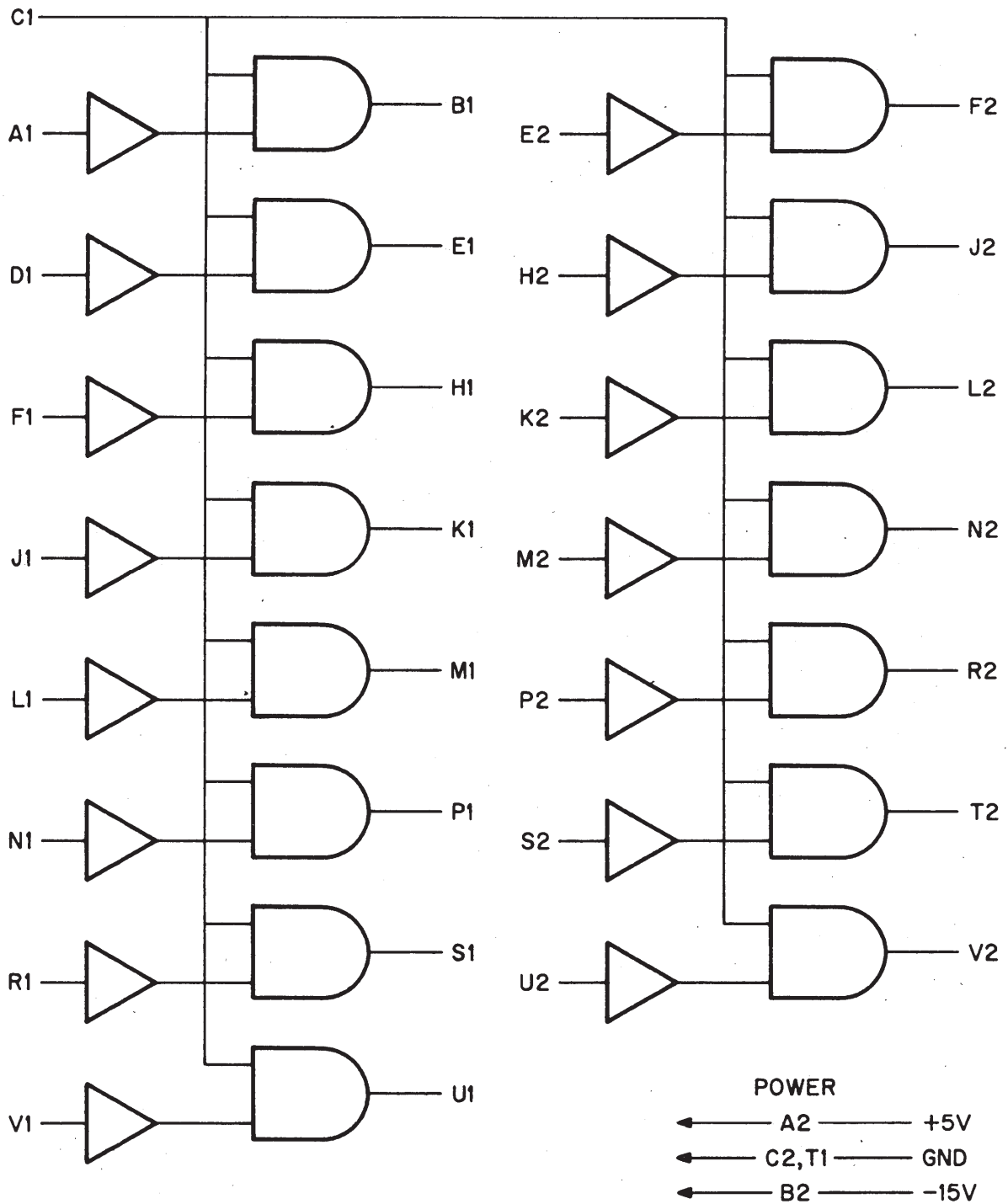


BUS DATA INTERFACE M100

M SERIES



The M100 Bus Data Interface contains fifteen circuits for convenient reception of data from the PDP-8, PDP-8/I negative voltage bus. It is pin compatible with the M101 Positive Bus Data Interface.

The loading presented to the negative voltage bus differs from that loading using the standard negative bus modules (i.e., R107, R111) in that the data lines are loaded only if the appropriate device is selected. The option select output of the M102 must be connected to the strobe input pin, C1, of the M100.

The enabling line of the M100 cannot be used as a strobe line. The output signals are indeterminant for a period of 200 nsec after the enabling line has become true.

Inputs: All outputs will drive 10 TTL unit loads.

Conversion: Logic Diagram: An active voltage is a True State, i.e., -3 v or $+3\text{ v} = "1"$.

A ground is a $\overline{\text{True}}$ State.

A data input of -3 v will yield an output of ground when C1 is gated by a positive voltage logic "1" of $+2.4\text{ volts}$.

Threshold switching level: -1.5 v . typ.

Propagation delay: 40 nsec typ.

Power: $+5\text{ volts}$ at 60 ma. (max.); -15 volts at 10 ma. (max.)