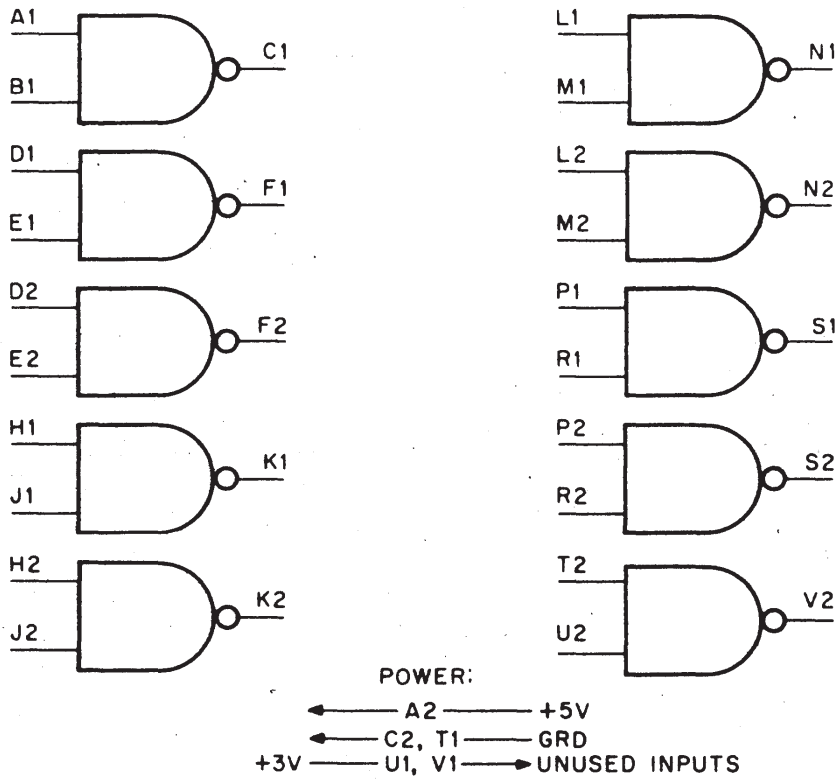


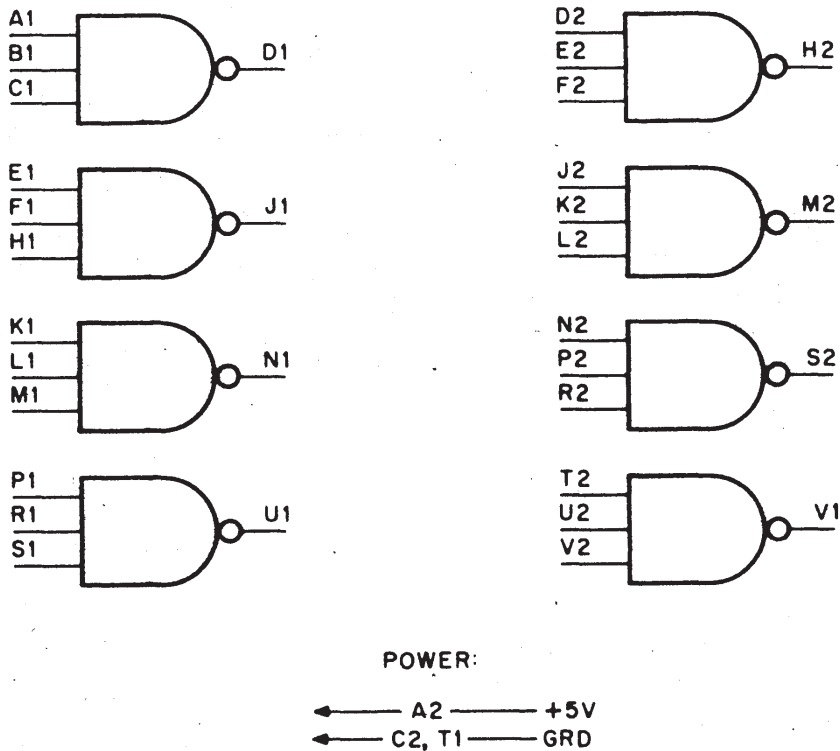
NAND GATES

TYPES M113, M115, M117, M119

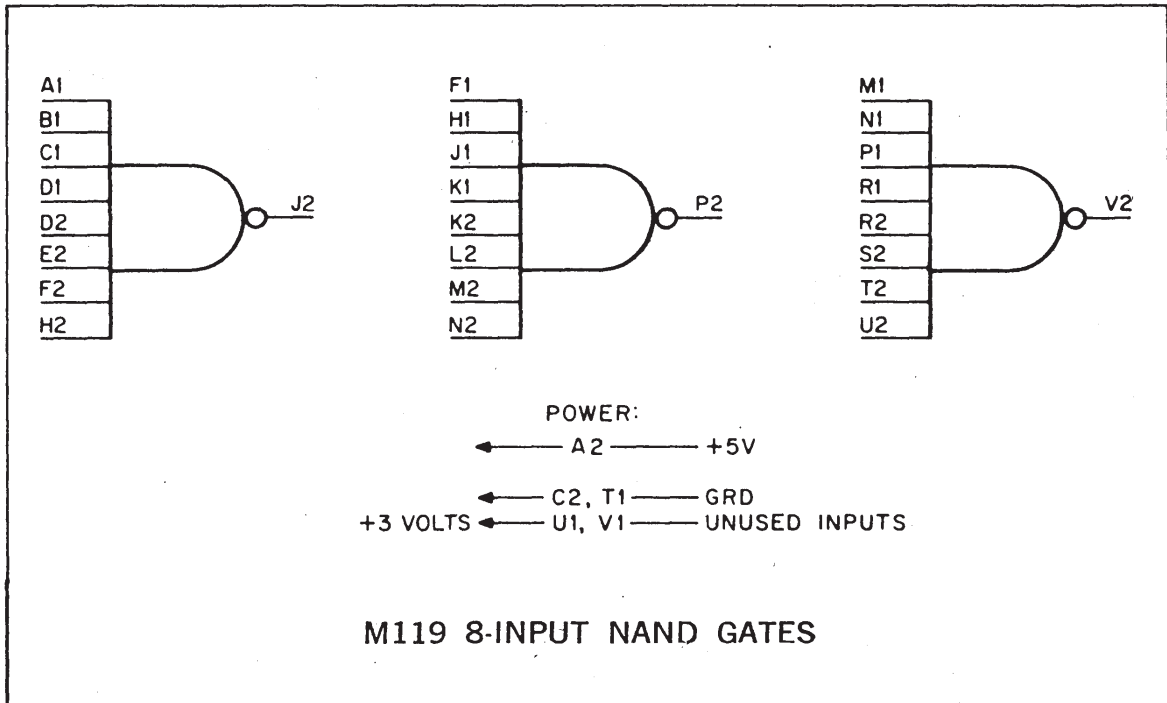
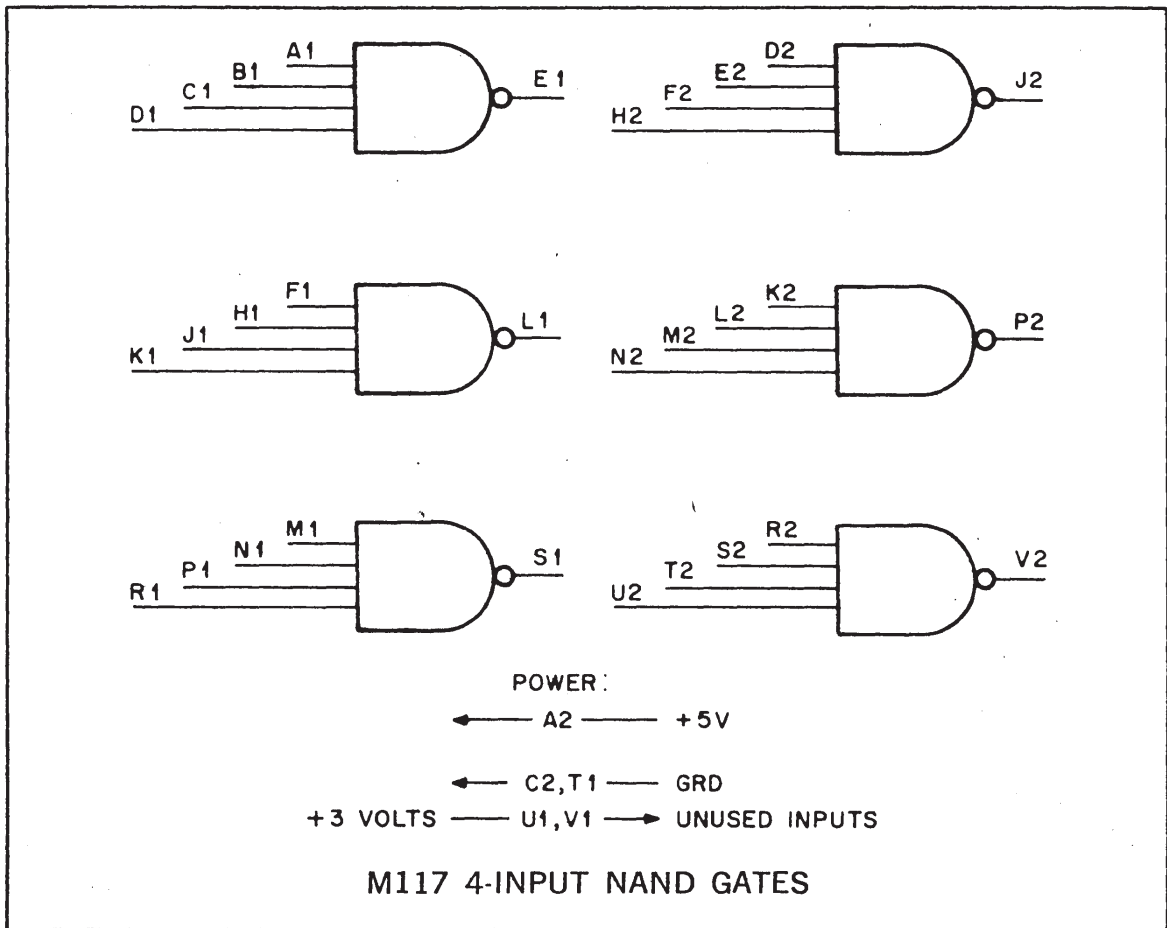
M
SERIES



M113 2-INPUT NAND GATES



M115 3-INPUT NAND GATES



These modules provide general-purpose gating for the M Series, and are most commonly used for decoding, comparison, and control. Each module performs the NAND function $A \cdot B \cdot \dots \cdot N$, depending upon the number of inputs.

M113 — Ten, two-input NAND gates that also may be used as inverters.

M115 — Eight, three-input NAND gates.

M117 — Six, four-input NAND gates.

M119 — Three, eight-input NAND gates.

Unused inputs on any gate must be returned to a source of logic 1, for maximum noise immunity. In the M113, M117, M119, M121, M617 and M627 modules, two pins are provided (U1 and V1) as source of +3 volts for this purpose. Each pin can supply up to 25 unit loads.

Typical propagation delay of M Series gates is 15 nsec.

Inputs: Each inputs presents one unit load.

Outputs: Each output is capable of driving 10 unit loads.

Power:

M113: 30 ma (avg)	}	at +5 volts
M115: 28 ma (avg)		
M117: 18 ma (avg)		
M119: 9 ma (avg)		

M113 — \$23.00
M115 — \$24.00
M117 — \$24.00
M119 — \$24.00
