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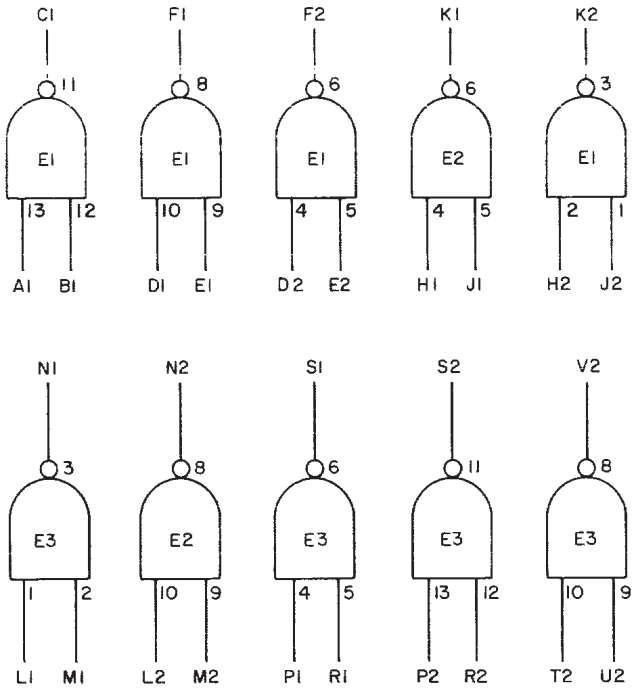

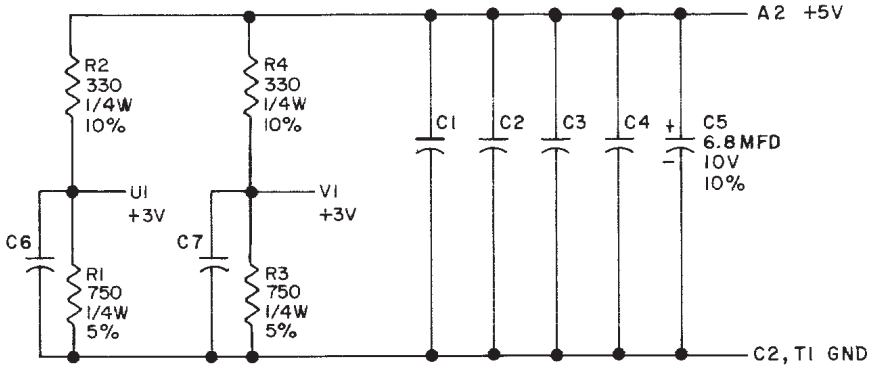

UNLESS OTHERWISE INDICATED:
E1 - E3 ARE DEC74H00N
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V
CAPACITORS ARE .01 MFD, 100V, 20%

REVISIONS		
REV.	A	
CHG NO.	00001	
CHK		

DRN.	M. MALLER	DATE	8-8-68
CHK'D	W. MULLEN	DATE	8-9-68
ENG.	D. O'CONNOR	DATE	10-25-68
PROD.		DATE	

TRANSISTOR & DIODE CONVERSION CHART

DEC	EIA	DEC	EIA

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE 10-2 INPUT NAND GATES M133

SIZE	CODE	NUMBER	REV.
B	CS	M133-0-1	A

PRINTED CIRCUIT REV. B