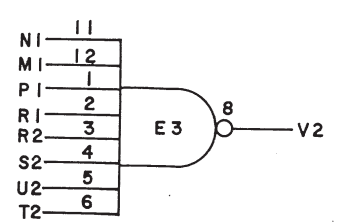
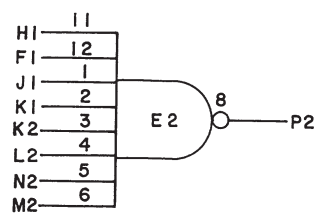
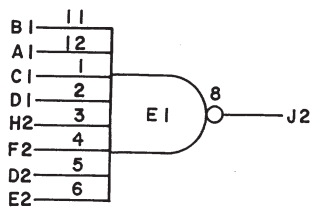


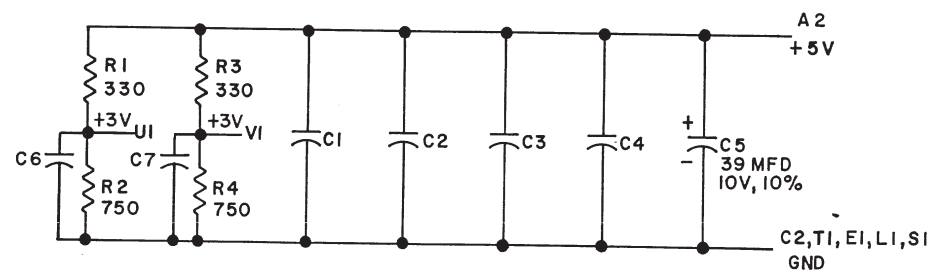


REV. D	NUMBER M139-0-1	CS	B
		SIZE	B

THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION



UNLESS OTHERWISE INDICATED:
 IC'S ARE DEC1074H30
 PIN 7 ON EACH IC = GND
 PIN 14 ON EACH IC = +5V
 CAPACITORS ARE .01 MFD, 100V, 20%
 RESISTORS ARE 1/4W, 5%



REVISIONS CHK CHG NO. REV. B 00001 C 00002 D 00003	DRN. <i>Spitzberg</i>	DATE 3-4-69	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE 3-8 INPUT NAND GATE M139			
	CHK'D <i>A. Yanga</i>	DATE 3-6-69	DEC	EIA	DEC	EIA		SIZE B	CODE CS	NUMBER M139-0-1	REV. D.
	ENG. <i>DEO</i>	DATE 4-7-69						PRINTED CIRCUIT REV. B			
	PROD.	DATE						D51. 374, 434, 435 2 PINK			

DEC FORM NO. DRB 102

