

M1500 BIDIRECTIONAL BUS INTERFACING GATES

**UNIBUS/
OMNIBUS**

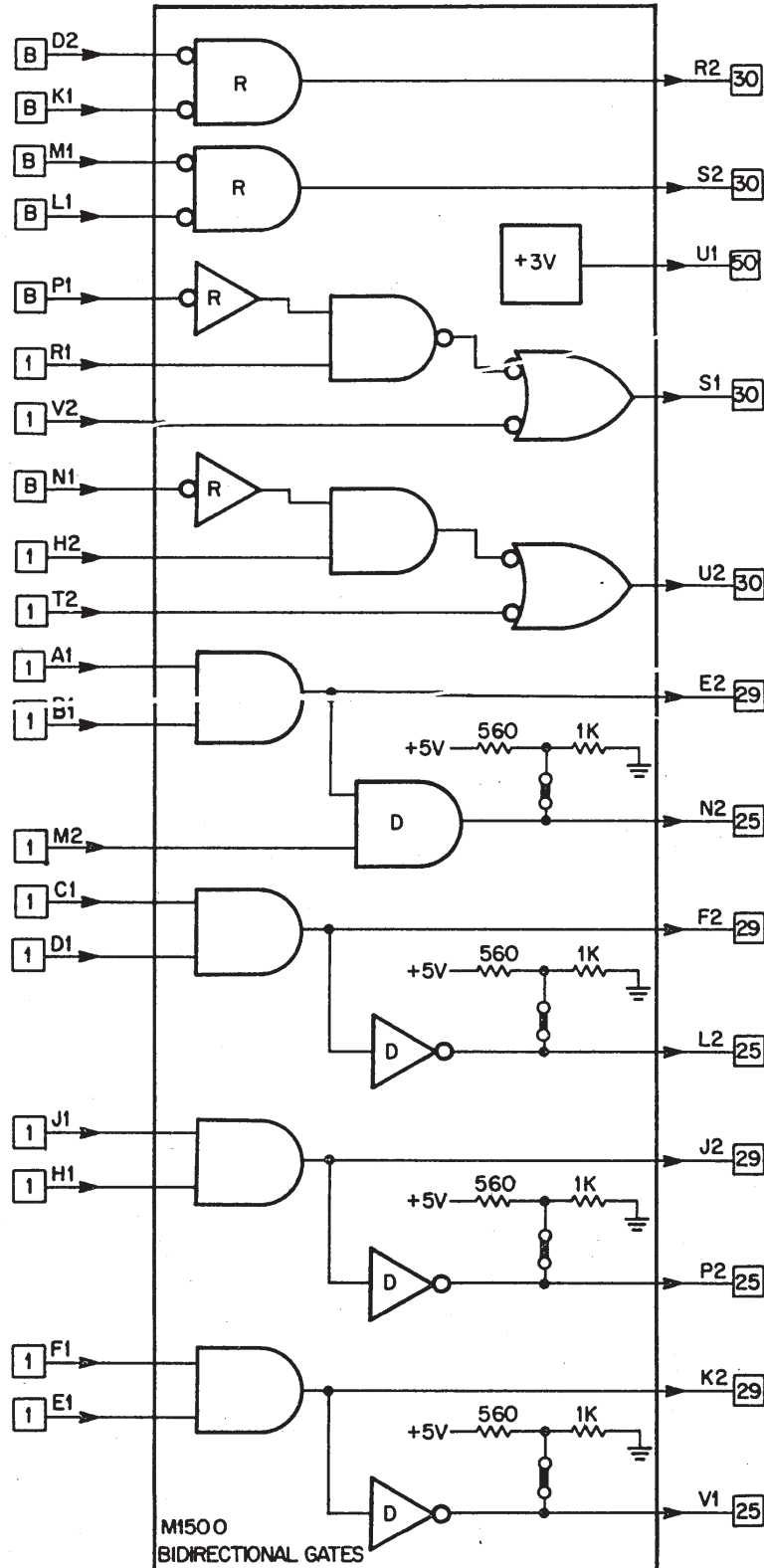
M SERIES

Length: Extended
Height: Single
Width: Single

Price:

\$35

Volts +5 GND
Power mA (max.) 300
Pins A2 C2, T1



This module provides gating arrangements useful for interfacing to the PDP-8/e, PDP-8/m and PDP-11 computers. It is designed specifically to provide additional gating and output drive when using the M1501-M1502 Input/Output modules. Examples are shown in Figures 1 and 2.

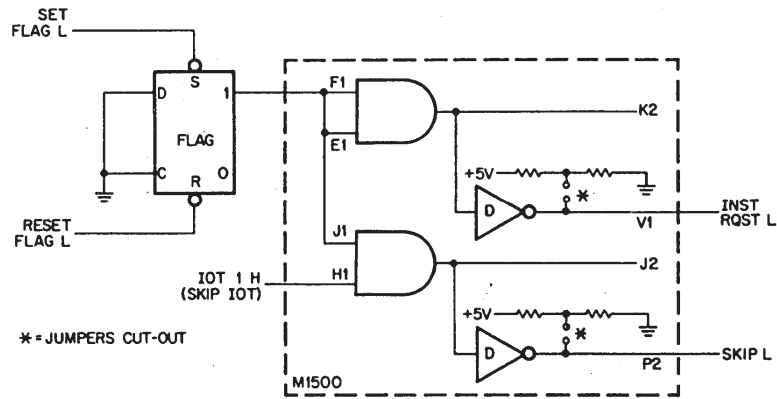


Figure 1. Skip and Interrupt Drivers for PDP-8/e or PDP-8/m Interfacing.

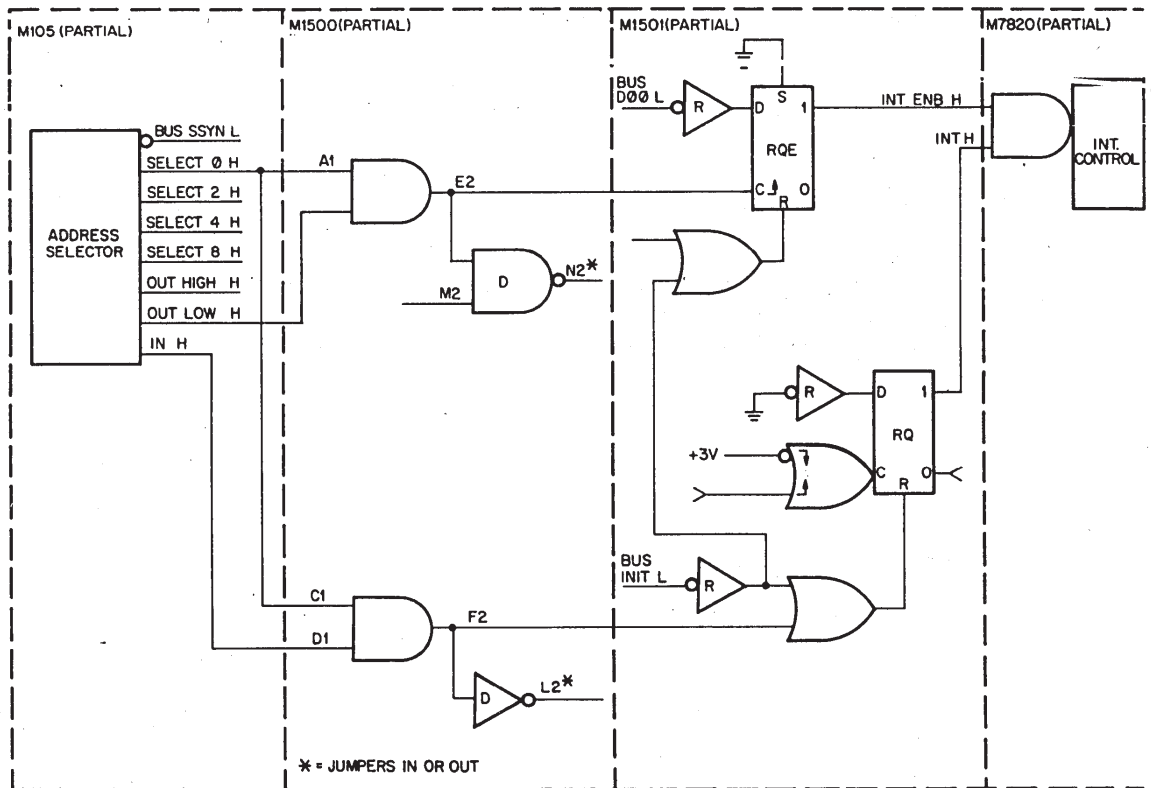


Figure 2. Using M1500 AND Gates with M1501 in PDP-11 Interfacing.

APPLICATIONS

PDP-11 Interfacing:

1. ANDing SELECT signals with direction signals (OUT HIGH, OUT LOW) to load registers
2. ANDing SELECT signals with direction signals (OUT HIGH, OUT LOW) to form set or reset pulses
3. Receiving the INIT (initialize) signal from the UNIBUS and distributing it via high-power drivers

PDP-8/e, PDP-8/m Interfacing:

1. Generation of additional SKIP and INT RQST signals to supplement those available on the M1510
2. ANDing BTP3 with an IOT for loading registers

General-Purpose Use:

1. Providing general-purpose high fan-out drivers
2. Providing a stage of inversion with high fan-out capability

Restrictions: The module is electrically, but not mechanically, compatible with the PDP-8/e OMNIBUS. Do not plug the module directly into the OMNIBUS. OMNIBUS signals may be connected to appropriate module pins by backplane wiring. This module is designed for use in bus expansion hardware such as:

BB11 Blank System Unit (PDP-11 UNIBUS)
H9190 Bus Expander (PDP-8/e OMNIBUS)

FUNCTIONS

Inputs marked B present one bus receiver load to the UNIBUS or OMNIBUS. All other inputs are standard TTL; unit loads are shown on the logic diagram.

Output drivers marked D provide open collector outputs with jumpered-in pull-up resistors to enable their use in general logic applications. These outputs may be used to drive UNIBUS or OMNIBUS lines if the associated jumpers are cut out by the user.

All other outputs provide standard TTL drive as shown on the logic diagram.