

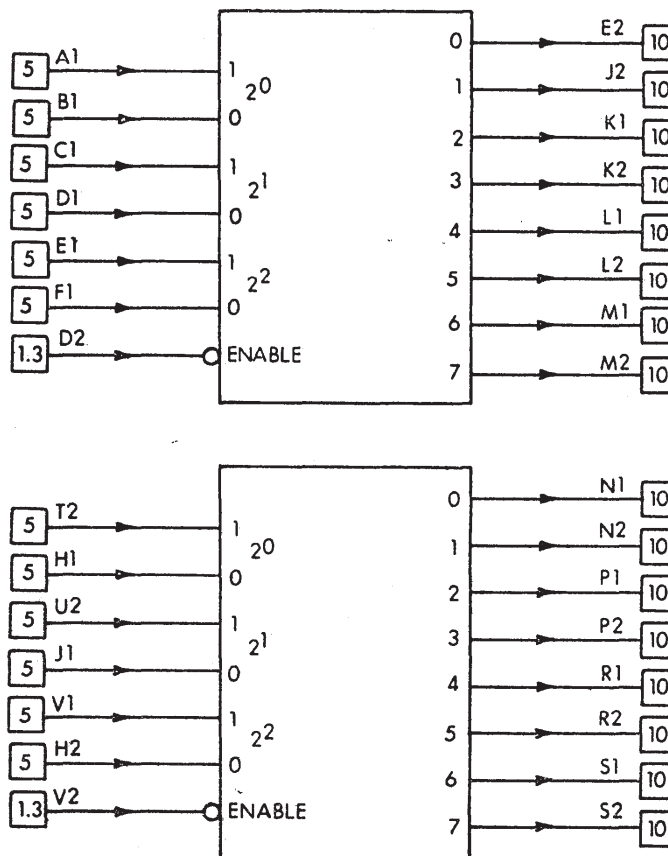
M152 DUAL 1-of-8 DECODER

NUMERIC

M SERIES

Length: Standard
Height: Single
Width: Single

Price
\$36



Volts	Power	Pins
+5	mA (max.)	A2
GND	265	C2, T1

The M152 module consists of two identical binary-to-octal decoders. Each requires complementary inputs.

With the addition of a simple inverter circuit, the module may be used as a single 1-of-16 decoder using the ENABLE lines as the highest order address lines.

The outputs are asserted HIGH when selected.

APPLICATIONS

The module may be used for applications such as memory address decoding.

The module may also be used as a demultiplexer by supplying the data at the ENABLE input at the same frequency as a control clock, and generating the binary address 0 through 7 from the control clock and applying these addresses to the address pins of the decoder. The data is routed to output 0 through 7.

SPECIFICATIONS

Propagation Delay

From:

Any Data Input

Either ENABLE Input

To:

Any Output = 12 ns (max)

Any Output = 24 ns (max)