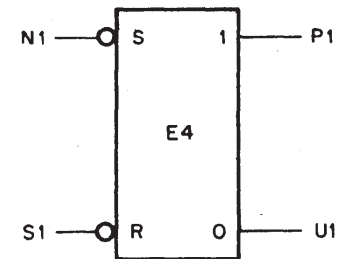
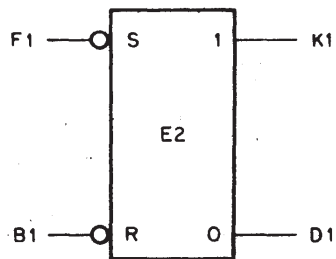
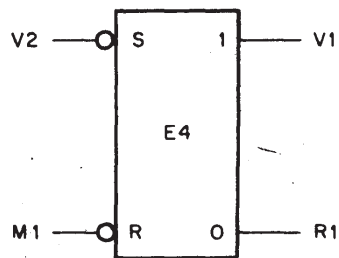
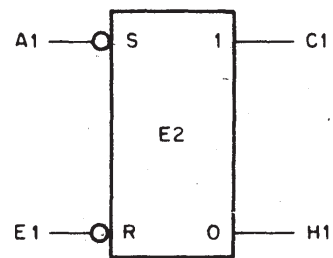
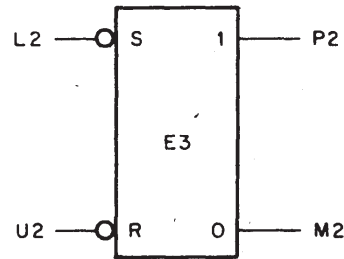
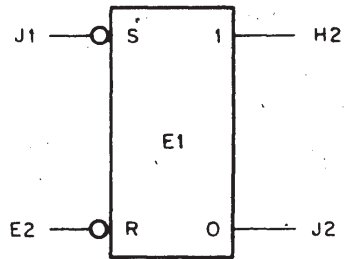
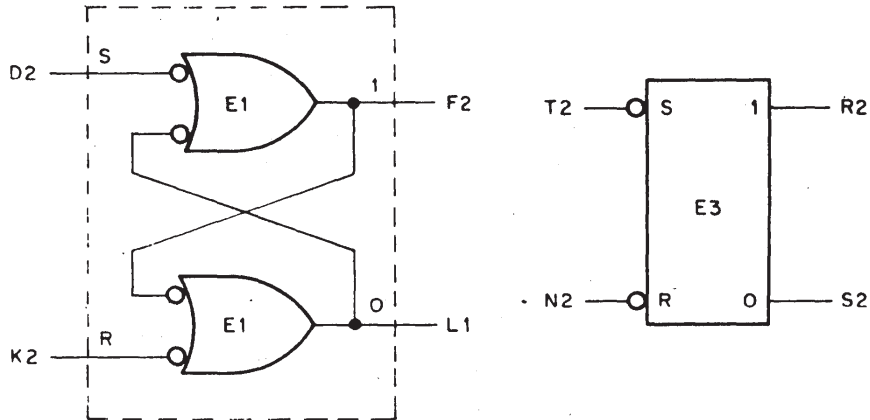


# 8-R/S Flip-Flops

## TYPE M203

# M

## SERIES



POWER:

← A2 — +5V

← C2, T1 — GRD

**M203 8-R/S FLIP-FLOPS**

The M203 is made up of 8 R/S type flip-flops. Each flip-flop is made up of two 2-input NAND gates whose outputs are cross coupled. R/S flip-flops provide an inexpensive method of storage but care must be taken to inhibit placing the Set and Reset inputs low at the same time. In this case, the last of the inputs to be removed will control the final state of the flip-flop.

The propagation delay of the M203 is approximately 30 nsec.

**INPUTS:** All inputs present 1 unit load

**OUTPUTS:** All outputs are capable of driving 9 unit loads.

**POWER:** +5 volts, 35 ma (avg.)

---

M203 — \$32.00

---