



FLIP CHIP MODULES TEST SPECS

TYPE: M204

COUNTER/BUFFER

TEST	CONDITIONS	MAXIMUM	MINIMUM
LOGIC "0" OUTPUT	$V_{CC} = +4.75 \text{ V}$ $I_L = +16.0 \text{ MA}$	+ 0.4 V	/
LOGIC "1" OUTPUT	$V_{CC} = +4.75 \text{ V}$ $I_L = -0.4 \text{ MA}$	/	+ 2.4 V
LOGIC "0" CLEAR INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 12.8 MA	/
LOGIC "0" SET & CLOCK INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 3.2 MA	/
LOGIC "0" GATE INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +0.4 V	- 1.6 MA	/
LOGIC "1" CLEAR INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO 2.4 V	+ .32 MA	/
LOGIC "1" SET INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +2.4 V	+ 0.8 MA	/
LOGIC "1" CLOCK INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +5.25 V	+ 1.0 MA	/
LOGIC "1" GATE INPUT CURRENT	$V_{CC} = +5.25 \text{ V}$ MEASURED TO +2.4 V	+ .04 MA	/
CLOCK TO OUTPUT DELAY TD_1 & TD_0	$V_{CC} = +5.0 \text{ V}$ NO LOAD	50 NS	10 NS
CLOCK TO OUTPUT DELAY TD_1 & TD_0	$V_{CC} = +5.0 \text{ V}$ WITH RC LOAD *	75 NS	10 NS
SET/CLEAR TO OUTPUT DELAY TD_1 & TD_0	$V_{CC} = +5.0 \text{ V}$ WITH RC LOAD *	75 NS	/

* RC LOAD = 330 Ω AND 150 PFD

TECHNICAL INFORMATION

Instruction literature and technical bulletins are available on all digital products, if you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

MAINTENANCE INFORMATION

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting.
Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.