M227
AC Shifter

The M227 module contains 9 bits of the accumulator with input gating and shifting logic. Two of these modules are used in the central processor of the PDP-15 to form the 18-bit accumulator (AC) shift register. (Refer to Engineering Drawings D-BS-KP15-0-1 through D-BS-KP15-0-18.) The register is used for manipulating data and temporarily storing results of arithmetic/logical operations.

The following are the input, output, and power characteristics of the M227 module.

INPUTS: All inputs but the CLOCK input present 1.25 TTL unit loads. The CLOCK input presents 18 unit loads.

OUTPUTS: Each output is capable of driving 10 TTL unit loads.

POWER: Power dissipation of the M227 module is 5V at 420 mA (maximum).