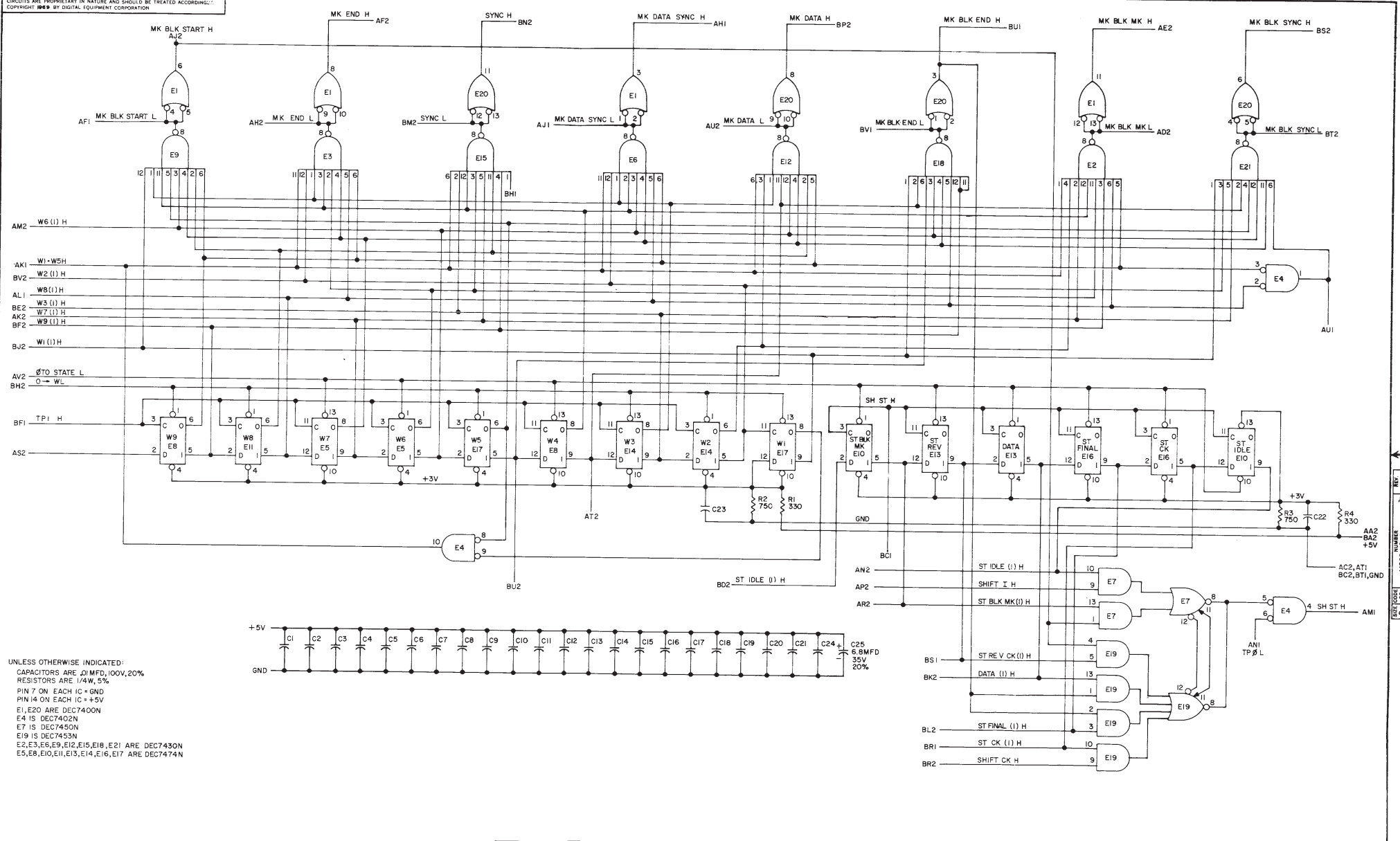


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION.

1-0-922M SD D  
#38W1N 300329



UNLESS OTHERWISE INDICATED:  
CAPACITORS ARE .01 MFD, 100V, 20%  
RESISTORS ARE 1/4W, 5%  
PIN 7 ON EACH IC = GND  
PIN 14 ON EACH IC = +5V  
E1, E20 ARE DEC7400N  
E4 IS DEC7402N  
E7 IS DEC7450N  
E19 IS DEC7453N  
E2, E3, E6, E9, E12, E15, E18, E21 ARE DEC7430N  
E5, E8, E10, E11, E13, E14, E16, E17 ARE DEC7474N

REV. 1  
NUMBER M228-0-1  
D. CS

DIG FORM NO. 010

REVISIONS CHKD AND REV	DRN	DATE	TRANSISTOR & DIODE CONVERSION CHART				TITLE <b>digital</b> MARK TRACK DECODER M228	DATE CODE D CS	NUMBER M228-0-1	REV. A
	CHKD	DATE	DEC	EIA	DEC	EIA				
DRN: <i>W. J. ...</i> CHKD: <i>...</i> DATE: <i>...</i>			DEC: <i>...</i> EIA: <i>...</i> DEC: <i>...</i> EIA: <i>...</i>				PRINTED CIRCUIT REV.			