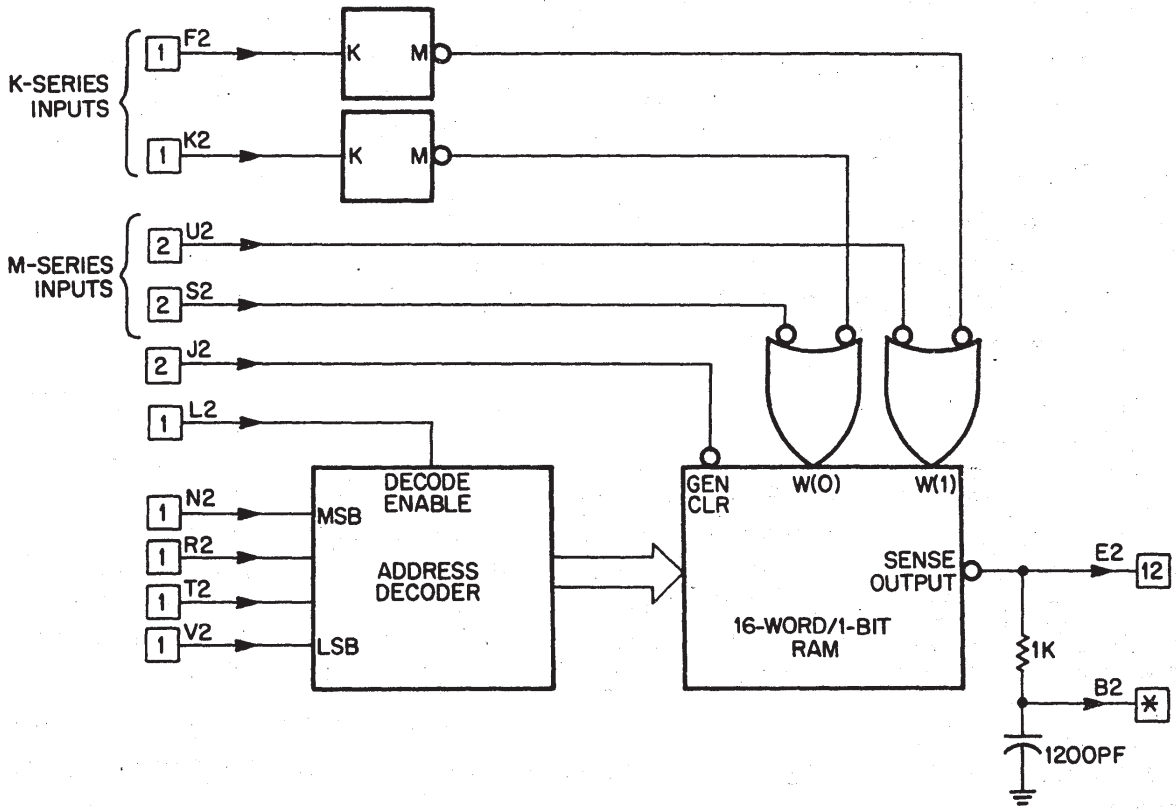


M232 16-WORD RAM

FLIP-FLOPS
M SERIES

Length: Standard
 Height: Single
 Width: Single

Price:
\$125



*=SEE TEXT

Volts	Power	Pins
+5	mA (max.)	A2
GND	200	C2, T1

The M232 provides individually addressable storage for 16 bits.

FUNCTIONS

Each bit is addressed by a 4-bit code on input lines N2, R2, T2, and V2. If a binary 0 is stored at an accessed address, the output sense pin E2 remains HIGH. If a binary 1 is stored, the output sense signal goes LOW.

Writing with M Series signals is achieved by causing pin U2 to go LOW for a binary 1 or by causing pin S2 to go LOW for a binary 0 after a location has been accessed. Writing with K Series signals is achieved by causing pin F2 to go HIGH for a binary 1, or pin K2 to go HIGH for a binary 0 after a location has been accessed.

All locations can be accessed simultaneously and all bits cleared to binary 0 by a 5-microsecond LOW signal on the GENERAL CLEAR pin J2. Pin B2 is a special-purpose OUTPUT SENSE connection which is used when module outputs are ORed or connected in parallel as is done in some PDP-14 systems.

SPECIFICATIONS

Access Time: Access to an addressed location occurs 25 ns after the DECODE ENABLE pin L2 goes HIGH.

Write Pulse (M Series): 25 ns min.

Write Pulse (K Series): 5 μ s min.

Clear Pulse (J2): 5 μ s min.