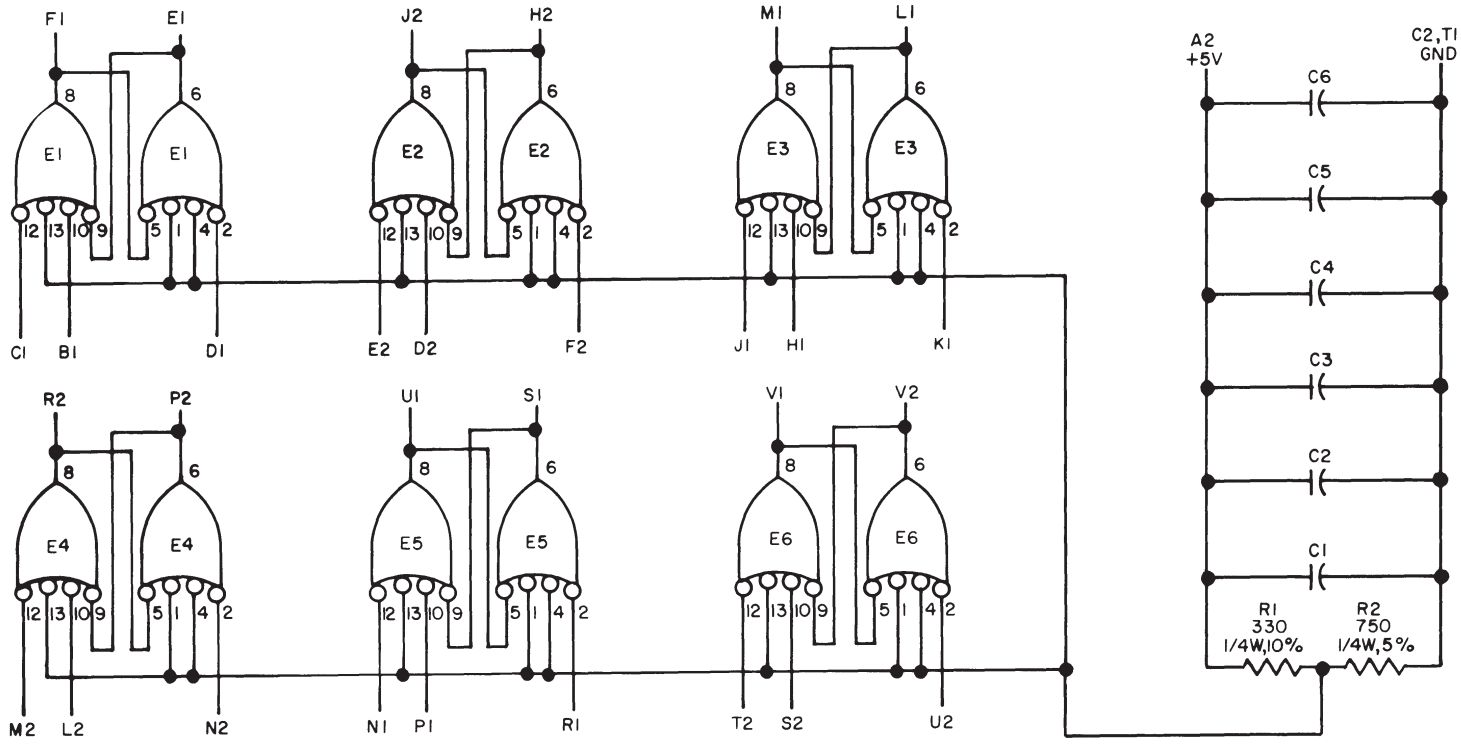


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION



UNLESS OTHERWISE INDICATED:
 PIN 7 ON EACH IC = GND
 PIN 14 ON EACH IC = +5V
 CAPACITORS ARE .01MFD 100V
 E1, E2, E3, E4, E5, E6 ARE 74H40N

M240-2

REV. NO.	CHK	NO.	REV.
1		00001	B

DRN. <i>R. BUTLER</i>	DATE <i>4/1/69</i>
CHK'D <i>M. Mans...</i>	DATE <i>4/15/69</i>
ENG. <i>R. ...</i>	DATE <i>3/20/69</i>
PROD.	DATE

TRANSISTOR & DIODE CONVERSION CHART			
DEC	EIA	DEC	EIA

digital
 EQUIPMENT CORPORATION
 MAYNARD, MASSACHUSETTS

TITLE 6 R/S FLIPFLOPS M240			
SIZE B	CODE CS	NUMBER M240-0-1	REV. B
PRINTED CIRCUIT REV.			B

5 INK DIST 3241434, 4353