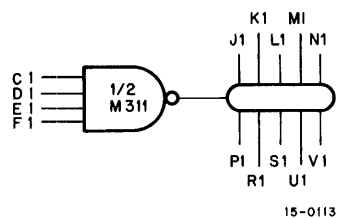


M311 Tapped Delay Lines

The M311 module contains two tapped delay lines. Each delay line has ten taps providing delays in 25 ns intervals from 25 ns through 250 ns (see simplified diagram). Pin J1 supplies the minimum delay of 25 ns and pin V1 supplies the maximum delay of 250 ns. The input NAND gate of the delay line provides an additional delay of 10 ns (maximum). Delay line tolerance is $\pm 5\%$.

The following are the input, output, and power characteristics of the M311 module.

- INPUTS:** Each input presents 1.25 TTL unit loads.
- OUTPUTS:** Each output is capable of driving 1.25 unit loads. Maximum driving capability of the delay line is 6 unit loads, with a maximum line length of 8 ns.
- POWER:** Power dissipation of the M311 module is 5V at 170 mA (maximum).



M311 Simplified Diagram