

M420

Phase-Lock Clock

The M420 Phase-Lock Clock is used in the data separator control of the RP15 Disk Pack Control. Inputs include a reference signal at pin BE2 and a controlled input at pin BH2. The controlled input is the output frequency of the M420 at pin BK2 divided by 1, 2, or 4. This 5 MHz output has a 50 percent duty cycle.

The M420 contains a phase error detector and a voltage controlled oscillator (VCO). The phase error detector output is connected to the VCO input by a jumper between pins AV2 and AU2.

INPUTS:	Inputs at pins BE2 and BH2 present two unit loads.
OUTPUTS:	The output at pin BK2 can drive eight unit loads.
POWER:	13 mA at -15V (MAX) 10 mA at +10V (MAX) 108 mA at +5V (MAX)