

M516 Positive Bus Receiver Input Circuit

Six four input NAND gates with overshoot and undershoot clamp on one input of each gate. Pullup resistors to +5 v are also provided.

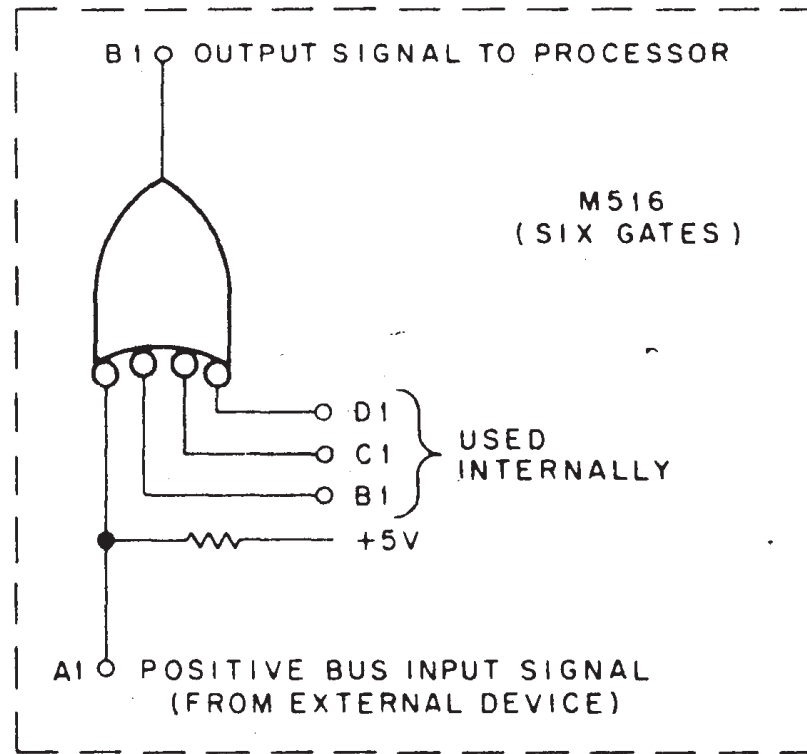


Fig. 44 Typical M516 Positive Bus Receiver Input Circuit