M610
OPEN COLLECTOR
TWO-INPUT NAND GATE

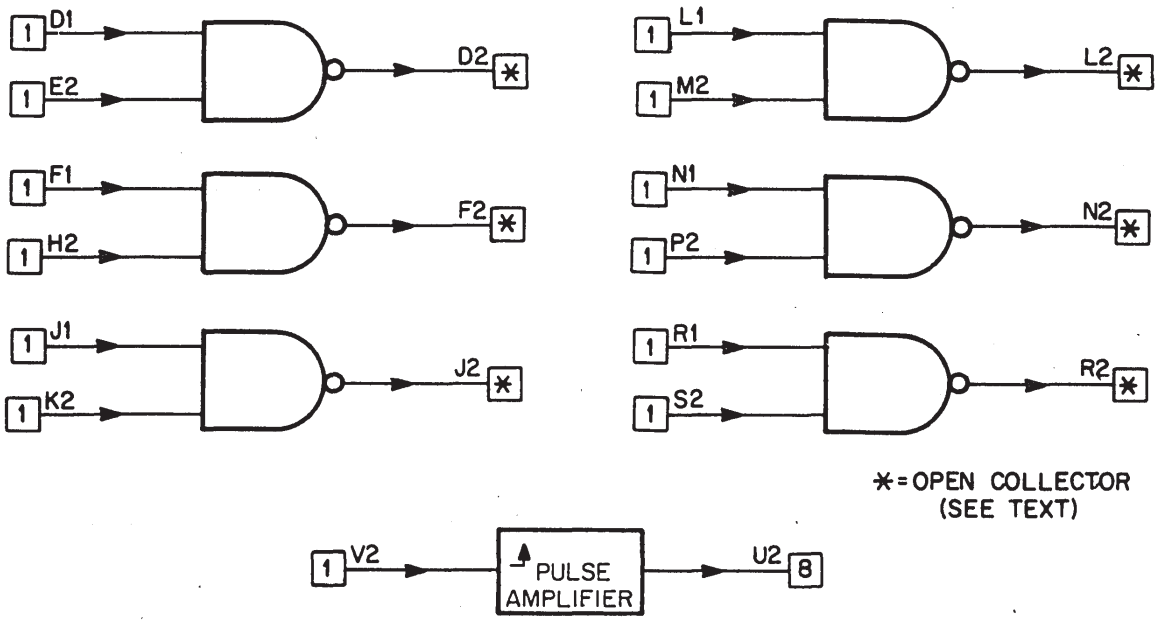
GATES

M SERIES

Length: Standard
Height: Single
Width: Single

Price:

$20



Volts	Power mA (max.)	Pins
+5	41	A2
GND		C2, E1, H1 M1, P1, S1

The M610 contains 6 two-input NAND gates with open collector outputs. It also contains a pulse amplifier which does not have an open collector output.

SPECIFICATIONS

Outputs: D2, F2, J2, L2, N2, R2 are capable of sinking 16 mA to ground.

NAND Gate Maximum Propagation Delay: 15 ns when the output goes from HIGH to LOW; however, when the output goes LOW to HIGH, the propagation delay depends upon the load impedance. As an example, with the load shown in the figure, the maximum propagation delay time from a logic LOW to a logic HIGH is 45 ns.

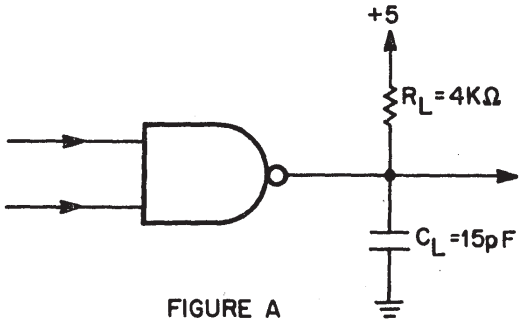


FIGURE A

Pulse Amplifier Maximum Propagation Delay: 60 ns for both HIGH going and LOW going output pulse transitions.