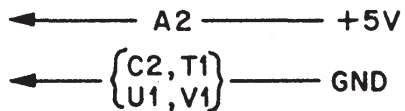
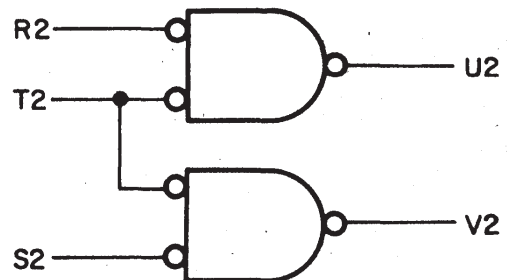
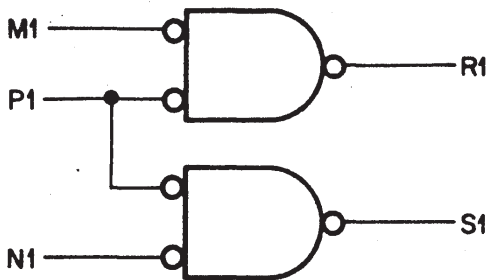
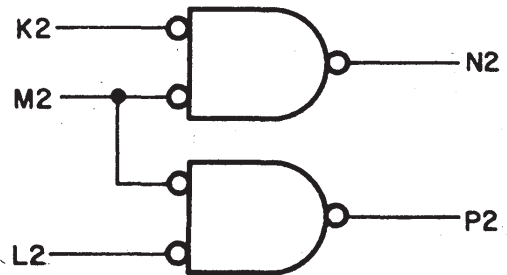
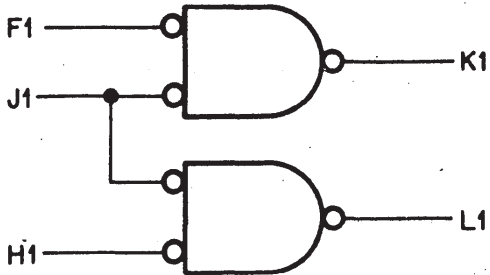
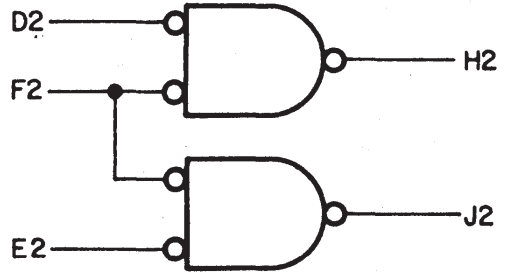
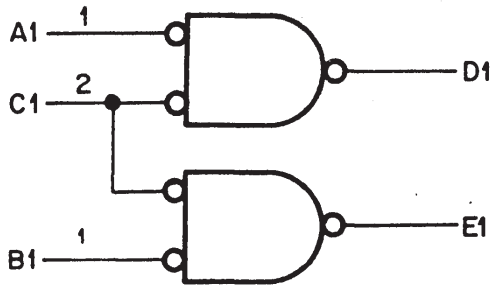


BUS DRIVER M623

M SERIES



The M623 contains twelve two input AND gate bus drivers for convenient driving of the positive input bus of either the PDP-8/I or PDP-8/L. Each driver can sink 100 ma. at ground and allows a maximum output voltage of +20 volts. The output consists of an open collector NPN transistor.

Inputs: Input levels are standard TTL levels of 0 volts and +2.4 volts. Data inputs A1, B1, F1, H1, M1, N1, D2, E2, K2, L2, R2, and S2 each present one TTL unit load. All other inputs present two unit loads.

Outputs: A driver output will be at ground when both inputs are at ground. Output rise and fall (TTT) are typically 30 nsec when a 100 ma. resistive load is connected to a driver output. Output voltage must not exceed +20 volts.

Power: +5 volts, 71 ma. (max.) plus external load.