M798
UNIBUS DRIVERS

Length: Extended
Height: Single
Width: Single

Price: $40
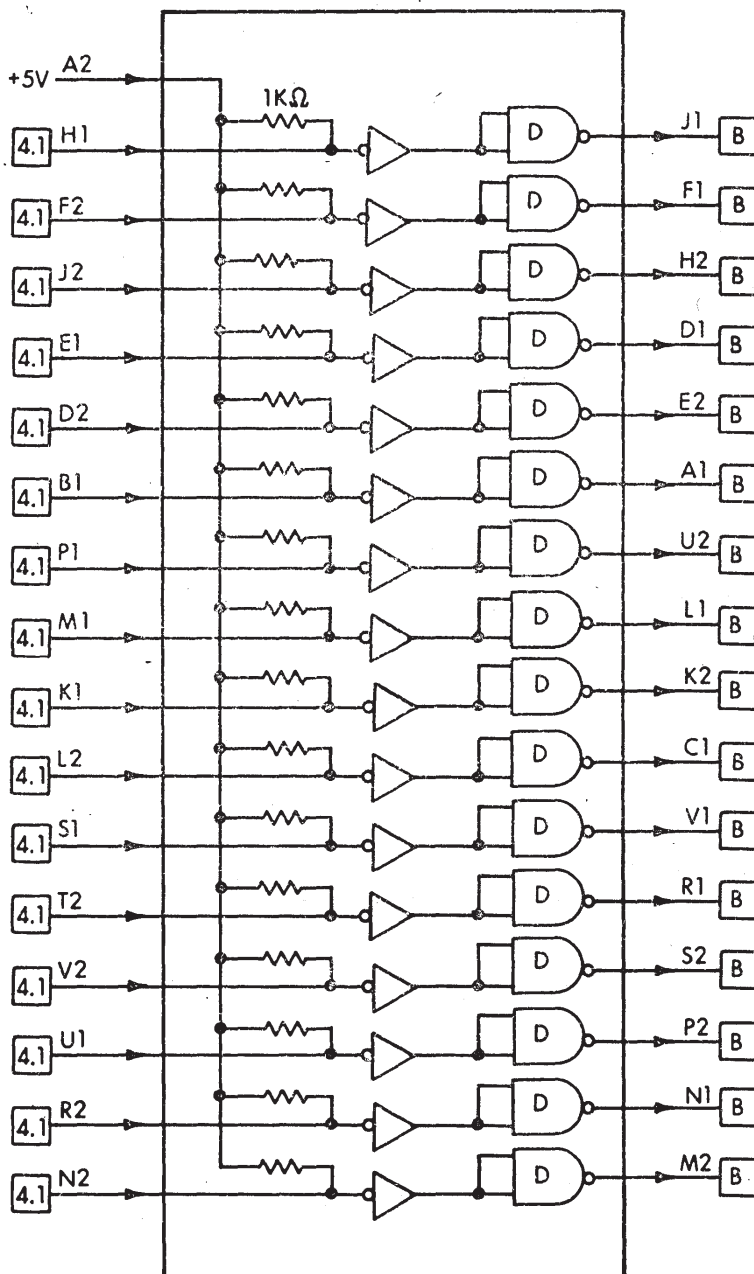
Volts	Power mA (max.)	Pins
+5	320	A2
GND		C2, T1

This module consists of 16 noninverting UNIBUS drivers. The module is used in device interfaces to minimize the loading effect caused by attaching several drivers to the same UNIBUS signal line, as in the case of a device containing multiple registers. Loading of signal lines on the UNIBUS is restricted to the equivalent of one UNIBUS receiver input and two UNIBUS driver outputs per device.

In addition, the M798 module allows the UNIBUS to be driven by standard open-collector TTL gates. The inputs to each M798 driver circuit are pulled up to +5 V through a 1-kilohm resistor. As shown in Figure 1, an internal wired OR bus is created that is driven from standard open-collector gates (available on M141 and M149 modules) or from UNIBUS drivers (available on M783 module).
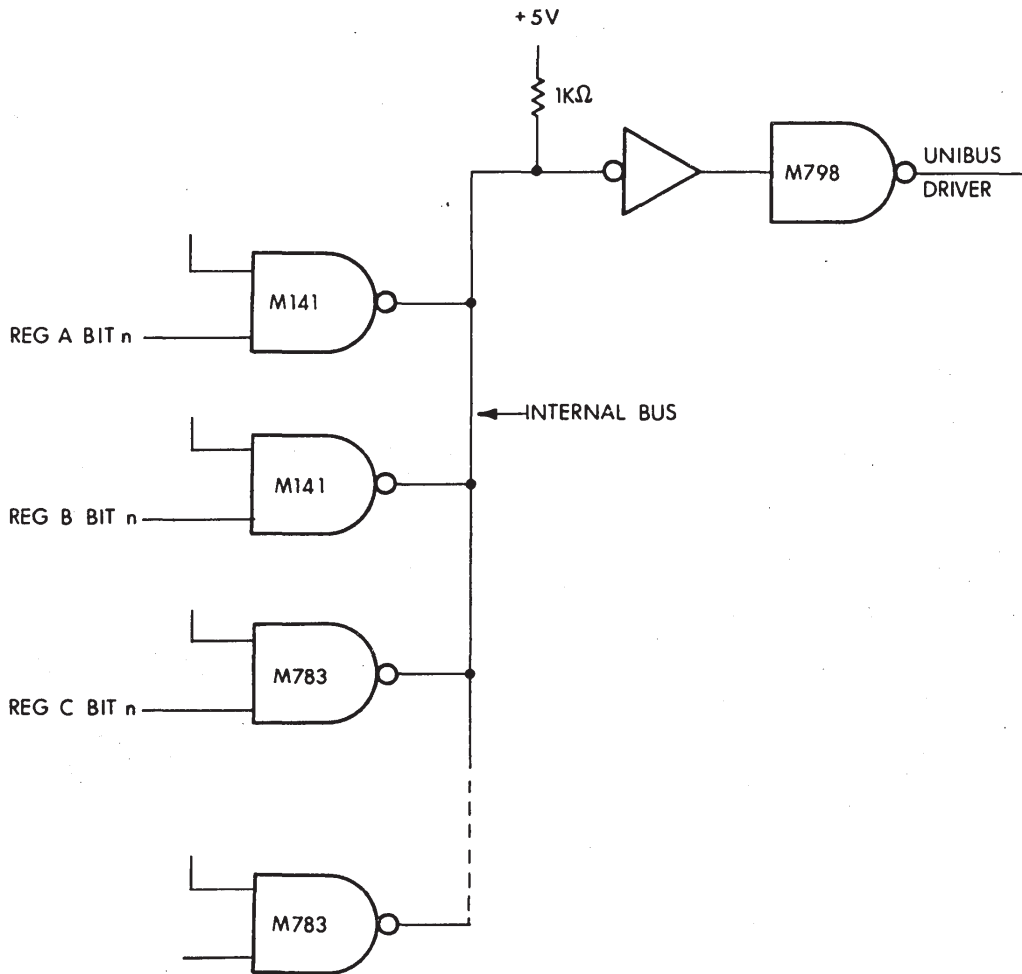

Figure 1.   Typical Use of M798

SPECIFICATIONS
Output Driver: The output Low voltage for each of the 16 outputs is 0.8 volts maximum with 50 mA current sink. The output High leakage current is 25 microamperes maximum.

Propagation Delay: The propagation delay between the 16 inputs and the driver outputs is 60 nanoseconds maximum.