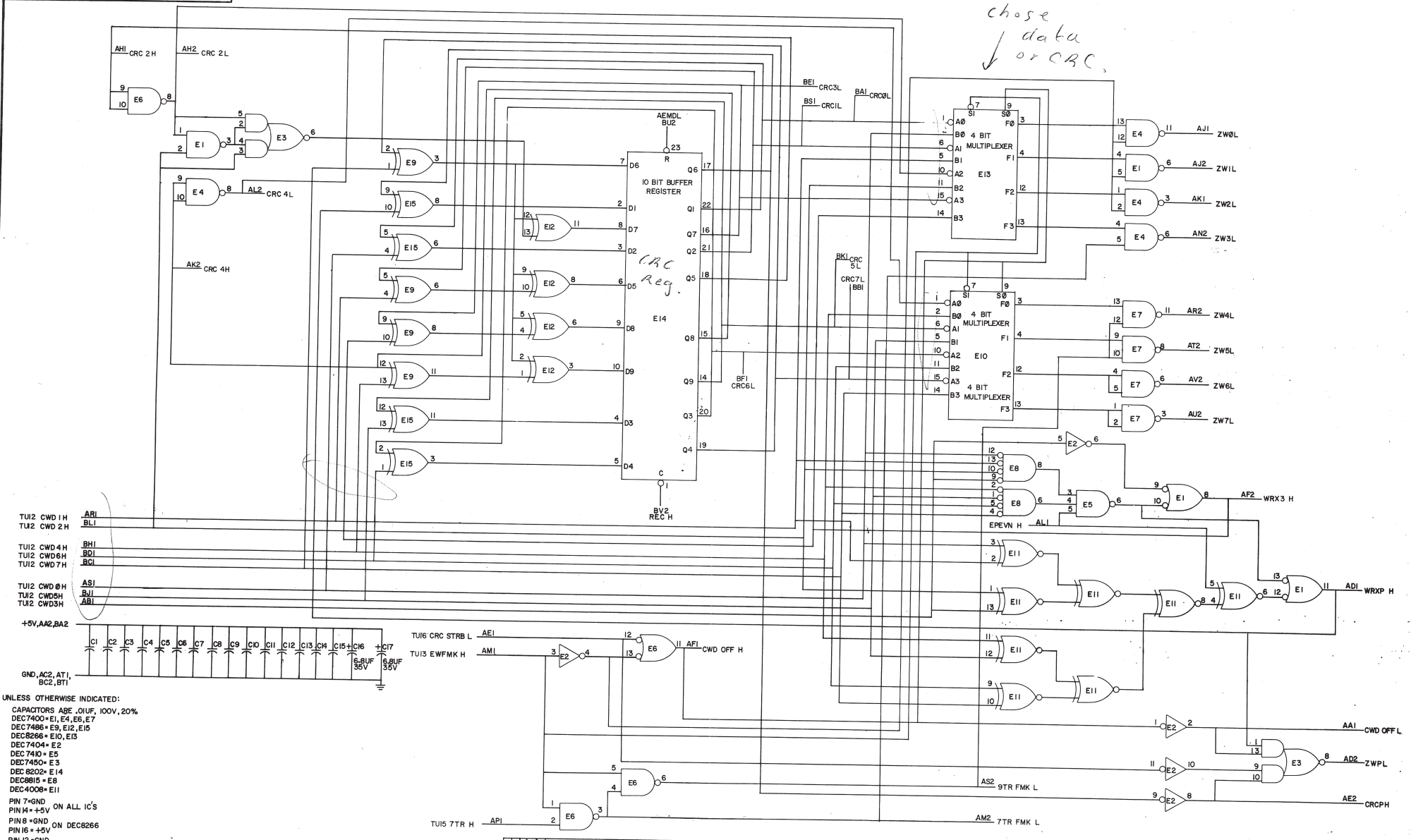


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*chose data
or CRC*



UNLESS OTHERWISE INDICATED:
CAPACTORS ARE .01UF, 100V, 20%
DEC 7400 = E1, E4, E6, E7
DEC 7486 = E9, E12, E15
DEC 8266 = E10, E13
DEC 7404 = E2
DEC 7410 = E5
DEC 7450 = E3
DEC 8202 = E14
DEC 9815 = E8
DEC 4008 = E11
PIN 7 = GND
PIN 14 = +5V ON ALL IC'S
PIN 8 = GND
PIN 16 = +5V ON DEC 8266
PIN 12 = GND
PIN 24 = +5V ON DEC 8202

REV	DESCRIPTION	DATE
1	INITIAL	1/71

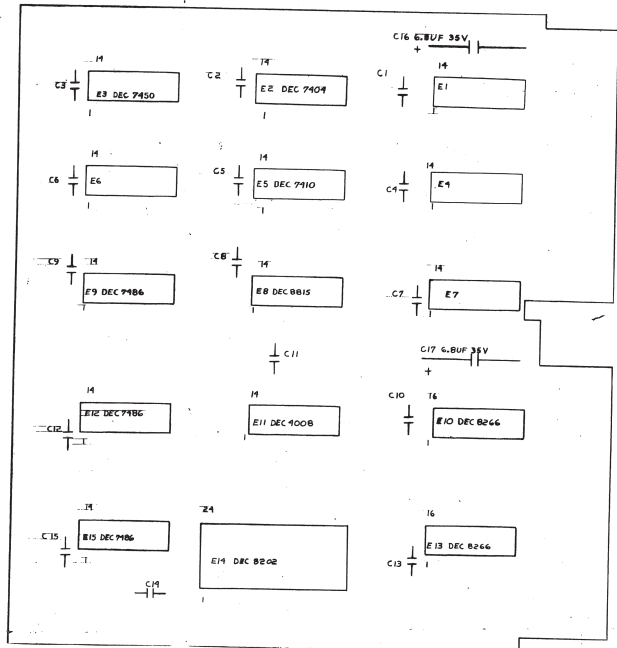
DESIGN	DATE	TRANSISTOR & DIODE CONVERSION CHART
CHRY	1/71	DEC EIA
ENG	1/71	DEC EIA
PROJ	1/71	DEC EIA

TITLE	DATE	NUMBER
CRC AND WRITE GATING TU14	1/71	D CS MB91-0-1

DATE	REV	DESCRIPTION
1/71	D	PRINTED CIRCUIT REV.

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0 168W S3 D
 1310244-0



A/R	DESCRIPTION	QTY.	REF DESIGNATION	DESCRIPTION	DEC PART NO.	TABLE NO.
4	GRIPLET				1210244-0	18
4	HANDLE, FTYELET				9006732	17
2	HANDLE, FLIP CHIP - MAGENTA				9008337-06	16
1	E14			I.C. DEC 8202	1910275	15
1	E11			I.C. DEC 4008	1910270	14
3	E12, 15, 9			I.C. DEC 7486	1910011	13
2	E10, 13			I.C. DEC 8266	1909934	12
1	E8			I.C. DEC 8815	1909713	11
1	E2			I.C. DEC 7404	1909686	10
1	E3			I.C. DEC 7450	1909580	9
1	E5			I.C. DEC 7410	1905576	8
4	E1, 4, 6, 7			I.C. DEC 7400	1905575	7
15	C1 - 15			CAP. .01UF 100V 20%	1001610	6
2	C16, 17			CAP. 6.BUF 35V 20%	1000067	5
1				ETCHED CIRCUIT BOARD	500 9536	4
				SCO MODULE HISTORY	B-MH-M891-0-6	3
				ASSY/DRILLING HOLE LAYOUT	D-AH-M891-0-5	2
				X-Y COORDINATE HOLE LOCATION	K-CO-M891-0-4	1
				DESCRIPTION	DEC PART NO.	TABLE NO.

REVISIONS	CHK	CHK NO	REV	DATE		
				BY	DATE	DATE

TRANSISTOR & DIODE CONVERSION CHART				TITLE	
DEC	EIA	DEC	EIA	CRC	
				AND WRITE GATING TU14	
				digital	
				EQUIPMENT CORPORATION	
				SIZE	NUMBER
				D CS	M891-0-1
				PRINTED CIRCUIT REV.	REV. C
				RAYBURN, MASSACHUSETTS	

DEC FORM NO. 080 118