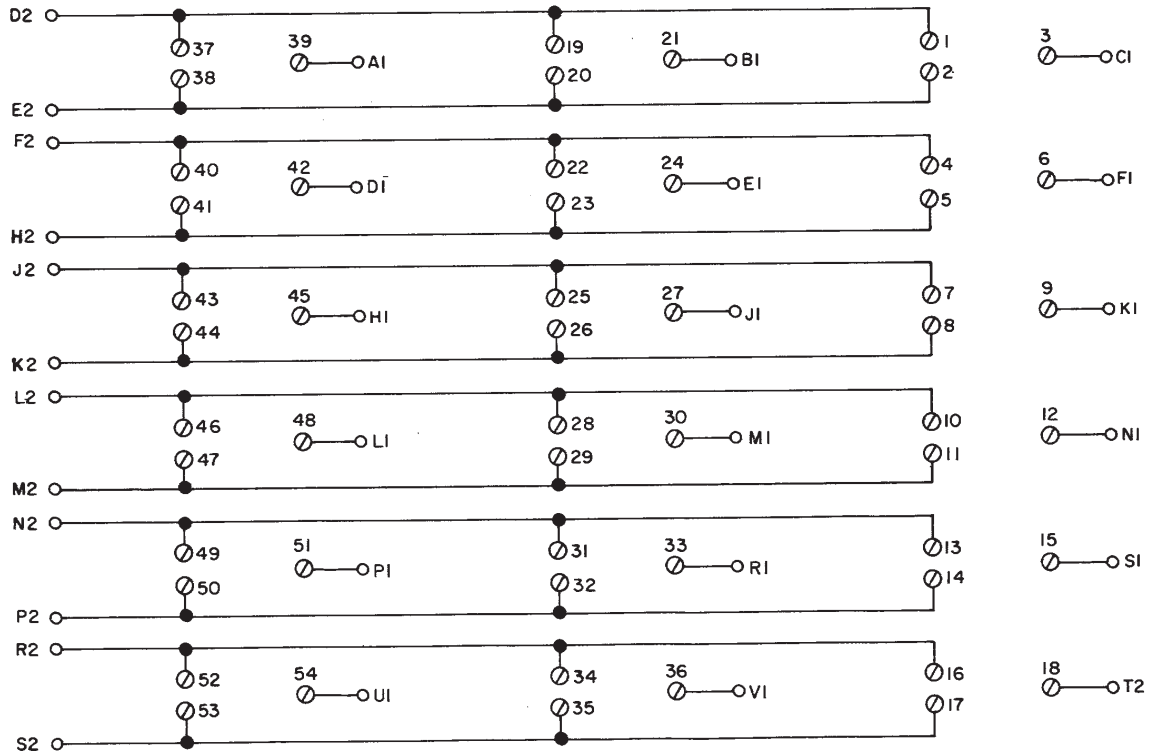


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION

PIN SIGNAL ASSIGNMENTS

OUT			
DEVICE A	DEVICE B	DEVICE C	SIGNAL
AI	BI	CI	MB3
DI	EI	FI	MB4
HI	JI	KI	MB5
LI	MI	NI	MB6
PI	RI	SI	MB7
UI	VI	T2	MB8

IN	
D2	MB3(O)
E2	MB3(I)
F2	MB4(O)
H2	MB4(I)
J2	MB5(O)
K2	MB5(I)
L2	MB6(O)
M2	MB6(I)
N2	MB7(O)
P2	MB7(I)
R2	MB8(O)
S2	MB8(I)



UNLESS OTHERWISE INDICATED:
 ⚡ ARE SPLIT LUGS

REVISIONS CHK CHG NO. REV.	DRN. BUTLER	DATE 1/16/69	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE DEVICE CODE SELECT JUMPER BOARD			
	CHK'D G. V. Paletta	DATE 2/2/69	DEC	EIA	DEC	EIA		SIZE B	CODE CS	NUMBER M921-0-1	REV.
	ENG. G. Dean	DATE 9/23/69						PRINTED CIRCUIT REV. B			
	PROD.	DATE									