



# FLIP CHIP MODULES TEST SPECS

TYPE: R 122<sup>A</sup>  
#001  
FLIP - FLOP

TEST	CONDITION	MAXIMUM	MINIMUM
LOWER LEVEL	$V_{IN} = 0.5 V$	-3.3 V	-3.8 V
LOAD CURRENT	$V_{IN} = 0$	2.2 MA	1.8 MA
$V_{CE}$ (SATURATION)	$I_C = 20 MA$ $V_{IN} = -2.0V$	$\leq 300$ MV	$\leq 300$ MV
INPUT CURRENT		1.1 MA	0.8 MA
TOTAL TRANSITION TIME	RISE	$\leq 70$ NS	$\leq 70$ NS
	FALL	$\leq 300$ NS	$\leq 300$ NS

\* ALL UNUSED INPUTS ARE GROUNDED.

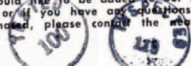
### TECHNICAL INFORMATION

Instruction literature and technical bulletins are available on all digital products, if you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

### MAINTENANCE INFORMATION

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting. Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.

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