

dec

FLIP CHIP MODULES TEST DATA

TYPE: R 601
#001
PULSE AMPLIFIER

DC TESTS		
OUTPUT PIN	D	
LOWER LEVEL	-3. <i>6</i>	V
LOAD CURRENT	<i>2.9</i>	MA

AC TESTS	
UPPER LEVEL	
D	MV
-	

600 Ω LOAD TO -15V.

AC SENSITIVITY TESTS			
INPUT	SENSITIVITY#1	SENSITIVITY#2	
J	+ <i>1.2</i>	+ <i>1.7</i>	V
L	+ <i>1.2</i>	+ <i>1.7</i>	V
N	+ <i>1.2</i>	+ <i>1.7</i>	V
R	+ <i>1.3</i>	+ <i>1.7</i>	V
T	+ <i>1.2</i>	+ <i>1.8</i>	V
V	+ <i>1.2</i>	+ <i>1.7</i>	V

#1. GATE ENABLE GROUNDED

#2. GATE ALTERNATELY ENABLED

GATE DISABLE TEST	
LEVEL INPUT	CHECK
H	✓
K	✓
M	✓
P	✓
S	✓
U	✓

MARGINS			
+10V	+ <i>10</i> V	- <i>10</i> V	
-15V	+ <i>7.5</i> V	- <i>5</i> V	

CHECK FOR NO OPERATION AT THE OUTPUTS
WITH THE ENABLE LEVEL AT -3.0 VOLTS.

TEST	OUTPUT	PULSE WIDTH		NS
		RISE TTT	PULSE WIDTH	
70 NSEC	70 NS	<i>32</i>	<i>85</i>	
INPUT PULSE	400 NS	<i>31</i>	<i>340</i>	
40 NSEC	70 NS	<i>31</i>	<i>85</i>	
INPUT PULSE	400 NS	<i>30</i>	<i>340</i>	
3 LEVEL	70 NS	<i>30</i>	<i>80</i>	
CHANGE INPUT	400 NS	<i>29</i>	<i>340</i>	

JUMPER PIN E TO PIN F TO OBTAIN 400 NS OUTPUT PULSE.

MAINTENANCE INFORMATION

Instruction literature and technical bulletins are available on all digital products, if you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting. Oscilloscopes' used to troubleshoot a module or system should be grounded to prevent damaging transients.

ELEC. TESTER:



DATE: MAY 30 1965

DAW
2/12/65