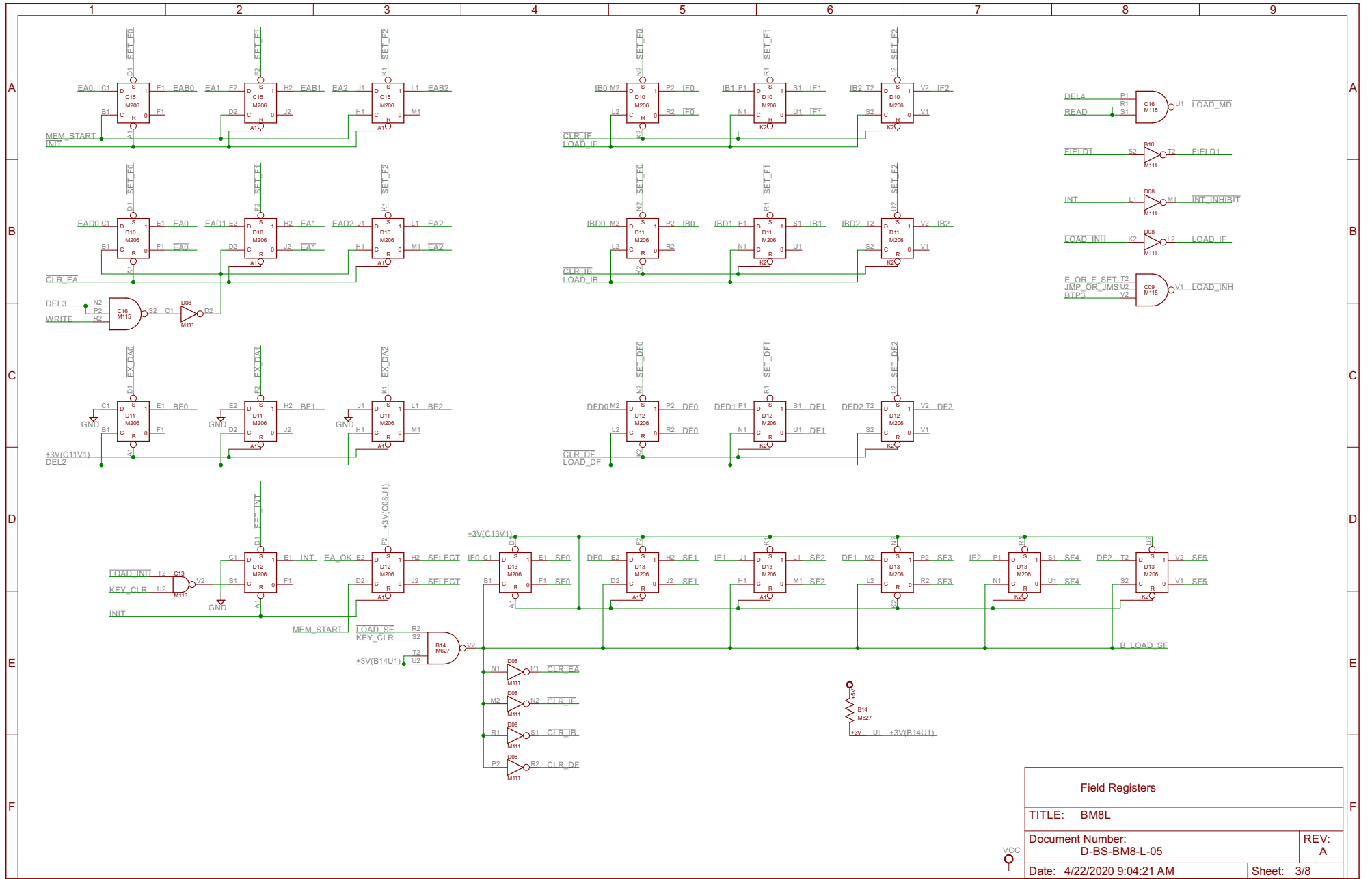


| Control Gating | | |
|-----------------------------------|------------|--|
| TITLE: BM8L | | |
| Document Number: D-BS-BM8-L-04 | REV: A | |
| Date: 4/22/2020 9:04:21 AM | Sheet: 2/8 | |

VCC



| Field Registers | | |
|-----------------------------------|------------|--|
| TITLE: BM8L | | |
| Document Number: D-BS-BM8-L-05 | REV: A | |
| Date: 4/22/2020 9:04:21 AM | Sheet: 3/8 | |

VCC

1 2 3 4 5 6 7 8 9

A

B

C

D

E

F

A

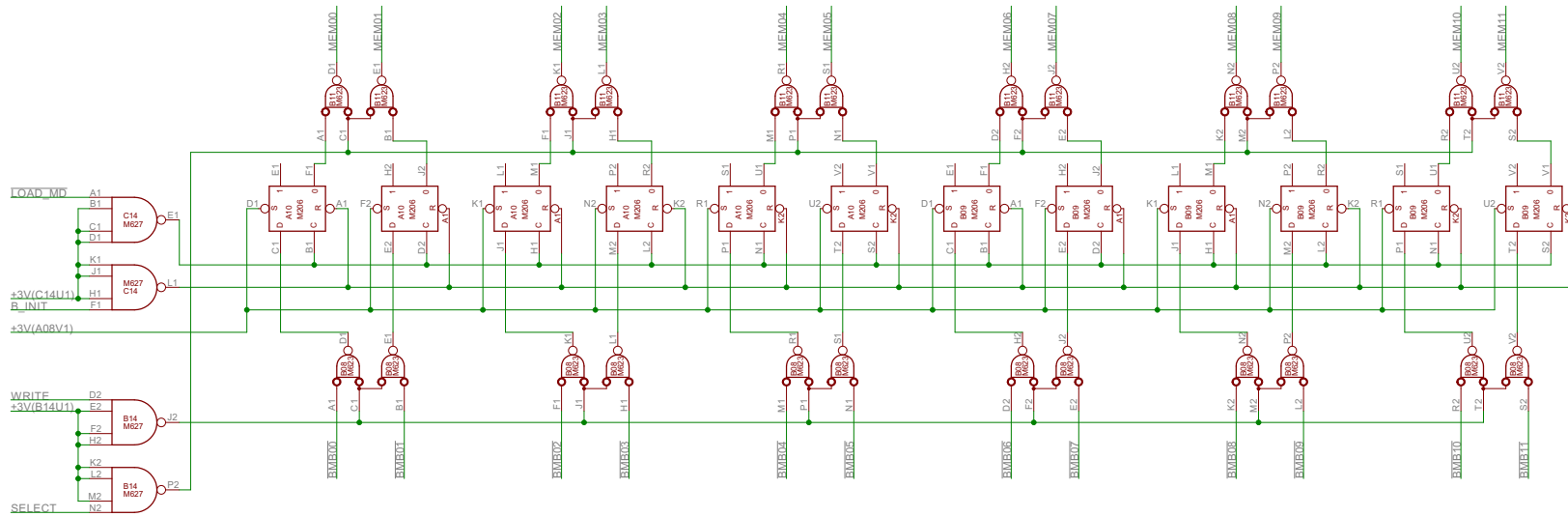
B

C

D

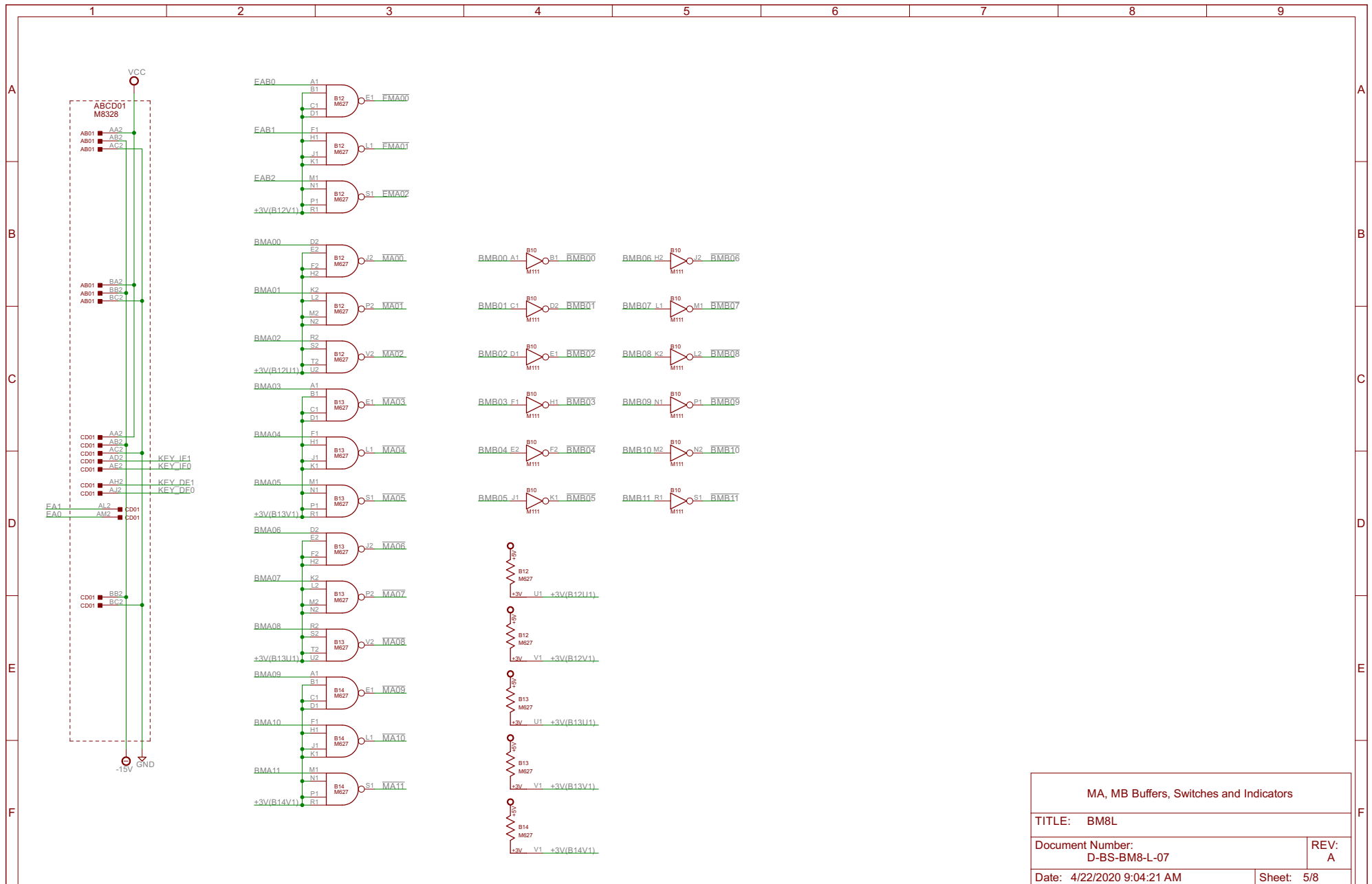
E

F



| | | |
|-----------------------------------|--|------------|
| MD Buffer | | |
| TITLE: BM8L | | |
| Document Number: D-BS-BM8-L-06 | | REV: A |
| Date: 4/22/2020 9:04:21 AM | | Sheet: 4/8 |





| MA, MB Buffers, Switches and Indicators | |
|---|------------|
| TITLE: BM8L | |
| Document Number: D-BS-BM8-L-07 | REV: A |
| Date: 4/22/2020 9:04:21 AM | Sheet: 5/8 |



| | | |
|-----------------------------------|--|------------|
| Expanded Omnibus 8E Memory | | |
| TITLE: BM8L | | |
| Document Number: D-BS-BM8-L-08 | | REV: A |
| Date: 4/22/2020 9:04:21 AM | | Sheet: 6/8 |



| | | | | |
|-----|----|-----|----|-----------|
| C20 | B1 | D00 | B1 | FA |
| C20 | D1 | D00 | D1 | BMA00 |
| C20 | E1 | D00 | E1 | BMA01 |
| C20 | H1 | D00 | H1 | BMA02 |
| C20 | J1 | D00 | J1 | BMA03 |
| C20 | L1 | D00 | L1 | BMA04 |
| C20 | M1 | D00 | M1 | BMA05 |
| C20 | P1 | D00 | P1 | BMA06 |
| C20 | S1 | D00 | S1 | BMA07 |
| C20 | D2 | D00 | D2 | BMA08 |
| C20 | E2 | D00 | E2 | BMA09 |
| C20 | H2 | D00 | H2 | BMA10 |
| C20 | K2 | D00 | K2 | BMA11 |
| C20 | M2 | D00 | M2 | FIELD1 |
| C20 | P2 | D00 | P2 | MEM_START |
| C20 | S2 | D00 | S2 | STROBE |
| C20 | T2 | D00 | T2 | BTP2 |
| C20 | V2 | D00 | V2 | MEM_DONE |

PDP-8L slot B34
MC8-LA slot A29

| | | | | |
|-----|----|-----|----|---------------|
| C19 | B1 | D19 | B1 | MEM_P |
| C19 | D1 | D19 | D1 | MEM00 |
| C19 | E1 | D19 | E1 | MEM01 |
| C19 | H1 | D19 | H1 | MEM02 |
| C19 | J1 | D19 | J1 | MEM03 |
| C19 | L1 | D19 | L1 | MEM04 |
| C19 | M1 | D19 | M1 | MEM05 |
| C19 | P1 | D19 | P1 | MEM06 |
| C19 | S1 | D19 | S1 | MEM07 |
| C19 | D2 | D19 | D2 | MEM08 |
| C19 | E2 | D19 | E2 | MEM09 |
| C19 | H2 | D19 | H2 | MEM10 |
| C19 | K2 | D19 | K2 | MEM11 |
| C19 | M2 | D19 | M2 | MB_PARITY_ODD |
| C19 | P2 | D19 | P2 | |
| C19 | S2 | D19 | S2 | |
| C19 | T2 | D19 | T2 | BEMA |
| C19 | V2 | D19 | V2 | EMA |

PDP-8L slot B35
MC8-LA slot A30

| | | | | |
|-----|----|-----|----|-------------|
| C18 | B1 | D18 | B1 | DF_ENABLE |
| C18 | D1 | D18 | D1 | SP_CYC_NEXT |
| C18 | E1 | D18 | E1 | BF_ENABLE |
| C18 | H1 | D18 | H1 | RTP2 |
| C18 | J1 | D18 | J1 | KEY CLR |
| C18 | L1 | D18 | L1 | INT_INHIBIT |
| C18 | M1 | D18 | M1 | LOAD_SF |
| C18 | P1 | D18 | P1 | KEY_DP2 |
| C18 | S1 | D18 | S1 | KEY_DP2 |
| C18 | D2 | D18 | D2 | F_OR_F_SET |
| C18 | E2 | D18 | E2 | IMP_OR_JMS |
| C18 | H2 | D18 | H2 | KEY_LOAD |
| C18 | K2 | D18 | K2 | |
| C18 | M2 | D18 | M2 | |
| C18 | P2 | D18 | P2 | |
| C18 | S2 | D18 | S2 | STOP_OK |
| C18 | T2 | D18 | T2 | POWER_OK |
| C18 | V2 | D18 | V2 | LINE_LOW |

PDP-8L slot B36
MC8-LA slot A31

A1, C1, F1, K1, N1, R1, and T1 are grounded on all connectors on this page.

Memory Cables to/from MC8-LA

| | | |
|-----------------------------------|------------|-----------|
| TITLE: BM8L | | REV: A |
| Document Number: D-BS-BM8-L-09 | | |
| Date: 4/22/2020 9:04:21 AM | Sheet: 7/8 | |



1 2 3 4 5 6 7 8 9

A
B
C
D
E
F

| | | | | |
|-----|----|-----|----|--------------|
| A16 | B1 | B16 | B1 | BAC00 |
| A16 | D1 | B16 | D1 | BAC01 |
| A16 | E1 | B16 | E1 | BAC02 |
| A16 | H1 | B16 | H1 | BAC03 |
| A16 | J1 | B16 | J1 | BAC04 |
| A16 | L1 | B16 | L1 | BAC05 |
| A16 | M1 | B16 | M1 | BAC06 |
| A16 | P1 | B16 | P1 | BAC07 |
| A16 | S1 | B16 | S1 | BAC08 |
| A16 | D2 | B16 | D2 | BAC09 |
| A16 | E2 | B16 | E2 | BAC10 |
| A16 | H2 | B16 | H2 | BAC11 |
| A16 | K2 | B16 | K2 | BIOP1 |
| A16 | M2 | B16 | M2 | BIOP2 |
| A16 | P2 | B16 | P2 | BIOP4 |
| A16 | S2 | B16 | S2 | BTS3 |
| A16 | T2 | B16 | T2 | BTS1 |
| A16 | V2 | B16 | V2 | R_INITIALIZE |

PDP-8L slot D36
MC8-LA slot A36

| | | | | |
|-----|----|-----|----|-------|
| A17 | B1 | B17 | B1 | BMR00 |
| A17 | D1 | B17 | D1 | BMR01 |
| A17 | E1 | B17 | E1 | BMR02 |
| A17 | H1 | B17 | H1 | BMR03 |
| A17 | J1 | B17 | J1 | BMR04 |
| A17 | L1 | B17 | L1 | BMR05 |
| A17 | M1 | B17 | M1 | BMR06 |
| A17 | P1 | B17 | P1 | BMR05 |
| A17 | S1 | B17 | S1 | BMR05 |
| A17 | D2 | B17 | D2 | BMR06 |
| A17 | E2 | B17 | E2 | BMR06 |
| A17 | H2 | B17 | H2 | BMR07 |
| A17 | K2 | B17 | K2 | BMR07 |
| A17 | M2 | B17 | M2 | BMR08 |
| A17 | P2 | B17 | P2 | BMR08 |
| A17 | S2 | B17 | S2 | BMR09 |
| A17 | T2 | B17 | T2 | BMR10 |
| A17 | V2 | B17 | V2 | BMR11 |

PDP-8L slot D35
MC8-LA slot A35

| | | | | |
|-----|----|-----|----|------------|
| A18 | B1 | B18 | B1 | AC00 BUS |
| A18 | D1 | B18 | D1 | AC01 BUS |
| A18 | E1 | B18 | E1 | AC02 BUS |
| A18 | H1 | B18 | H1 | AC03 BUS |
| A18 | J1 | B18 | J1 | AC04 BUS |
| A18 | L1 | B18 | L1 | AC05 BUS |
| A18 | M1 | B18 | M1 | AC05 BUS |
| A18 | P1 | B18 | P1 | AC07 BUS |
| A18 | S1 | B18 | S1 | AC08 BUS |
| A18 | D2 | B18 | D2 | AC09 BUS |
| A18 | E2 | B18 | E2 | AC10 BUS |
| A18 | H2 | B18 | H2 | AC11 BUS |
| A18 | K2 | B18 | K2 | SKIP BUS |
| A18 | M2 | B18 | M2 | IRO BUS |
| A18 | P2 | B18 | P2 | AC CLR BUS |
| A18 | S2 | B18 | S2 | B RUN |
| A18 | T2 | B18 | T2 | |
| A18 | V2 | B18 | V2 | |

PDP-8L slot D34
MC8-LA slot A34

| | | | | |
|-----|----|-----|----|--------------|
| A19 | B1 | B19 | B1 | DA00 |
| A19 | D1 | B19 | D1 | DA01 |
| A19 | E1 | B19 | E1 | DA02 |
| A19 | H1 | B19 | H1 | DA03 |
| A19 | J1 | B19 | J1 | DA04 |
| A19 | L1 | B19 | L1 | DA05 |
| A19 | M1 | B19 | M1 | DA06 |
| A19 | P1 | B19 | P1 | DA07 |
| A19 | S1 | B19 | S1 | DA08 |
| A19 | D2 | B19 | D2 | DA09 |
| A19 | E2 | B19 | E2 | DA10 |
| A19 | H2 | B19 | H2 | DA11 |
| A19 | K2 | B19 | K2 | BRO |
| A19 | M2 | B19 | M2 | DATA IN |
| A19 | P2 | B19 | P2 | BREAK |
| A19 | S2 | B19 | S2 | ADD ACCEPTED |
| A19 | T2 | B19 | T2 | MB INCR |
| A19 | V2 | B19 | V2 | R INIT |

PDP-8L slot C36
MC8-LA slot A33

| | | | | |
|-----|----|-----|----|-----------|
| A20 | B1 | B20 | B1 | DATA00 |
| A20 | D1 | B20 | D1 | DATA01 |
| A20 | E1 | B20 | E1 | DATA02 |
| A20 | H1 | B20 | H1 | DATA03 |
| A20 | J1 | B20 | J1 | DATA04 |
| A20 | L1 | B20 | L1 | DATA05 |
| A20 | M1 | B20 | M1 | DATA06 |
| A20 | P1 | B20 | P1 | DATA07 |
| A20 | S1 | B20 | S1 | DATA08 |
| A20 | D2 | B20 | D2 | DATA09 |
| A20 | E2 | B20 | E2 | DATA10 |
| A20 | H2 | B20 | H2 | DATA11 |
| A20 | K2 | B20 | K2 | 3CYCLE |
| A20 | M2 | B20 | M2 | CA INCR |
| A20 | P2 | B20 | P2 | WIC OUF L |
| A20 | S2 | B20 | S2 | EX DAT |
| A20 | T2 | B20 | T2 | EX DAT |
| A20 | V2 | B20 | V2 | EX DAT |

PDP-8L slot C35
MC8-LA slot A32

A1, C1, F1, K1, N1, R1, and T1 are grounded on all connectors on this page.



| I/O Connectors, Positive Bus | |
|-----------------------------------|------------|
| TITLE: BM8L | |
| Document Number: D-BS-BM8-L-10 | REV: A |
| Date: 4/22/2020 9:04:21 AM | Sheet: 8/8 |