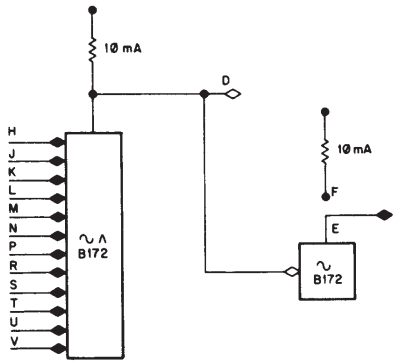
B172 DIODE GATE

Standard Size FLIP CHIP Module, 18 Pins



The B172 provides a 12-input 2 mA diode gate with 2 mA diode inverter permanently connected to the output of the first gate. It is often used for memory module selection on the PDP-10 memory bus.

INPUTS: The input load of the diode gates is 2 mA shared among the inputs which are at ground. Each diode input at ground represents 12 pF of capacitance. The input load at -3 V is less than 1 μA plus 5 pF of capacitance at each input provided at least one input is at ground.

OUTPUTS: Pin D - This output can drive up to seven other 2 mA diode gates (14 mA at ground). The internal clamped load will supply -7 mA at -3 V.

Pin E - This output can supply 26 mA at ground. The output capacitance is 3 pF. A 10 mA clamped load is available at pin F.

Typical propagation time through the first gate is 15 ns; through both gates, 25 ns. Rise TTT from input is 25 ns to pin D, 40 ns to pin E. Fall TTT from input is 40 ns to pin D, 35 ns to pin E. The output load for pin E is the clamped load at pin F for these measurements.

POWER:

Pin	Voltage	Margin Range	Current
A	+10 V	0 V to 20 V	.3 mA
B	-15 V	-10 V to -20 V	24 mA
C	ground		