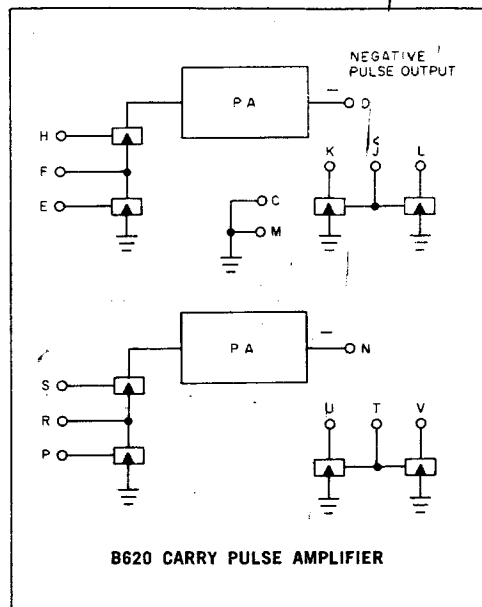


CARRY PULSE AMPLIFIER TYPE B620

**B
SERIES**



Module B620 supplements the B201 for 10-mc counting applications. It supplies the circuitry to complement two B201 Flip-Flops and propagate their carry pulses. One B620 and one B201 can also be combined to form one bit of an up-down counter. The B620 contains two pairs of inverters for complementing 10-mc flip-flops with conditional set and clear inputs, and two standardizing pulse amplifiers each capable of driving three inverter bases. The propagation delay is approximately 10 nsec. Maximum pulse repetition frequency is 5 mc for pulse amplifiers.

INPUT: The eight inverters of the B620 are similar to other 10-mc inverters, for example, the inverters

of the B104 and B105. To obtain minimum propagation delay, input H or S should be pulsed. Use inputs E, F, P, and R for gating inputs. Pulse amplifiers will not produce standard pulse outputs unless a standard 40-nsec pulse input is used.

OUTPUT: When 40-nsec pulses are used at their inputs, the pulse amplifiers produce 2.5-v, 40-nsec standard negative pulses capable of driving one to three inverter bases. Maximum length of wire used to connect pulse amplifiers to the inverters is 6 in. Noise pulses may occur if the ground pin next to each output pin is not connected directly to the local ground of the inverters that the output drives.

POWER: +10 v(A)/6 ma; -15 v(B)/20 ma.