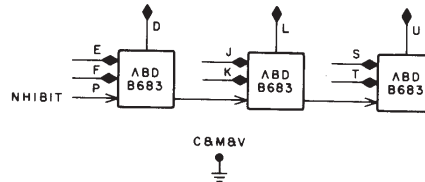


## B683 BUS DRIVER

Standard Size FLIP CHIP Module, 18 Pins



The B683 is the level counterpart of the W102 pulsed bus transceiver. It is designed to drive two 100  $\Omega$  transmission lines terminated to ground in their characteristic impedance. A single, terminated 50  $\Omega$  transmission can be driven instead. The output impedance of the B683 is zero when the output is low (-3 V) and infinite when high (ground) or when the power is off. Thus, two or more of these drivers may be attached to the same line in a "wired OR" fashion.

**INPUTS:** The input is that of a two input 2 mA diode AND gate. When both inputs are low, the output is also low. The input load is 2 mA shared by those inputs at ground, 0 for those inputs at -3 V.

An inhibit input is provided on pin P. If this input is held at ground (for example, by the CROBAR relay contact on the 844 Power Control possibly through an isolating diode) power-on, power-off transients are prevented from causing the outputs to go negative. This input normally is at -5.3 V and must not be tied to any other potential if normal operation is expected. Load is 30 mA at ground.

**OUTPUTS:** The outputs will supply -60 mA to a 50  $\Omega$  load at -3 V. The output circuit will tolerate short circuit operation for a short time (tens of seconds) but this type of operation is not recommended. Typical TTT is 30 ns.

### POWER:

| Pin     | Voltage | Margin Range   | Current |
|---------|---------|----------------|---------|
| A       | +10 V   | 0 V to 20 V    | .4 mA   |
| B       | -15 V   | -12 V to -18 V | 350 mA  |
| C, M, V | ground  |                |         |

Pins C, M, and V must all be grounded.