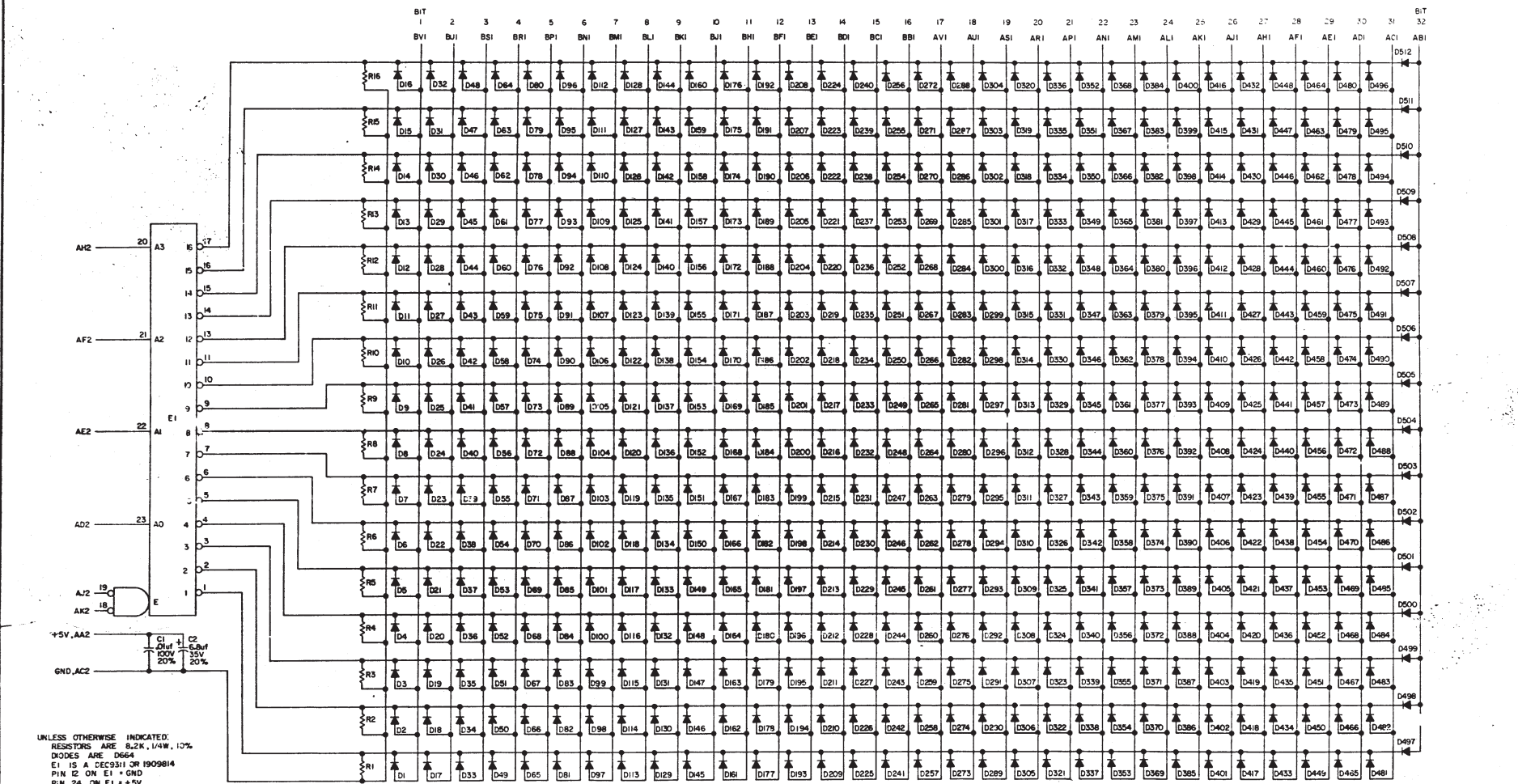


THIS SCHEMATIC IS SHOWN ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT, 1970 BY DIGITAL EQUIPMENT CORPORATION.

V 100P9D 50 0
+12 +100V 20%



UNLESS OTHERWISE INDICATED:
RESISTORS ARE 8.2K, 1/4W, 10%
DIODES ARE D664
E1 IS A DEC311 OR 1909814
PIN 12 ON E1 = GND
PIN 24 ON E1 = +5V

REV	1	DATE	10/1/70	TRANSISTOR & DIODE CONVERSION CHART		TITLE	ROM DIODE MATRIX AND DECODE G618
	2	DATE	10/1/70				SIZE D CS NUMBER G618-0-1 REV A
DESIGNED BY	ALLAN RITCEY	DATE	10/1/70	DEC 100V 20% DEC 35V 20%	CORPORATION PRINTED CIRCUIT REV B		

NUMBER G618-0-1