

PROCESSOR TYPE PDP-8/E

M8340-00001 CODE: D CS: D

JULY-71 – PROBLEM 1: E04 Pins 10 and 11 interchanged.

CORRECTION 1: Delete etch from E04 Pin 11; reconnect etch to E04 Pin 10.

PROBLEM 2: E30 Pins 10 and 13 interchanged.

CORRECTION 2: Delete existing etch connections on E30 Pins 10 and 13 and reconnect etch to E30 Pins 10 and 13 exactly opposite from original.

PROBLEM 3: Data bus inputs, DATA 08 thru DATA 11 are 180 degrees misplaced.

CORRECTION 3: DELETE DR1 to E32 Pin 9, DS1 to E32 Pin 11, DU1 to E32 Pin 7, and DV1 to E32 Pin 5. ADD DR1 to E32 Pin 5, DS1 to E32 Pin 7, DU1 to E32 Pin 11, and DV1 to E32 Pin 9.

PROBLEM 4: IC's are not numbered.

CORRECTION 4: Add etched numbers to layout wherever possible.

PROBLEM 5: Handle holes improperly located.

CORRECTION 5: Relocate handle holes to correct positions

NOTE 1: See continuation supplement ECO's M8340-00002 and M8340-00003.

NOTE 2: ECO M8340-00003 cancels the relayout to new etch revision "E" which is ordered by this ECO.

In-plant effectivity – 03 rework immediately.

M8340-00002 CODE: D

JULY-71 – PROBLEM: Timing considerations involving the signal MD DIRECTION on single Omnibus systems, inhibit step counter bits 3 and 4 from being loaded with the SCL instruction, STEP COUNTER LOAD FROM MEMORY.

CORRECTION: DELETE etch connection E20 Pin 07; ADD HF2 to E20 Pin 07.

NOTE: This ECO is a supplement to ECO M8340-00001.

In-plant effectivity – 04 rework immediately

M8340-00003 CODE: P

NOVEMBER-71 – PROBLEM: ECO's M8340-00001 and M8340-00002 ordered relayout of etch #50-09603 to revision "E". Since the board is still at a Limited Release level we do not wish to do this.

CORRECTION: Cancel relayout of etch and revise Module History to show that etch is remaining at revision "D"; rework is to continue as ordered by previous ECO's.

In-plant effectivity – 06 documentation change only

M8340-00004 CODE: D CS: E ETCH: E

NOVEMBER-71 – PROBLEM: Module does not meet production specifications, IC DEC380, input buffer to instruction register, may cause an erroneous EAE instruction to be decoded on a four Omnibus system, due to high threshold value on DEC380 input, and slow charge time on the bus.

CORRECTION: Create new etch revision "E" and reconfigure EAE instruction register.

In-plant effectivity – 01 phase-in

M8340-C0005 CODE: F CS: F

MARCH-72 – PROBLEM: IC E4, a 7476 JK flip-flop, is not edge triggered and may cause an illegal mode swap.

CORRECTION: Replace IC E4 with a pin compatible, edge triggered JK flip-flop 74H106, DEC #19-10408.

In-plant effectivity – rework immediately

Field effectivity – rework M8340's in KE8-E's if symptoms are present

(Time To Install And Test 1.0 Hour) (Kit Contents – FCO/Prints and Parts)

M8340-00006 CODE: D

MAY-72 – PROBLEM: M8340 etch revision "D" and earlier are not compatible with the M8341 etch revision "D" now being shipped.

CORRECTION: Stop building and shipment of all M8340 revision "D" and earlier; ship only M8340 etch revision "E", CS revision "D".

NOTE: See continuation supplement M8340-00006A.

In-plant effectivity – implement immediately

M8340-00006A CODE: D

MAY-72 – PROBLEM: ECO M8340-00006 did not include some parts added to and deleted from the Parts List.

CORRECTION: Correct Parts List accordingly.

In-plant effectivity – unchanged