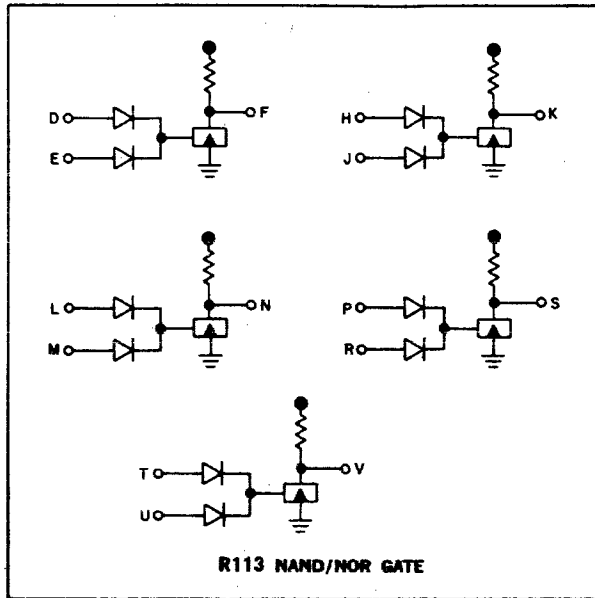


# NAND/NOR GATE TYPE R113

**R  
SERIES**



The R113 contains five diode gates, each connected to a transistor inverter. The gate operates as a NAND for negative inputs, and as a NOR for ground levels.

**INPUTS:** Standard levels of  $-3v$  and ground, 100-nsec minimum duration. Input load is 1 ma, shared among the inputs at ground. Unused inputs may be left open.

**OUTPUT:** Standard levels of  $-3v$  and ground. Each output can drive 18 ma of load at ground. Output terminals may be connected in parallel. Clamped

loads included in the module are 2 ma each. Some typical propagation delays are shown below. High frequency logic designs may benefit from the application note "Estimating Propagation Delays."

Fan-out	4	10	16
Output Rise	30 nsec	35 nsec	40 nsec
Output Fall	60 nsec	100 nsec	140 nsec

**POWER REQUIREMENTS:** +10V (A) 0.5 ma. -15V (B) 23 ma.

R113 — \$20.00