



# FLIP CHIP MODULES TEST SPECS

TYPE: R 121  
NAND GATE  
#002

TEST	CONDITION	MAXIMUM	MINIMUM
LOWER LEVEL	$V_{IN} = 0.5 \text{ V}$	3.9 V	3.2 V
LOAD CURRENT	$V_{IN} = 0.5 \text{ V}$	2.2 MA	1.8 MA
$V_{CE}$ (SATURATION)	$I_C = 20 \text{ MA}$ $V_{IN} = -2.0 \text{ V}$	$\approx 300 \text{ MV}$	
INPUT CURRENT	/ / / / / / / /	1.1 MA	0.8 MA
TOTAL TRANSITION TIME	RISE	$\approx 70 \text{ NS}$	
	FALL	$\approx 200 \text{ NS}$	
* ALL UNUSED INPUTS ARE BROUGHT TO -3.0V			

### TECHNICAL INFORMATION

Instruction literature and technical bulletins are available on all digital products. If you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

### MAINTENANCE INFORMATION

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting. Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.

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