



FLIP CHIP MODULES TEST SPECS

TYPE: R 141
DIODE GATE

TEST	CONDITIONS	MAXIMUM	MINIMUM
V SATURATION	20MA LOAD TO -15 VOLTS	≤ 300 MV	0 MV
I LEAKAGE	LEAKAGE MEASURED TO -4 VOLTS	≤ 10 μA	0 μA
TTT	RISE	≤ 70 NS	0 NS
	FALL	≤ 400 NS	0 NS

TECHNICAL INFORMATION

Instruction literature and technical bulletins are available on all digital products. If you would like to be added to our mailing list for this type of material or if you have any questions about the equipment you have purchased, please contact the nearest Digital Sales Office.

MAINTENANCE INFORMATION 12/8/66

Repair of printed circuitry should be done with a low voltage, fairly cool soldering iron to prevent damage to the transistors and keep the copper from lifting. Oscilloscopes used to troubleshoot a module or system should be grounded to prevent damaging transients.

12/1/64 DAW