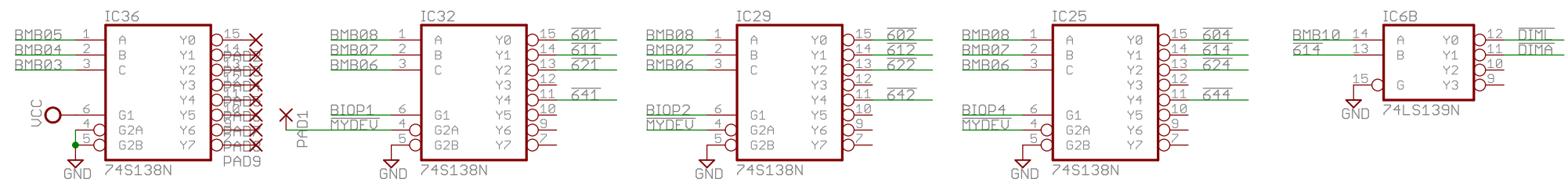
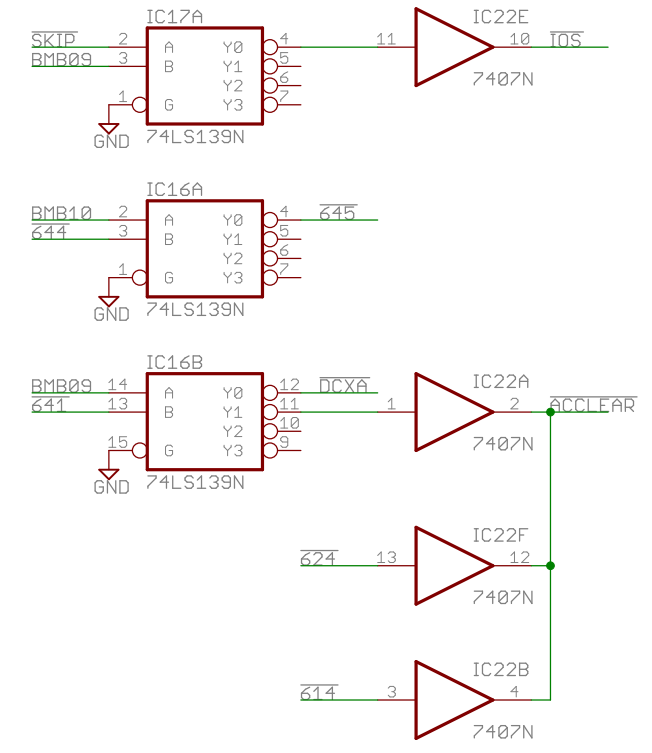
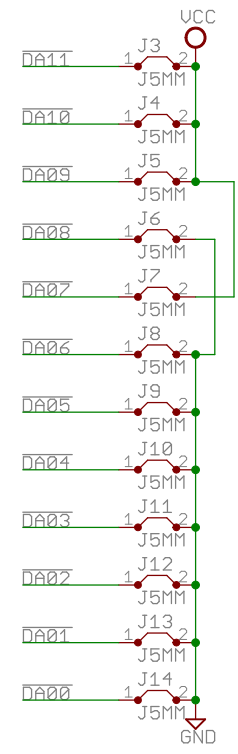


Posibus Connections

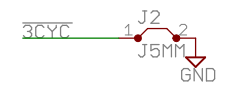
- 6601 DCMA - Use 601\
- 6603 DMAR - Use 602\
- 6605 DMAX - Use 604\
- X 6611 DCIM - Use 611\
- X 6612 DSAC - Use 612\, but all skips are skip conditional with MB9\
- X 6615 DIML - Use DIML\ (614\ with MB10\)
- X 6616 DIMA - Use DIMA\ (614\ with MB10\)
- X 6621 DFSE - Use 621\
- X 6622 DFSC - Use 622\
- X 6623 DISK - Handled above.
- X 6626 DMAC - Use 624\
- X 6641 DCXA - Use 641, MB9 chooses DAR clear vs AC clear
- X 6643 DXAL - Use 642
- X 6645 DXAC - Use 644, unless MB10
- X 6646 DMMT - Handled above.



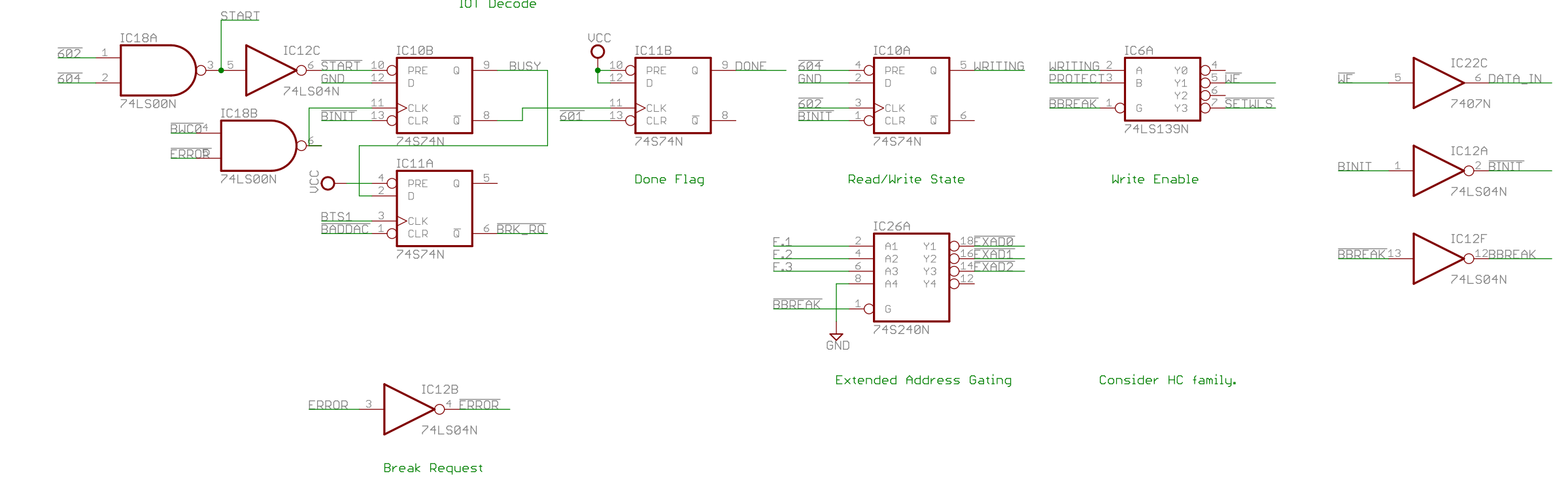
IOT Decode



Hard-Wired 7750



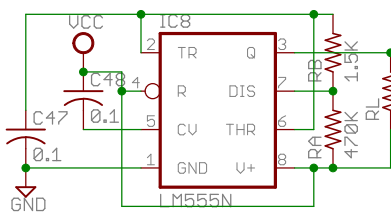
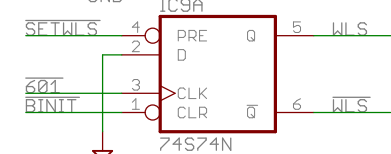
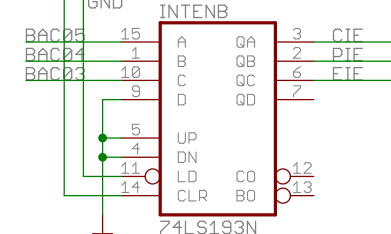
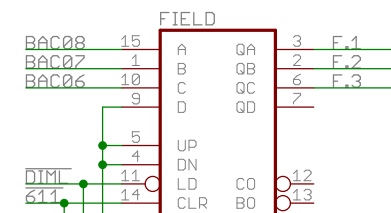
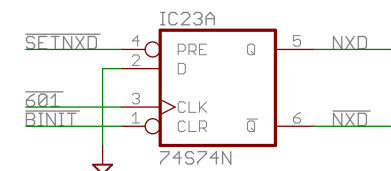
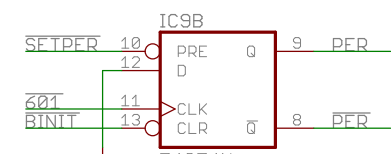
Hard-Wired 3-Cycle Data Break



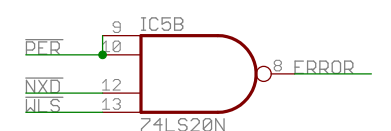
CA_INCR is left open, as no increment inhibit is desired.
MEM_INC\ is left open, as no increment is desired.

Extended Address Gating Consider HC family.

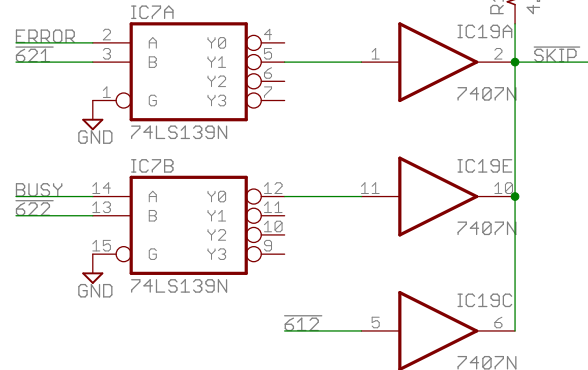
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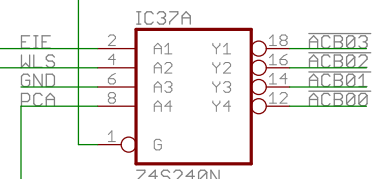
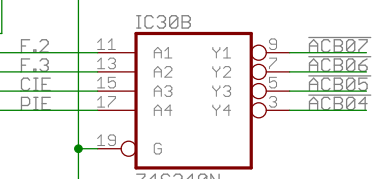
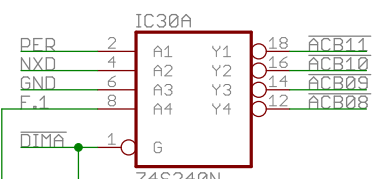
Status Register



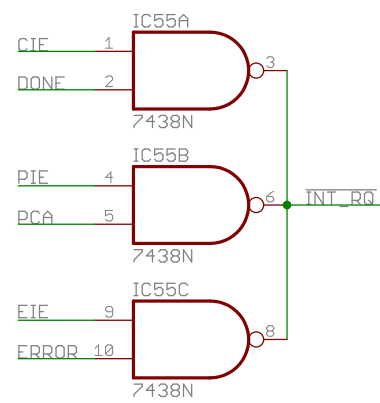
Error "flag"



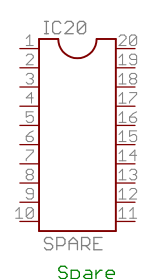
Skip Request



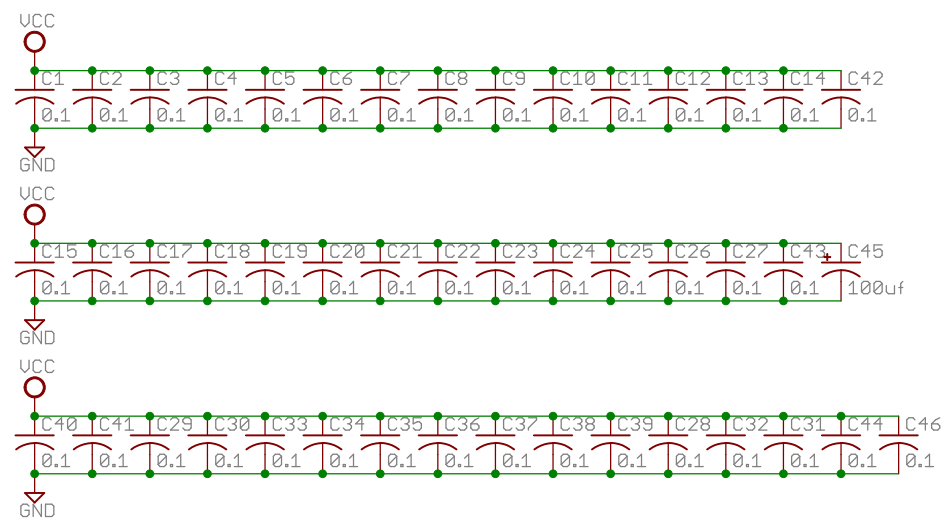
Status Register Gating



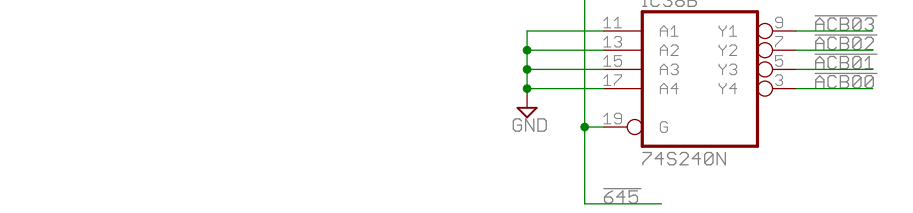
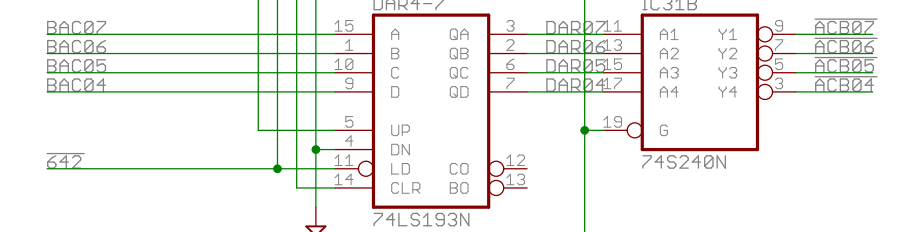
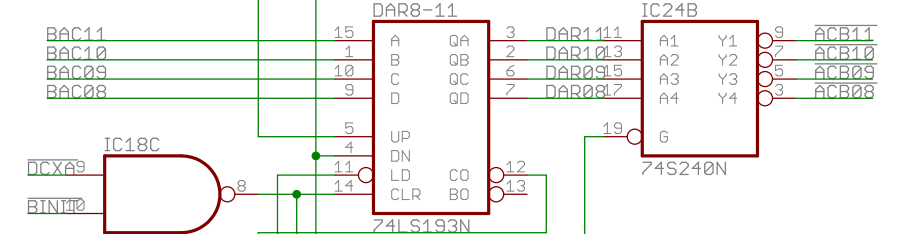
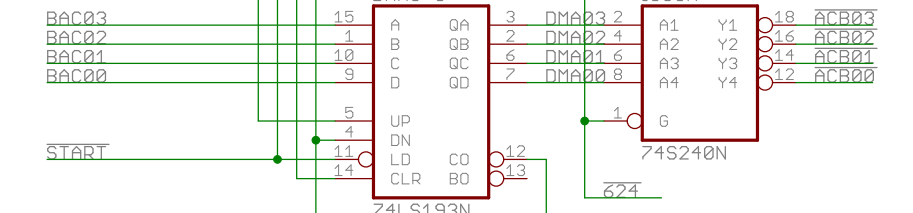
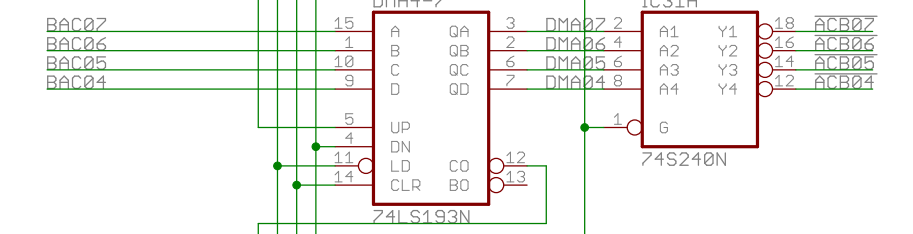
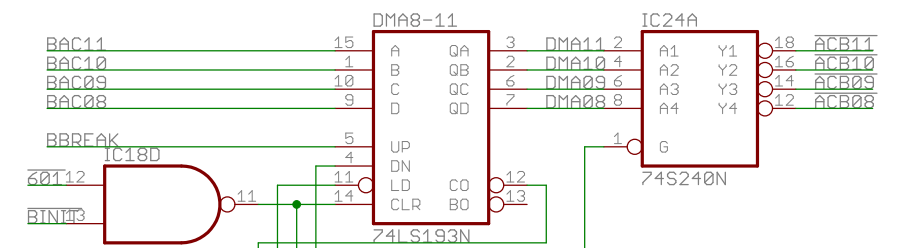
Interrupt Request



Spare



Bypass Capacitors



Disk Memory Address Registers

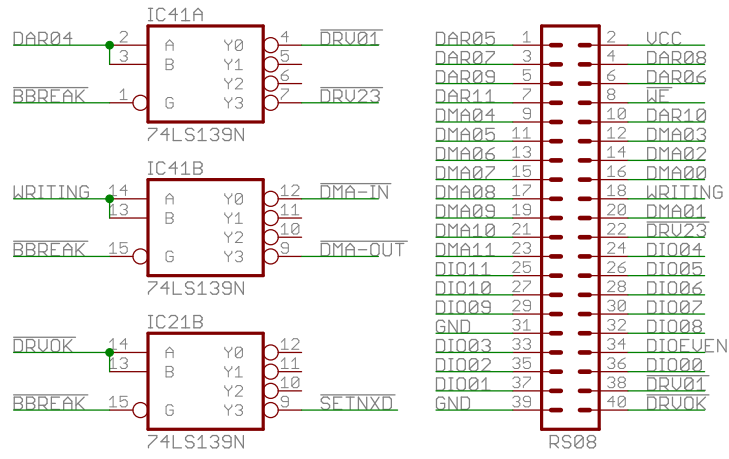
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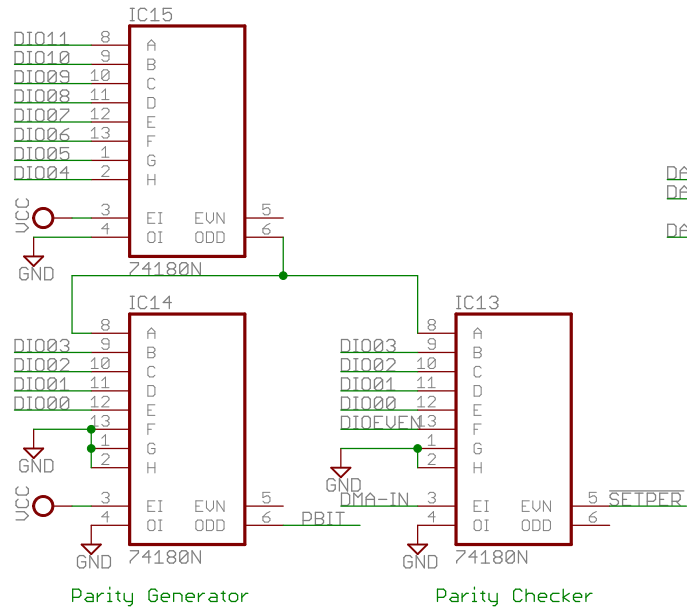
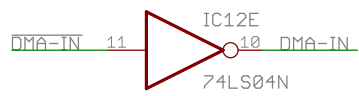
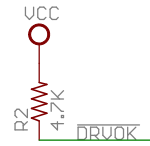
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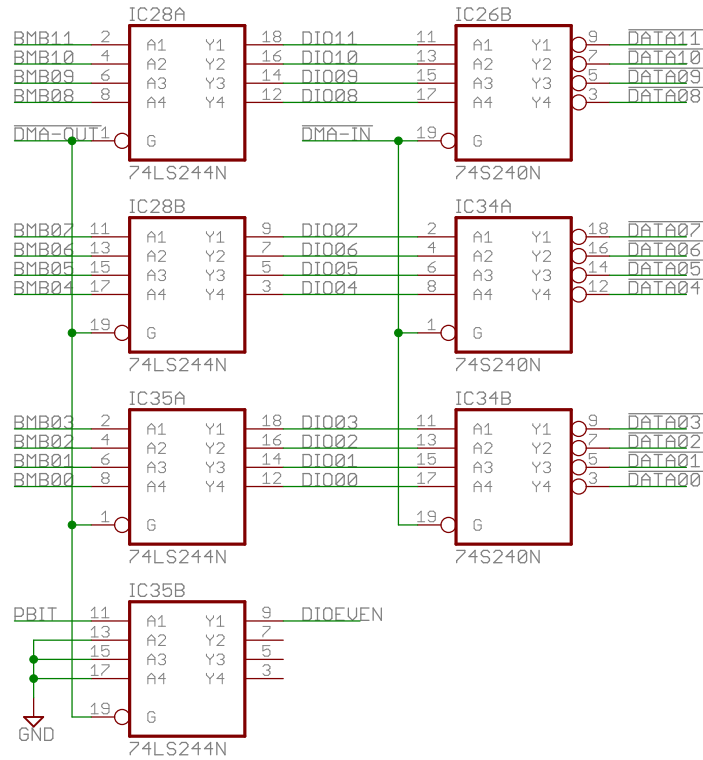


Data Storage

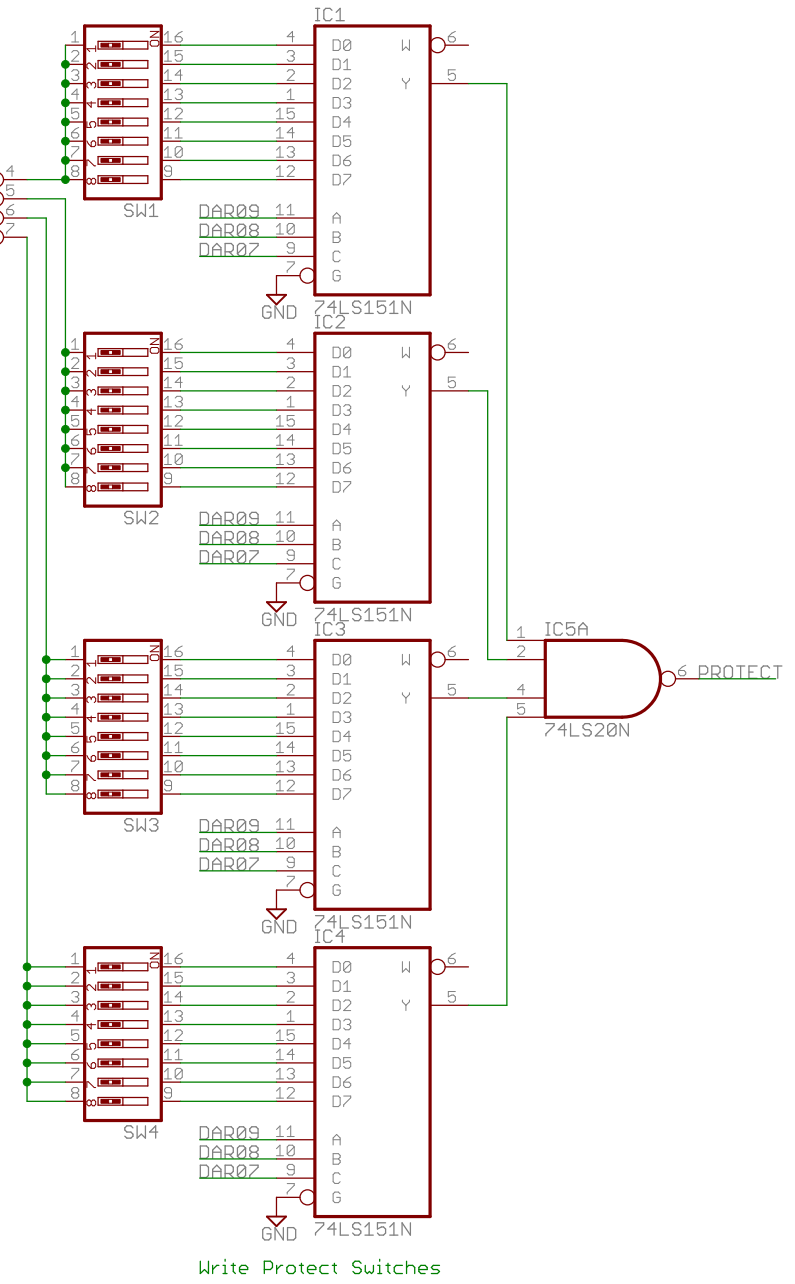
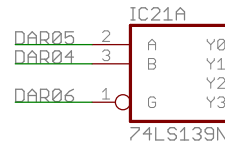


Parity Generator

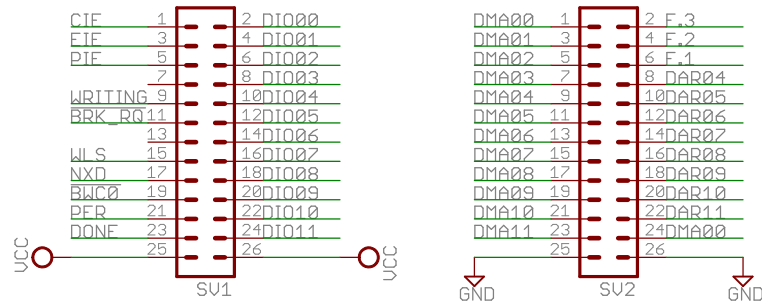
Parity Checker



Memory Data Gating



Write Protect Switches



Indicator Connectors

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