

SNS: 0=1
RSW: 0001
B-MODE: NO PRESET START 20

KF12B

39 SECONDS / MIN

IDENTIFICATION
MAINDEC-12-DBSA-D
KF12B (AUTOMATIC PRIORITY INTERRUPT)
4-5-71
DIAGNOSTIC GROUP
WALTER MANTER

PRODUCT CODE I
PRODUCT NAME I
DATE CREATED I
MAINTAINER I
AUTHOR I

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/1. ABC RACT

THE AUTOMATIC PRIORITY INTERRUPT SYSTEM (KF12B VERSION) DIAGNOSTIC IS DESIGNED TO TEST ALL ASSOCIATED IOT'S, INSTRUCTIONS AND SIMULATE AUTOMATIC PRIORITY INTERRUPTS (A.P.I.) VIA MAINTENANCE IOT'S. THIS PROGRAM REQUIRES NO TELETYPE COMMUNICATIONS AS ERROR HALTS ARE USED EXCLUSIVELY. THE ASSUMPTION IS MADE THAT THE PDP-12 USED IN CONJUNCTION WITH THE API SYSTEM IS A SOLID, ERROR FREE MACHINE.

/2. REQUIREMENTS

/2.1 EQUIPMENT

1. A PDP-12 WITH THE KF12B AUTOMATIC PRIORITY INTERRUPT OPTION.
2. AN ASR-33 TELETYPE OR EQUIVALENT

/2.2 STORAGE

THIS PROGRAM IS DESIGNED TO RUN IN MEMORY BANK 0 AND IT OCCUPIES VIRTUALLY ALL BANK 0. WITH SENSE SWITCH 0 DEPRESSED THE EXTENDED MEMORY ADDRESSING PORTION OF THE DIAGNOSTIC IS ENABLED TESTING AS MANY MEMORY FIELDS AS ARE SPECIFIED BY RIGHT SWITCHES 9-11.

/2.3 PRELIMINARY PROGRAMS

ALL PDP-12 BASIC INSTRUCTION DIAGNOSTICS AND EXERCISERS MUST HAVE BEEN SUCCESSFULLY RUN PRIOR TO RUNNING THE PROGRAM.

/3. LOADING PROCEDURE

/3.1 METHOD

THIS PROGRAM MUST BE LOADED WITH THE BINARY LOADER, IF YOU ARE UNFAMILIAR WITH THE PROPER BINARY LOADING PROCEDURES REFER TO APPENDIX A OF THIS DOCUMENT, OTHERWISE PROCEED WITH THE FOLLOWING:

- A. SET THE TELETYPE READER SWITCH TO FREE.
- B. OPEN THE TELETYPE READER AND INSERT THE PROGRAM TAPE SO THAT THE ARROWS ON THE TAPE ARE VISIBLE TO AND POINTING TOWARD THE OPERATOR.
- C. CLOSE THE READER AND SET THE READER SWITCH TO START.
- D. SET THE TELETYPE FRONT PANEL SWITCH TO START.
- E. SET THE LEFT SWITCHES TO 7777.
- F. SET THE RIGHT SWITCHES TO 4000.

- G. SET THE MODE SWITCH TO 8 MODE.
- H. DEPRESS I/O PRESET.
- I. DEPRESS START LS.
- J. WHEN THE PROGRAM TAPE HAS BEEN READ IN THE ACCUMULATOR MUST BE 0000, IF IT IS NOT, A READ-IN ERROR HAS OCCURRED AND ONE MIGHT TRY RELOADING THE BINARY LOADER. SEE APPENDIX A.
- K. REMOVE THE PROGRAM TAPE FROM THE READER.

/4. STARTING PROCEDURE

THIS PRELIMINARY SET UP PROCEDURE IS CRITICAL AND ANY OMISSION WILL RESULT IN AN ERROR.

1. SET SENSE SWITCH 0 IF YOU DESIRE TO UTILIZE THE EXTENDED MEMORY ADDRESSING FEATURE OF THE PROGRAM.
2. SET THE RIGHT SWITCH REGISTER SWITCHES 9 TO 11 EQUAL TO THE NUMBER OF EXTENDED MEMORY FIELDS TO BE TESTED. (NOTE: WITH SNS SW 0 SET AND RSW=0000 THE PROGRAM HALTS).
3. SET THE MODE SWITCH TO 8-MODE.

4. DEPRESS I/O PRESET
5. DEPRESS START 20

THE PROGRAM IS RUNNING.

CONTROL SWITCH SETTINGS

SENSE SWITCH 0 ALLOWS THE PROGRAM TO TEST SEQUENTIALLY AS MANY EXTENDED 4K MEMORY BANKS AS ARE SPECIFIED BY THE RIGHT SWITCH REGISTER BITS 9-11.

FOR EXAMPLE WITH SNS 0=11
 RSW=XXX1--TEST EXT MEM FIELD 1
 RSW=XXX2--TEST EXT MEM FIELD 1 AND 2
 ETC
 RSW=XXX7--TEST EXT MEM FIELD 1, 2, 3, 4, 5, 6 AND 7

IF SNS 0=1 AND RSW=XXX0 THE PROGRAM WILL HALT AND ALLOW THE OPERATOR TO SET THE RIGHT SWITCH REGISTER BITS 9-11 TO THE DESIRED NUMBER OF EXTENDED MEMORY FIELDS. KEY CONTINUE WILL TRY TO RESTART THE EXTENDED MEMORY TEST.

NOTE: IT IS VITAL TO A COMPLETE TEST OF THE KW128 AUTOMATIC PRIORITY INTERRUPT SYSTEM TO TEST AT LEAST 1 EXTENDED MEMORY FIELD.

MESSAGE FORMAT

1. THERE ARE NO ERROR TYPEOUTS IN THE PROGRAM, THE DIAGNOSTIC'S OF THE FORM OF A BASIC INSTRUCTION TEST AND ERROR HALTS HAVE BEEN USED EXCLUSIVELY WITH A WELL DOCUMENTED TESTING.

About 1 min/pass
Bell rings every 24 seconds when testing only field 0,
every 50 seconds when testing fields 0-1

6. MAINTENANCE INSTRUCTIONS

THERE ARE TWO MAINTENANCE IOT'S USED IN THE PROGRAM,

1. MAIN1=6051

THIS IOT USED IN CONJUNCTION WITH THE CONTENTS OF THE AC SIMULATES AN AUTOMATIC PRIORITY INTERRUPT TO THE HIGHEST PRIORITY LEVEL SPECIFIED BY THE AC.

FOR EXAMPLE WITH THE MACHINE LEVEL SET TO ALLOW ALL LEVELS OF INTERRUPTS:

```
AC=0000 AND MAIN1 IOT--NO INTERRUPT
AC=4XXX AND MAIN1 IOT--LEV0 INTERRUPT
AC=7XXX AND MAIN1 IOT--LEV0 INTERRUPT
AC=3XXX AND MAIN1 IOT--LEV1 INTERRUPT
AC=1XXX AND MAIN1 IOT--LEV2 INTERRUPT
      ETC
AC=0001 AND MAIN1 IOT--LEV11 INTERRUPT
```

2. MAIN2=6052

THIS IOT USED IN CONJUNCTION WITH THE CONTENTS OF THE AC BITS 9-11 SIMULATES AN AUTOMATIC PRIORITY INTERRUPT TO THE HIGHEST PRIORITY LEVEL SPECIFIED BY THE AC.

FOR EXAMPLE WITH THE MACHINE LEVEL SET TO ALLOW ALL LEVELS OF INTERRUPTS:

```
AC=XXX4 AND MAIN2 IOT--LEV12 INTERRUPT
AC=XXX7 AND MAIN2 IOT--LEV12 INTERRUPT
AC=XXX2 AND MAIN2 IOT--LEV13 INTERRUPT
AC=XXX3 AND MAIN2 IOT--LEV13 INTERRUPT
AC=XXX1 AND MAIN2 IOT--LEV14 INTERRUPT
AC=XXX0 AND MAIN2 IOT--NO INTERRUPT
```

APPENDIX A

PDP-8 MODE PERFORATED - TAPE LOADER

READIN MODE LOADER

THE READIN MODE (RIM) LOADER IS A MINIMUM LENGTH, BASIC, PERFORATED-TAPE PROGRAM FOR THE 33 ASR. IT IS INITIALLY STORED IN MEMORY BY MANUAL USE OF THE OPERATOR CONSOLE KEYS AND SWITCHES. THE LOADER IS PERMANENTLY STORED IN 18 LOCATIONS OF PAGE 37.

THE RIM LOADER CAN ONLY BE USED IN CONJUNCTION WITH THE 33ASR READER (NOT THE HIGH-SPEED PERFORATED-TAPE READER). BECAUSE A TAPE IN RIM FORMAT IS, IN EFFECT, TWICE AS LONG AS IT NEED BE, IT IS SUGGESTED THAT THE RIM LOADER BE USED ONLY TO READ THE BINARY LOADER WHEN USING THE 33 ASR. (NOTE: SOME PDP-12 DIAGNOSTIC PROGRAM TAPES ARE IN RIM FORMAT).

THE COMPLETE PDP-12 RIM LOADER (SA#7756 IS AS FOLLOWS!)

ABSOLUTE ADDRESS	OCTAL CONTENT	TAG	INSTRUCTION I Z	COMMENTS
7756	6032	BEG,	KCC	/CLEAR AC AND FLAG
7757	6031		KSF	/SKIP IF FLAG = 1
7760	5357		JMP-1	/LOOKING FOR CHARACTER
7761,	6036		KRB	/READ BUFFER
7762,	7106		CLL RTL	
7763,	7006		RTL	/CHANNEL 8 IN ACO
7764,	7510		SPA	/CHECKING FOR LEADER
7765,	5357		JMP BEG+1	/FOUND LEADER
7766,	7006		RTL	/OK, CHANNEL 7 IN LINK
7767	6031		K8F	
7770,	5367		JMP-1	
7771,	6034		KRS	/READ, DO NOT CLEAR
7772,	7420		SNL	/CHECKING FOR ADDRESS
7773,	3776		DCA 1 TEMP	/STORE CONTENT
7774,	3376		DCA TEMP	/STORE ADDRESS
7775,	5356		JMP BEG	/NEXT WORD
7776,	0	TEMP,	0	/TEMP STORAGE
7777,	5XXX		JMP X	/JMP START OF BIN LOADER

PLACING THE RIM LOADER IN CORE MEMORY BY WAY OF THE OPERATOR CONSOLE KEYS AND SWITCHES IS ACCOMPLISHED AS FOLLOWS:

- A. SET THE STARTING ADDRESS 7756 IN THE LEFT SWITCHES.
- B. SET THE FIRST INSTRUCTION (6032) IN THE RIGHT SWITCHES.
- C. PRESS THE FILL SWITCH.
- D. PRESS THE FILL STEP SWITCH
- E. SET THE NEXT INSTRUCTION (6031) IN THE RIGHT SWITCHES.
- F. PRESS THE FILL STEP SWITCH.
- G. REPEAT STEPS D AND E UNTIL ALL 16 INSTRUCTIONS HAVE BEEN DEPOSITED.

ON A TAPE IN RIM FORMAT, PLACE THE TAPE IN THE READER, SET THE LEFT SWITCHES TO THE STARTING ADDRESS 7756 OF THE RIM LOADER

(NOT OF THE PROGRAM BEING READ), PRESS THE START LS KEY, AND
START THE TELETYPE READER.

/KFI2B
 /
 /AUTOMATIC PRIORITY INTERRUPT OPTION TEST
 //////////////////////////////////////

/SYMBOL TABLE

6006 APION=6006
 6760 PJA=6760
 6771 RES=6771
 6772 SMLV=6772
 6773 RMLV=6773
 6774 RSTK=6774
 6775 RVEC=6775
 6776 SSTK=6776
 6777 SVEC=6777
 6051 MAIN1=6051
 6052 MAIN2=6052
 6001 ION=6001
 6002 IOP=6002
 6224 RIF=6224
 6214 RDF=6214

PMODE

/PAGE 0
 /CONSTANTS, TEMPORARY STORAGE AND FLAGS

LOC0, *0 /SHOULD NOT GET AN 0 MODE INTERRUPT VIA LOC 0
 HLT /EXCEPT IN TEST TO WHERE LOC 1 = JMP :-1
 INC
 UT16
 UT37A, UT37
 SETUP, SETUPP
 CLRSTK, CLAST

*20 JMP I *-1 /JMP TO
 T0 /START OF PROG

IOPRES, IOPRE
 COUNT, CNT-1
 RAN, RANN
 RAN1, 4731
 RAN2, 5074
 PJPC, TSTPJ-4
 PJPC1, TSTPJ1-4
 PJPC2, TSTPJ2-4

1	0000	0000
2	0001	0000
3	0002	7402
4	0003	0000
5	0004	0266
6	0005	1402
7	0006	3402
8	0007	0213
9		0230
10	0020	0020
11	0021	3421
12	0022	0400
13	0023	0202
14	0024	1122
15	0025	0275
16	0026	4731
17	0027	5074
18	0030	1212
19	0031	1421
20	0031	2047

0032 5600 STACK, STACKK
 0033 5601 STACKK+1
 0034 5602 STACKK+2
 0035 5603 STACKK+3
 0036 5604 STACKK+4
 0037 5605 STACKK+5

/ *40
 0
 HLT
 TST

TSTA,

K1,
 K2,
 K3,
 K4,
 K7,
 K10,
 K17,
 K20,
 K37,
 K40,
 K60,
 K77,
 K100,
 K177,
 K200,
 K207,
 K220,
 K377,
 K400,
 K520,
 K720,
 K777,
 K1000,
 K1441,
 K1641,
 K1741,
 K1777,
 K2000,
 K2020,
 K2071,
 K2076,
 K2103,
 K2110,
 K2115,
 K2122,
 K2127,
 K2134,
 K2141,
 K2146,
 K2153,
 K2160,
 K2165,
 K2172,

/SHOULD NOT GET A LINC MODE INTERRUPT VIA LOC 40

107				
108	0116	2177	K2177,	2177
109	0117	2500	K2500,	2500
110	0120	2501	K2501,	2501
111	0121	2525	K2525,	2525
112	0122	2777	K2777,	2777
113	0123	3500	K3500,	3500
114	0124	3777	K3777,	3777
115	0125	4000	K4000,	4000
116	0126	4400	K4400,	4400
117	0127	5020	K5020,	5020
118	0130	5240	K5240,	5240
119	0131	5241	K5241,	5241
120	0132	5252	K5252,	5252
121	0133	5400	K5400,	5400
122	0134	5577	K5577,	5577
123	0135	5600	K5600,	5600
124	0136	5777	K5777,	5777
125	0137	6000	K6000,	6000
126	0140	6100	K6100,	6100
127	0141	6300	K6300,	6300
128	0142	7000	K7000,	7000
129	0143	7017	K7017,	7017
130	0144	7020	K7020,	7020
131	0145	7077	K7077,	7077
132	0146	7277	K7277,	7277
133	0147	7400	K7400,	7400
134	0150	7402	K7402,	7402
135	0151	7577	K7577,	7577
136	0152	7600	K7600,	7600
137	0153	7740	K7740,	7740
138	0154	7741	K7741,	7741
139	0155	7760	K7760,	7760
140	0156	7770	K7770,	7770
141	0157	7772	M6,	-6
142	0160	7762	M16,	-16
143	0161	6140	VEC0,	VECT0
144	0162	6145	VEC1,	VECT1
145	0163	6152	VEC2,	VECT2
146	0164	6157	VEC3,	VECT3
147	0165	6164	VEC4,	VECT4
148	0166	6200	VEC5,	VECT5
149	0167	6205	VEC6,	VECT6
150	0170	6212	VEC7,	VECT7
151	0171	6217	VEC10,	VECT10
152	0172	6224	VEC11,	VECT11
153	0173	6231	VEC12,	VECT12
154	0174	6236	VEC13,	VECT13
155	0175	6244	VEC14,	VECT14
156	0176	6251	VEC15,	VECT15
157	0177	6256	VEC16,	VECT16

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158 /
159 /
160 /
161 0200 0200 *200
162 0201 5601 JMP I ,+1
163 0400 70 T0
164 /
165 /
166 /LINC MODE PROGRAMABLE IO PRESET
167 /ENTERED IN 8-MODE
168 /
169 0202 0000 /CONTAINS RETURN JUMP ADDRESS
170 0203 6141 /LINC MODE
171 /
172 0204 0011 /CLEAR AC LINK AND M0
173 0205 1020 /LOAD AC
174 0206 0020 /WITH 0020
175 0207 0004 /AC TO SPECIAL FUNCTIONS REGISTER
176 0210 0002 /8-MODE
177 /
178 0211 7200 /CLEAR AC
179 0212 5602 /RETURN TO MAIN PROGRAM
180 /
181 /SETUP ROUTINE FOR STACK, VECTOR AND
182 /MACHINE LEVEL PARAMETERS
183 /ENTERED IN 8-MODE
184 /
185 0213 0000 /CONTAINS RETURN JUMP ADDRESS
186 0214 7300 /CLEAR AC AND LINC
187 0215 4202 /IO PRESET
188 0216 1135 /AC = 5600
189 0217 0776 /SET STACK POINTER REGISTER TO 5600
190 0220 7200 /CLEAR AC
191 0221 1140 /AC = 6100
192 0222 6777 /SET VECTOR POINTER REGISTER TO 6100
193 0223 7200 /CLEAR AC
194 0224 1092 /AC = 20
195 0225 6772 /SET MACHINE LEVEL TO 0 AND CLEAR THE STACK AND VECTOR FIELD BITS
196 0226 7300 /CLEAR AC AND LINC
197 0227 5613 /RETURN TO MAIN PROGRAM

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0000
7300
1134
3010
1156
3011
7300
3410
2011
5237
5630

CLRST, 0
CLA CLL
TAD K5577
DCA 10
TAD K7770
DCA 11
CLA CLL
DCA 1 10
ISE 11
JMP *-2
JMP I CLRST

```

(0134) = 5577
 (0136) = 5777
 (0138) = 7770

```

/CONTAINS RETURN JUMP ADDRESS
/CLEAR AC AND LINK
/AC = 5777
/LOC 10 = 5777 / LOC 10 = 5577
/AC = 7770
/LOC 11 = 7770
/CLEAR AC AND LINK
/CLEAR STACK ADDRESS IN LOC 10
/DONE IF LOC 11 = 0
/NO-DO IT AGAIN
/RETURN TO MAIN PROGRAM

```

```

/LINC MODE SUBROUTINE
/TO MOVE MQ REGISTER INTO AC

LMODE
XACMQ, DJR
QAC
ROL I 1
OLZ I
JMP *-4
BSE I 1
JMP *-1
JMP 0

```

```

/DISABLE JUMP RETURN
/MQ 0-10 TO AC 1-11
/ROTATE LEFT 1
/SKIP IF MQ 11 = 1
/JMP 4 LOC (ENABLE JMP 0)
/SET AC BIT 0
/TO 1
/ENABLE JMP 0
/RETURN TO MAIN PROG

```

```

/LINC MODE SUBROUTINE
/TO SET AC AND MQ = 7777
/ALSO SET LINC AND FLO FLIP FLOPS

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SACMQL, LDA I
7777
ROR I 14
LDA I
3777
ADA I
1
LDA I
7777
JMP 0

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/AC = 7777
/MQ = 7777 AND LINC = 1
/AC = 3777
/ADD
/1 SETTING FLO = 1
/AC = 7777
/RETURN TO MAIN PROG

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244 /
245 /8-MODE SUBROUTINE
246 /TO INCREMENT RETURN PC STORED IN LOC STACKK+1
247 /AS A RESULT OF A PUSH JUMP IOT OR AN INTERRUPT
248 /
249
250
251 INCC, 0000 /RMODE
252 0266 0000 /RETURN JUMP ADDRESS TO MAIN PROGRAM
253 0267 7300 /CLEAR AC AND LINC
254 0270 1433 /GET PC STORED IN LOC STACKK+1
255 0271 1043 /INCREMENT
256 0272 3433 /STORE BACK IN LOC STACKK+1
257 0273 1150 /AC = 7402 OR AN 8-MODE HLT
258 0274 5666 /RETURN TO MAIN PROGRAM
259
260 /RANDOM NUMBER GENERATOR
261 /USED IN TEST T65
262 /
263 RANN, 0000 /CONTAINS RETURN JUMP ADDRESS
264 0275 7300 /CLEAR AC AND LINC
265 0276 1025 /AC = RAN1
266 0277 7006 /ROTATE 2 LEFT
267 0300 7001 /INCREMENT
268 0301 1026 /ADD RAN2
269 0302 3026 /STORE IN RAN2
270 0303 1026 /GET RAN2
271 0304 7006 /ROTATE 2 LEFT
272 0305 7006 /ROTATE 2 LEFT
273 0306 7001 /INCREMENT
274 0307 3025 /STORE IN RAN1
275 0310 7300 /CLEAR LINC
276 0311 3025 /RETURN TO MAIN PROGRAM
277 0312 5675

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/
//////////
/AUTOMATIC PRIORITY INTERRUPT
/DIAGNOSTIC TESTS
//////////
/TEST NORMAL INTERRUPT SYSTEM VIA TELETYPE
/BY OUTPUTTING 207 (BELL) TO TTY
/AND WAITING FOR AN INTERRUPT TO OCCURE
/A TIME OUT LOOP IS USED TO DETECT
/AN INTERRUPT NOT OCCURRING WITHIN 120 MS
/

*400
IOF /INTERRUPTS OFF
CLA CLL K5400 /CLEAR AC AND LINK
TAD /AC = 5400
DCA /LOC 1 = 5400 OR A JMP .-1
ION /INTERRUPTS ON
TAD /AC = 207
TLS /AC TO TTY, PRINT AND CLEAR FLAG
CLA CLL /CLEAR AC AND LINK
TAD /AC = 7772
DCA /LOC 15 = 7776
DCA /LOC 16 = 0
TAD K7600 /AC = 7600
DCA /LOC 17 = 7600
TSF /SKIP ON TTY FLAG SET
JMP .+2 /NOT SET
JMP .+6 /SET-GO TO TEST ONE
ISE 16 /INCREMENT TIME OUT LOOP
JMP .-4 /TEST TTY FLAG AGAIN
ISE 15 /INC LOC 15 AND SKIP IF = 0
JMP .-6 /NO-DO IT AGAIN
HLT /ERROR-TTY DID NOT INTERRUPT
TCF /CLEAR FLAG
TAD K7400 /AC = 7402
DCA /LOC 1 = 7402 OR AN 0-MODE HLT
RMP /RESTORE MEMORY FIELDS

/SET STACK ADDRESS REGISTER TO ZERO
/READ STACK POINTER REGISTER INTO CLEARED AC
/IT COMES BACK IN COMPLIMENT FORM
/

*400
IOF /INTERRUPTS OFF
CLA CLL /CLEAR AC AND LINK
SSTK /SET BTS 3-14 OF STK ADD TO AC BTS 0-11
CLA /CLEAR AC
RSTK /READ STK ADD BITS 3-14 INTO AC BTS 0-11
CMA /COMPLIMENT AC
SZA /SHOULD BE 0
HLT /ERR-EXAMINE AC

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329 /
330 /SET MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS ALL TO ZERO
331 /READ THEM BACK INTO CLEARED AC
332 /BITS 6 AND 7 ALWAYS COME BACK IN THE 1 STATE
333 /THE MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS COME BACK IN COMPLIMENT FORM
334 /
335
336 0441 7300 CLA CLL /CLEAR AC AND LINC
337 0442 1052 TAD K20 /AC = 20
338 0443 6772 SMLV /SET STACK AND VECTOR FIELD BITS AND MACHINE LEVEL = 0
339 0444 7200 CLA /CLEAR AC
340 0445 6773 RMLV /READ STK FLD INTO AC BITS 0-2 AND VCT FLD INTO AC BITS 3-5
341 0446 3002 DCA IMAGE /IMAGE OF AC
342 0447 1002 TAD IMAGE /GET IMAGE
343 0450 7040 CMA /COMPLIMENT AC
344 0451 7440 SZA /AC = 0
345 0452 7402 HLT /ERR-EXAMINE AC
346
347 /SET STACK ADDRESS REGISTER TO ZERO
348 /GENERATE AN IO PRESET
349 /READ STACK REGISTER INTO CLEARED AC
350 /IO PRESET SHOULD HAVE SET STACK POINTER REGISTER TO ALL ONES
351 /READ BACK IN COMPLIMENT FORM
352 /
353 0453 7300 CLA CLL /CLEAR AC AND LINC
354 0454 6776 SSTK /SET BITS 3-14 OF STK ADD TO AC BITS 0-11
355 0455 7200 CLA /CLEAR AC
356 0456 6774 RSTK /READ STK BITS 3-14 INTO AC BITS 0-11
357 0457 7040 CMA /COMPLIMENT AC
358 0460 7440 SZA /SHOULD BE 0
359 0461 7402 HLT /ERR-EXAMINE AC
360 0462 4422 JMS I IOPRES /LINC MODE IO PRESET
361 0463 6774 RSTK /READ STACK ADD BITS 3-14 INTO AC BITS 0-11
362 0464 7440 SZA /AC SHOULD = 0
363 0465 7402 HLT /ERR AC NOT = 0
364
365 /SET STACK ADDRESS REGISTER TO 7777 IN LINC MODE
366 /READ STACK POINTER REGISTER INTO CLEARED AC
367 /IT COMES BACK IN COMPLIMENT FORM
368 /
369 0466 7340 CLA CLL CMA /AC = 7777
370 0467 6141 LINC /LINC MODE
371 LMODE
372 0470 0500 IOB /EXEC 8MODE INST
373 0471 0776 SSTK /SET BITS 3-14 OF STACK ADD TO AC BITS 0-11
374 0472 0011 CLR /CLEAR AC LINC AND MQ
375 0473 0500 IOB /EXEC 8MODE INST
376 0474 0774 RSTK /READ STACK ADD BITS 3-14 TO AC BITS 0-11
377 0475 1460 SAE I /SKIP IF AC
378 0476 0000 0 /EQUALS 0
379 0477 0000 HLT /ERR-AC NOT = 7777
380 0500 0002 PDP /8MODE
381 PMODE

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/TEST STACK REGISTER ADDRESS POINTER
/IN 8-MODE

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T5,          7340          CLA CLL CMA          /AC = 7777
0501         6776          SSTK          /SET BTS 3-14 OF STACK ADD TO AC BTS 0-11
0502         7200          CLA          /CLEAR AC
0503         1144          TAD          /AC=7020
0504         6772          SMLV          /SET STK FLD TO AC BTS 0-2
0505         7200          CLA          /CLEAR AC
0506         6773          RMLV          /READ STACK FIELD BTS 0-2 INTO AC BTS 0-2
0507         3002          DCA          /IMAGE OF RESULT
0508         1002          TAD          /GET IMAGE
0509         7041          CIA          /COMPLIMENT AND INCREMENT
0510         1070          TAD          /IMAGE SHOULD = 777
0511         7440          SEA          /SHOULD BE 0
0512         7402          HLT          /ERR-EXAMINE LOC IMAGE
0513         4422          JMS I          /LINC MODE IO PRESET
0514         6773          RMLV          /READ STACK FIELD BTS 0-2 INTO AC BTS 0-2
0515         3002          DCA          /IMAGE OF AC
0516         1002          TAD          /GET IMAGE
0517         7041          CIA          /COMPLIMENT AND INC
0518         1055          TAD          /IMAGE SHOULD = 60
0519         7440          SEA          /AC SHOULD = 0
0520         7402          HLT          /ERR

```

/TEST STACK REGISTER ADDRESS POINTER
/IN 8-MODE

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T6,          7300          CLA CLL          /CLEAR AC AND LINC
0526         1127          TAD          /AC=5020
0527         6772          SMLV          /SET STK FLD TO AC BTS 0-2
0528         7200          CLA          /CLEAR AC
0529         1121          TAD          /AC=2525
0530         6776          SSTK          /SET BTS 3-14 OF STK ADD TO AC BTS 0-11
0531         7200          CLA          /CLEAR AC
0532         1121          TAD          /READ AC
0533         6776          SSTK          /READ STK FLD BTS 0-2 INTO AC BTS 0-2 ALSO GET VGT FLD BTS 3-5
0534         7200          CLA          /CLEAR AC
0535         6773          RMLV          /IMAGE OF RESULT
0536         3002          DCA          /GET IMAGE
0537         1002          TAD          /COMPLIMENT AND INCREMENT
0538         7041          CIA          /IMAGE SHOULD = 2777
0539         1122          TAD          /AC SHOULD = 0
0540         7440          SEA          /ERR-EXAMINE LOC IMAGE
0541         7402          HLT          /READ STACK ADD BTS 3-14 INTO AC BTS 0-11
0542         6774          RSTK          /IMAGE OF RESULT
0543         3002          DCA          /GET IMAGE
0544         1002          TAD          /COMPLIMENT AND INCREMENT
0545         7041          CIA          /IMAGE SHOULD = 5252
0546         1132          TAD          /AC SHOULD = 0
0547         7440          SEA          /ERR-EXAMINE LOC IMAGE
0548         7402          HLT          /JMP TO
0549         5754          JMP I          /TEST T7
0550         6600          T7

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435 /
436 /TEST STACK REGISTER ADDRESS POINTER
437 /IN 8-MODE
438 /
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0600	*600		
0601	CLA CLL		/CLEAR AC AND LINC
0602	TAD K2020		/AC=2020
0603	SMLV		/SET STK FLD BTS 0-2 TO AC BTS 0-2 ALSO SET VCT BTS = 0
0604	CLA		/CLEAR AC
0605	TAD K5252		/AC=5252
0606	SSTK		/SET BTS 3-14 OF STK ADD EQUAL TO AC BTS 0-11
0607	CLA		/CLEAR AC
0610	RMLV		/READ STK FLD BTS 0-2 INTO AC BTS 0-2
0611	DCA	IMAGE	/IMAGE OF RESULT
0612	TAD	IMAGE	/GET IMAGE
0613	CIA		/COMPLIMENT AND INCREMENT
0614	TAD	K5777	/IMAGE SHOULD = 5777
0615	SZA		/AC SHOULD = 0
0616	HLT		/ERR-EXAMINE LOC IMAGE
0617	RSTK		/READ STACK ADD BTS 3-14 INTO AC BTS 0-11
0620	DCA	IMAGE	/IMAGE OF RESULT
0621	TAD	IMAGE	/GET IMAGE
0622	CIA		/COMPLIMENT AND INCREMENT
0623	TAD	K2525	/IMAGE SHOULD = 2525
0624	SZA		/AC = 0
7402	HLT		/ERR-EXAMINE LOC IMAGE

```

/TEST VECTOR REGISTER ADDRESS POINTER
/IN 8-MODE
/
T10,

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7300	CLA CLL		/CLEAR AC AND LINC
6777	SVEC		/SET VECTOR ADD BTS 3-9 TO AC BTS 0-6
7200	CLA		/CLEAR AC
6775	RVEC		/READ VECTOR ADD BTS 3-14 INTO AC BTS 0-11
3002	DCA	IMAGE	/IMAGE OF AC
1002	TAD	IMAGE	/GET IMAGE
7041	CIA		/COMPLIMENT AND INCREMENT
1154	TAD	K7741	/IMAGE SHOULD = 7741
7440	SZA		/AC = 0
7402	HLT		/ERR-EXAMINE AC

```

476 /
477 /
478 /TEST VECTOR REGISTER ADDRESS POINTER
479 /IN 8-MODE
480 /
481 /T11,
482 /CLEAR AC AND LINC
483 /AC=7740
484 /SET VECTOR ADD BITS 3-9 TO AC BITS 0-6
485 /CLEAR AC
486 /READ VECTOR ADD BITS 3-14 INTO AC BITS 0-11
487 /IMAGE OF AC
488 /GET IMAGE
489 /COMPLIMENT AND INC
490 /IMAGE SHOULD = 1
491 /AC = 0
492 /ERR-EXAMINE AC
493 /LINC MODE IO PRESET
494 /READ VECTOR ADDRESS BITS 3-14 INTO AC BITS 0-11
495 /IMAGE OF AC
496 /GET IMAGE
497 /COMPLIMENT AND INC
498 /IMAGE SHOULD = 1
499 /AC = 0
500 /ERR-IO PRESET DID NOT CLEAR VECTOR ADD BITS 3-14
501 /
502 /TEST VECTOR REGISTER ADDRESS POINTER
503 /IN 8-MODE
504 /LINC
505 /T12,
506 /CLEAR AC AND LINC
507 /LINC MODE
508 /LOAD AC WITH
509 /OPERAND = 7740
510 /EXEC 8MODE INST
511 /SET VECTOR BITS 3-9 TO AC BITS 0-6
512 /CLEAR AC MQ LINC
513 /EXEC 8MODE INST
514 /READ VECTOR ADD BITS 3-14 INTO AC BITS 0-11
515 /AC =
516 /1
517 /ERR-AC NOT = 7740
518 /8 MODE
519 /IOPRES /LINC MODE PROG IO PRESET

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0637 7300 CLA CLL
0640 1153 TAD K7740
0641 6777 SVEC
0642 7200 CLA
0643 6775 RVEC
0644 3002 DCA
0645 1002 TAD
0646 7041 CIA
0647 1043 TAD
0650 7440 SZA
0651 7402 HLT
0652 4422 JMS I
0653 6775 RVEC
0654 3002 DCA
0655 1002 TAD
0656 7041 CIA
0657 1043 TAD
0660 7440 SZA
0661 7402 HLT

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```

0662 7300 CLA CLL
0663 6141 LINC
0664 1020 LMODE
0665 7740 LOA I
0666 0500 IOB
0667 0777 SVEC
0670 0011 CLR
0671 0500 IOB
0672 0775 RVEC
0673 1460 SAE I
0674 0001 1
0675 0000 HLT
0676 0002 PDP
0677 4422 PMODE
0678 IOPRES
0679 /LINC MODE PROG IO PRESET

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520 /
521 /TEST VECTOR REGISTER ADDRESS POINTER
522 /IN 8-MODE
523 /
524 /
525 /T13,
526 /CLEAR AC
527 /AC = 7740
528 /SET VECTOR ADD BTS 3-9 TO AC BITS 0-6
529 /CLEAR AC
530 /AC=0720
531 /SET VECTOR FLD BTS 0-2 TO AC BITS 3-5
532 /CLEAR AC
533 /READ VECTOR FLD BTS 0-2 INTO AC BITS 3-5 ALSO GET STK FLD BTS 0-2
534 /IMAGE OF RESULT
535 /GET IMAGE
536 /COMPLIMENT AND INC
537 /IMAGE SHOULD = 7077
538 /AC = 0
539 /ERR-EXAMINE LOC IMAGE
540 /LINC MODE IO PRESET
541 /READ VECTOR FIELD BITS INTO AC
542 /IMAGE OF AC
543 /GET IMAGE
544 /COMPLIMENT AND INC
545 /IMAGE SHOULD = 60
546 /AC = 0
547 /ERR-AC NOT = 0

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```

548 /TEST VECTOR REGISTER ADDRESS POINTER
549 /IN LINC MODE
550 /
551 /T14,
552 /CLEAR AC AND LINC
553 /LINC MODE
554 /LOAD AC WITH
555 /OPERAND = 7720
556 /EXEC 8MODE INST
557 /SET STACK AND VECTOR FIELD BITS
558 /CLEAR AC HQ LINC
559 /EXEC 8MODE INST
560 /READ STACK AND VECTOR FIELD BITS TO AC BITS 0-5
561 /AC =
562 /77
563 /ERR-AC NOT = 77
564 /8MODE
565 /
566 /JMS I IOPRES /LINK MODE PROG IO PRESET

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567 /
568 /TEST VECTOR REGISTER ADDRESS POINTER
569 /IN 8-MODE
570 /
571 /
572 /T15,
573 /CLEAR AC AND LINC
574 /AC=0920
575 /SET VECTOR FLD BTS 0-2 TO AC BTS 3-5
576 /CLEAR AC
577 /AC=2500
578 /SET VECTOR ADD BTS 3-9 EQUAL TO AC BTS 0-6
579 /CLEAR AC
580 /READ VECTOR FLD BTS 0-2 INTO AC BTS 3-5
581 /IMAGE OF AC
582 /GET IMAGE
583 /COMPLIMENT AND INCREMENT
584 /IMAGE SHOULD = 7277
585 /AC SHOULD = 0
586 /ERR-EXAMINE LOC IMAGE
587 /CLEAR AC
588 /READ VECTOR ADD BTS 3-14 INTO AC BTS 0-11
589 /IMAGE OF RESULT
590 /GET RESULT
591 /COMPLIMENT AND INCREMENT
592 /IMAGE SHOULD = 5241
593 /AC SHOULD = 0
594 /ERR-EXAMINE LOC IMAGE
595 /JMP TO
596 /TEST T16

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/TEST VECTOR REGISTER ADDRESS POINTER
/IN 8-MODE

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622 /
623 /TEST SETTING OF MACHINE LEVEL REGISTER AND STACK AND VECTOR FIELD BITS
624 /TO ZERO IN 8-MODE
625 /TEST THAT IO PRESET WILL SET THEM ALL HIGH AGAIN
626 /
627 /17,
628 1025 7300 /CLEAR AC AND LINC
629 1026 1052 /AC = 20
630 1027 6772 /SET MACHINE LEVEL TO AC BITS 8-11 AND THE STACK AND VECTOR FIELD BITS TO 0
631 1030 7200 /CLEAR AC
632 1031 6773 /READ MACHINE LEVEL INTO AC BITS 8-11 AND THE STACK AND VECTOR FIELD BITS
633 1032 3002 DCA IMAGE
634 1033 1002 TAD IMAGE
635 1034 7040 CMA
636 1035 7440 SZA
637 1036 7402 HLT /AC SHOULD = 0
638 1037 4422 JMS I IOPRES /ERR-EXAMINE LOC IMAGE
639 1040 6773 RMLV /LINC MODE IO PRESET
640 1041 3002 DCA /READ MACHINE LEVEL INTO AC BITS 8-11 AND THE STACK AND VECTOR FIELD BITS
641 1042 1002 TAD /IMAGE OF AC
642 1043 7041 CIA /GET IMAGE
643 1044 1055 TAD /COMPLIMENT AND INC
644 1045 7440 SZA /IMAGE SHOULD = 60
645 1046 7402 HLT /SHOULD BE ZERO
646 /ERR-IO PRESET DID NOT CLEAR MACHINE LEVEL REGISTER
647 /
648 /TEST SETTING OF MACHINE LEVEL REGISTER
649 /TO ZERO IN LINC MODE

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650 /20,
651 1047 7300 /CLEAR AC AND LINK
652 1050 6141 /LINK MODE
653 1051 1020 /LOAD THE AC
654 1052 0017 /WITH 17
655 1053 0500 /EXEC 8MODE IOT
656 1054 0772 /SET MACH LEVEL TO AC BITS 8-11
657 1055 0011 CLR /CLEAR AC LINC AND MQ
658 1056 0500 IOB /EXEC 8MODE IOT
659 1057 0773 RMLV /READ MACH LEVEL INTO AC BITS 8-11
660 1060 1400 SAE I /AC =
661 1061 0000 /60
662 1062 0000 HLT /ERR-AC NOT = 17
663 1063 0011 CLR /CLEAR AC MQ LINK
664 1064 0002 POP /8 MODE
665 1065 4422 PMODE
666 JMS I IOPRES /LINC MODE IO PRESET

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/TEST SETTING OF MACHINE LEVEL REGISTER
/TO ALL POSSIBLE COMBINATIONS
/IN 8-MODE
/

1066	1067	1070	1071	1072	1073	1074	1075	1076	1077	1100	1101	1102	1103	1104	1105	1106	1107	1110	1111	1112	1113	1114	1115	1116	1117	1120	1121	1122
7320	3010	1160	3011	1023	3012	7300	1010	6772	7200	6773	3002	1002	7041	1412	7440	7402	4422	6773	0051	7440	7402	2010	5317	7402	2011	5274	5722	1200
CLA CLL	DCA	TAD	DCA	TAD	DCA	CLA CLL	TAD	SMLV	CLA	RMLV	DCA	TAD	CIA	TAD I	SEA	HLT	JMS I	RMLV	AND	SEA	HLT	ISE	JMP	HLT	ISE	JMP	JMP I	T22
	10	M16	11	COUNT	12		10			IMAGE	IMAGE		12			IOPRES	K17					10	.*2	11	AGAIN	.*1		
	/CLEAR AC AND LINC	/LOC 10 = 0	/AC = -16	/LOC 11 = -16	/POINTS TO TOP OF EXPECTED RESULT LIST	/STORE IN LOC 12	/AC = 0	/LOC 10 TO AC	/SET MACHINE LEVEL TO AC BITS 8-11	/CLEAR AC	/READ MACHINE LEVEL INTO AC BITS 8-11	/GET IMAGE	/COMPLIMENT AND INCREMENT	/IMAGE SHOULD = CONTENTS OF ADDRESS SPECIFIED BY LOC 12	/AC = 0	/ERR-EXAMINE LOC IMAGE	/LINC MODE TO PRESET	/READ MACHINE LEVEL INTO AC BITS 8-11 AND STACK AND VECTOR FLD BITS INTO AC BITS 0-5	/AC = 17	/AC SHOULD = 0	/ERR-MACHINE LEVEL NOT CLEARED BY IO PRESET	/INCREMENT LOC 10	/LOC 10 NOT = 0--SHOULD NEVER = 0	--NEVER GET HERE--	/INCREMENT LOC 11 AND SKIP IF ZERO	/NO-DO IT AGAIN	/JMP TO	/TEST T22

/EXPECTED RESULTS TABLE FOR TEST T21

1123	1124	1125	1126	1127	1130	1131	1132	1133	1134	1135	1136	1137	1140	1141	1142	
0077	0076	0075	0074	0073	0072	0071	0070	0067	0066	0065	0064	0063	0062	0061	0060	
CNT,	77	76	75	74	73	72	71	70	67	66	65	64	63	62	61	60

```

721 /
722 /
723 /TEST PUSHJ INSTRUCTION WITH API OFF
724 /WITH ALL STORED STATUS CLEARED IF POSSIBLE
725 /TEST RESTORE IOT WITH API OFF
726 /AFTER ALTERING VALUE OF RETURN PC IN STACK*1
727 /AND SETTING MACHINE PARAMETERS TO BE RESTORED
728 /TO THE OPPOSITE OF THE EXPECTED STATE
729 /
730
731 1200 *1200
732 1201 JMS I CLRSTK /CLEAR STACK
733 1202 6141 JMS I SETUP /SETUP API REGISTERS
734 /LINC MODE /LINC MODE
735 1203 0474 /SKIP IF FLO = 0
736 1204 0000 FLO I /ERR-FLO SET
737 1205 0002 HLT /8-MODE
738 PDP
739 1206 7340 CLA CLL CMA /AC = 7777
740 1207 3000 DCA /LOC 0 = 7777
741 1210 6760 PJA10 /PUSH JUMP WITH FIELD BITS = 0
742 1211 1216 TSTPJ /TO LOC TSTPJ
743 1212 7402 HLT /ERR--PUSH JUMP DID NOT EXECUTE
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```

LOC0
T22A
LOC0
XACHQ
IMAGE
IMAGE
K2172
STACK

```

```

/DECODES AS A HLT IF RESTORE NOT TO 8-MODE
/CONTINUE WITH TEST
/ARRIVE FROM PUSH JUMP IN 8-MODE (DECODES HLT IN LINC MODE)
/AC = 0
/ERR-AC NOT = 0
/LINC = 0
/ERR-LINC NOT = 0
/LINC MODE
/FLO = 0
/ERROR
/EXCHANGE AC AND MQ
/8-MODE
/AC = 0
/ERR-AC NOT = 0 THEREFORE MQ WAS NOT = 0
/READ STACK REGISTER POINTER INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INCREMENT
/IMAGE SHOULD = 2172
/AC = 0
/ERR
/GET AC STORED ON STACK BY PUSH JUMP
/AC = 0
/ERR

```

```

TSTPJ,

```

```

1213 7402 HLT
1214 0000 AND
1215 5276 JMP
1216 0000 AND
1217 7440 SZA
1220 7402 HLT
1221 7430 SZL
1222 7402 HLT
1223 6141 LINC
1224 0474 LMODE
1225 0000 FLO I
1226 6243 HLT
1227 0002 JMP
PDP
RMODE
SZA
7440
1230 7440 SZA
1231 7402 HLT
1232 6774 RSTK
1233 3002 DCA
1234 1002 TAD
1235 7041 CIA
1236 1115 TAD
1237 7440 SZA
1240 7402 HLT
1241 1432 TAD I
1242 7440 SZA
1243 7402 HLT

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1244 1433
1245 7041
1246 1027
1247 7440
1250 7402
1251 1434
1252 7440
1253 7402
1254 1435
1255 7440
1256 7402
1257 1436
1260 7041
1261 1045
1262 7440
1263 7402
1264 1433
1265 1044
1266 3433
1267 6141

1270 6254
1271 1040
1272 0000
1273 0500
1274 0771
1275 0000

1276 7440
1277 7402
1300 7430
1301 7402
1302 6141

1303 0474
1304 0000
1305 6243
1306 1460
1307 0000
1310 0000
1311 0900
1312 0224
1313 1460
1314 0000
1315 0000

TAD I
CIA
TAD
SZA
HLT
TAD I
SZA
HLT
TAD I
SZA
HLT
CIA
TAD
SZA
HLT
TAD I
TAD
DCA I
LINC
LMODE
JMP
STA
0
JOB
RES
HLT
PMODE
SEA
HLT
SZL
HLT
LINC
LMODE
FLO I
HLT
JMP
SAE I
0
HLT
JOB
RIF
SAE I
0
HLT

STACK+1 /GET PC STORED ON STACK BY PUSH JUMP
PJPC /COMPLIMENT
/SHOULD EQUAL TSTPJ-4
/AC SHOULD = 0
/ERR
STACK+2 /GET MODE FLO LINK AND MACH LEV STORED ON STACK BY PUSH JUMP
/AC = 0
/ERR
STACK+3 /GET MQ STORED ON STACK BY PUSH JUMP
/AC SHOULD = 0
/ERR
STACK+4 /GET UF IF AND DF STORED ON STACK BY PUSH JUMP
/COMPLIMENT AND INC
/LOC STACKK+4 SHOULD = 3
/AC = 0
/ERR
K3 /ERR
STACK+1 /GET PC STORED IN LOC STACK+1
K2 /INCREMENT BY 2
STACK+1 /STORE BACK IN LOC STACK+1
/LINC MODE

SACMQL /SET AC LINC MQ AND FLO
/SET LOC 0
/TO 7777
/EXECUTE 8-MODE INST
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR-RESTORE IOT DID NOT EXECUTE

/AC = 0
/ERR-AC NOT = 0
/LINC = 0
/ERR-LINC NOT = 0
/LINC MODE

/AC = 0
/ERR-FLO NOT = 0
/MQ TO AC
/AC =
0
/ERR-AC NOT = 0 THEREFORE MQ WAS NOT = 0
/EXECUTE 8-MODE INST
/READ INSTRUCTION FIELD
/AC =
0
/ERR-AC NOT = 0

XACMQ

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Page 4 of 83

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1316 0011
1317 0500
1320 0214
1321 1460
1322 0006
1323 0000
1324 0011
1325 0002

1326 6773
1327 3002
1330 1002
1331 7040
1332 7440
1333 7402
1334 6774
1335 3002
1336 1002
1337 7041
1340 1116
1341 7440
1342 7402
1343 6775
1344 3002
1345 1002
1346 7041
1347 1073
1350 7440
1351 7402
1352 5753
1353 1403

CLR
IOB
ROF
SAE I
6
HLT
CLR
PDP
PMODE
RMLV
DCA
TAD
CMA
SZA
HLT
RSTK
DCA
TAD
CIA
TAD
SZA
HLT
RVEC
DCA
TAD
CIA
TAD
SZA
HLT
JMP I
T24

IMAGE
IMAGE
IMAGE
IMAGE
K2177
IMAGE
IMAGE
K1641
.01
T24

/CLEAR AC LINC AND MQ
/EXECUTE 8-MODE INST
/READ DATA FIELD
/AC =
/6
/ERR-DF IN AC NOT = 6
/CLEAR AC LINC AND MQ
/8-MODE
/READ MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AC
/AC = 0
/ERR
/READ STACK POINTER REGISTER INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INC
/IMAGE SHOULD EQUAL 2177
/AC = 0
/ERR
/READ VECTOR POINTER REGISTER INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INC
/IMAGE SHOULD = 1641
/AC = 0
/ERR
/JMP TO
/TEST T24

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/ THE FIRST INSTRUCTION EXECUTED AFTER A RESTORE NOT WHEN
/ THE MACHINE SHOULD BE RETURNED TO LINC MODE IS A LINC MODE JUMP TO LOC 1402
/ OF THE CURRENT INSTRUCTION FIELD
/ THIS DECODES AS AN 8-MODE HLT IF THE RESTORE TO LINC MODE SHOULD FAIL

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1402 1402
UT16. 0

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/TEST PUSHJ INSTRUCTION WITH API OFF IN LINC MODE
 /AND ALL STATUS TO BE STORED SET TO
 /1 STATE IF POSSIBLE
 /TEST RESTORE IOT WITH API OFF
 /AFTER ALTERING THE VALUE OF THE RETURN PC
 /ALSO SET MACHINE PARAMETERS THAT WILL BE RESTORED
 /TO OPPOSITE OF EXPECTED STATE
 /

Address	Instruction	Comments
1403	JMS I	*1403
1404	JMS I	
1405	TAD	
1406	SMLV	
1407	LINC	
1410	LMODE	
1411	LDA I	
1412	6000:TSTPJ1+1	
1413	STA	
1414	1402	
1415	0643	
1416	0254	
1417	0500	
1420	0760	
1421	1425	
1422	0000	
1423	0000	
1424	7402	
1425	7527	
1426	7402	
1427	0472	
1430	0000	
1431	1460	
1432	7777	
1433	0000	
1434	0454	
1435	0000	
1436	6243	
1437	1460	
1440	7777	
1441	0011	
1442	0500	
1443	0214	
1444	1460	
1445	0006	
1446	0000	
1447	0011	
1450	0643	
1450	0643	

/CLEAR STACK
 /SETUP API REGISTERS
 /AC = 37
 /SET MACH LEV = 17 AND THE STACK AND VECTOR FIELD BITS = 0
 /LINC MODE
 /AC =
 /JMP TO TSTPJ1+1
 /STORE AC
 /LOC 1402 OF FIELD 0 (DECODES AS HLT IN 8-MODE)
 /SET DATA FIELD = 3
 /SET AC LINC MG AND FLO
 /EXECUTE 8-MODE INST
 /PUSH JUMP (WITH FIELD BITS = 0)
 /TO LOC TSTPJ1
 /ERR = PUSH JUMP SKIPPED
 /DECODES AS AN 8-MODE HLT IF RESTOR WAS NOT TO LINC MODE
 /CONTINUE WITH TEST
 /ARRIVE FROM PUSH JUMP IN LINC MODE (DECODES AS HLT IF 8-MODE)
 /LINC BIT =1
 /ERR
 /AC =
 /7777
 /ERR
 /FLO =1
 /ERR-FLO NOT = 1
 /MG TO AC
 /AC =
 /7777
 /ERR
 /CLEAR AC LINC AND MG
 /EXECUTE 8-MODE INST
 /READ DATA FIELD
 /AC =
 /6
 /ERR
 /CLEAR AC LINC AND MG
 /LOAD DATA FIELD 3

CLRSTK
 SETUP
 K37
 SMLV
 LINC
 LMODE
 LDA I
 6000:TSTPJ1+1
 STA
 1402
 LDF
 3
 SACHML
 JOB
 PJA10
 TSTPJ1
 HLT
 HLT
 JMP
 JMP
 JMP
 LMODE
 TSTPJ1:
 JMP
 LZE I
 HLT
 HLT
 SAE I
 7777
 HLT
 FLO
 HLT
 HLT
 JMP
 SAE I
 7777
 HLT
 CLR
 IOB
 RDF
 SAE I
 6
 HLT
 CLR
 LDF

UT16
 T24A
 UT16
 XACMG
 3

T24,
 *1403
 JMS I
 JMS I
 TAD
 SMLV
 LINC
 LMODE
 LDA I
 6000:TSTPJ1+1
 STA
 1402
 LDF
 3
 SACHML
 JOB
 PJA10
 TSTPJ1
 HLT
 HLT
 JMP
 JMP
 JMP
 LMODE
 TSTPJ1:
 JMP
 LZE I
 HLT
 HLT
 SAE I
 7777
 HLT
 FLO
 HLT
 HLT
 JMP
 SAE I
 7777
 HLT
 CLR
 IOB
 RDF
 SAE I
 6
 HLT
 CLR
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1451 0500 /EXECUTE 8 MODE INST
1452 0774 /READ STACK POINTER REGISTER INTO AC
1453 1460 /AC =
1454 2172 /2172
1455 0000 /ERR

1456 0011 /CLEAR AC LINC AND MQ
1457 0002 /8 = MODE

1460 1432 STACK /GET AC STORED ON STACK BY PUSH JUMP
1461 7040 /COMPLIMENT
1462 7440 /AC = 0
1463 7402 /ERR
1464 1433 STACK+1 /GET PC STORED ON STACK BY PUSH JUMP
1465 7041 PJPC1 /COMPLIMENT AND INC
1466 1030 /SHOULD EQUAL TSTPJ1-4
1467 7440 /AC = 0
1470 7402 /ERR
1471 1434 STACK+2 /GET MODE FLO LINC AND MACHINE LEVEL STORED ON STACK BY PUSH JUMP
1472 7041 K7017 /COMPLIMENT AND INC
1473 1143 /LOCATION STACK+2 SHOULD=7017
1474 7440 /AC = 0
1475 7402 /ERR
1476 1435 STACK+3 /GET MQ STORED ON STACK BY PUSH JUMP
1477 7040 /COMPLIMENT AC
1478 7440 /AC SHOULD = 0
1479 7402 /ERR
1480 1436 STACK+4 /GET UF IF AND OF STORED ON STACK BY PUSH JUMP
1481 7041 K3 /COMPLIMENT AND INC
1482 1045 /STACK+4 SHOULD = 3
1483 7440 /AC SHOULD = 0
1484 7402 /ERR
1485 1433 STACK+1 /GET PC STORED ON STACK BY PUSH JUMP
1486 1044 K2 /INCREMENT BY 2
1487 3433 STACK+1 /STORE BACK IN STACK+1
1488 6141 /LINC MODE
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LMODE
LDA I
6000:TSTPJ1-1
STA
1402
CLR
ADA I
0
FLO I
HLT
PDP
PMODE
RES
HLT

/AC =
/JMP TO LOC TSTPJ1-1
/STORE AC
/IN LOC 1402 OF FIELD 0 (DECODES AS HLT IN 8=MODE)
/CLEAR AC LINC AND MQ
/ADD
/0 TO AC
/TO INSURE FLO = 0
/ERR-FLO NOT = 0
/8 = MODE
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR - RESTORE SKIPPED

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T24A,
1527 1460
1530 7777
1531 0000
1532 0472
1533 0000
1534 0494
1535 0000
1536 6243
1537 1460
1540 7777
1541 0000
1542 0011
1543 0500
1544 0224
1545 1460
1546 0000
1547 0000
1550 0011
1551 0500
1552 0214
1553 1460
1554 0000
1555 0000
1556 0643
1557 0011
1560 0500
1561 0774
1562 1460
1563 2177
1564 0000
1565 0002

1566 5767
1567 1600

LMODE
SAE I
7777
HLT
LFE I
HLT
FLO
HLT
JMP
SAE I
7777
HLT
CLR
IOB
RIP
SAE I
0
HLT
CLR
IOB
RQP
SAE I
6
HLT
LOF
CLR
IOB
RSTK
SAE I
2177
HLT
PDP
PMODE
JMP I
T26

XACMQ

/AC =
/7777
/ERR-AC NOT = 7777
/LINC = 1
/ERR-LINC NOT = 1
/FLO = 1
/ERR-FLO NOT = 1
/MQ TO AC
/AC =
/7777
/ERR-AC NOT = 7777 THEREFORE MQ HAS NOT = 7777
/CLEAR AC LINC AND MQ
/EXECUTE 8-MODE INST
/READ INST FIELD
/AC =
/0
/ERR-IF IN AC NOT = 0
/CLEAR AC LINC AND MQ
/EXECUTE 8-MODE INST
/READ DATA FIELD
/AC =
/6
/ERR-OF IN AC NOT = 76
/LOAD DATA FIELD 3
/CLEAR AC LINC AND MQ
/EXECUTE 8-MODE INST
/READ STACK POINTER REGISTER INTO AC
/AC =
/2177
/ERR
/8-MODE
/JMP TO
/TEST 26

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/TURN ON API ---FIRST TIME---
/TEST THAT STACK AND VECTOR FIELD BITS
/CANNOT BE ALTERED IN 8MODE WITH API ON
/TURN API OFF WITH IOF ---FIRST TIME---
/TEST THAT STACK AND VECTOR FIELD BITS CAN BE ALTERED
/
1600 *1600
4407 JMS I CLRSTK /CLEAR STACK
4422 JMS I IOPRES /GENERATE IO PRESET
6141 LINC /LINC MODE
LMODE /CLEAR AC LINC AND MQ
CLR /8-MODE
PDP
PHODE
APION /TURN API INTERRUPT SYS ON
TAD /AC = 20
SMLV /SET MACHINE LEVEL = 0
CLA /CLEAR AC
RMLV /READ STACK AND VECTOR FIELD BITS - MACHINE LEVEL INTO AC
DCA /IMAGE OF AC
TAD /GET IMAGE
CIA /COMPLIMENT AND INC
TAD /IMAGE SHOULD = 77
SEA /AC = 0
HLT /ERR
IOF /TURN API INTERRUPT SYS OFF
TAD /AC = 20
SMLV /CLEAR STACK AND VECTOR FIELD BITS - SET MACHINE LEVEL = 0
CLA /CLEAR AC
RMLV /READ STACK AND VECTOR FIELD BITS - MACHINE LEVEL INTO AC
DCA /IMAGE OF AC
TAD /GET IMAGE
GMA /COMPLIMENT AC
SEA /AC = 0
HLT /ERR

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1600
4407
4422
6141
1603 0011
1604 0002
1605 6006
1606 1052
1607 6772
1610 7200
1611 6773
1612 3002
1613 1002
1614 7041
1615 1056
1616 7440
1617 7402
1620 6002
1621 1052
1622 6772
1623 7200
1624 6773
1625 3002
1626 1002
1627 7040
1630 7440
1631 7402

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/TURN API ON
/TEST THAT STACK AND VECTOR FIELD BITS
/CANNOT BE ALTERED IN LINC MODE WITH API ON
/TURN API OFF WITH IOF
/TEST THAT STACK AND VECTOR FIELD BITS CAN BE ALTERED
/
T27,
1632 4422 JMS I IOPRES /GENERATE IO PRESET
1633 6141 LINC /LINC MODE
LMODE
1634 0500 IOB /EXECUTE 8-MODE INST
1635 0006 APION /TURN API INTERRUPT SYSTEM ON
1636 1020 LOA I /AC =
1637 0020 20 /20
1640 0500 IOB /EXECUTE 8-MODE INST
1641 0772 SMLV /SET MACHINE LEVEL = 0
1642 0011 CLR /CLEAR AC LINC AND MQ
1643 0500 IOB /EXECUTE 8-MODE INST
1644 0773 RMLV /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
1645 1460 SAE I /AC =
1646 0077 77 /77
1647 0000 HLT /ERR
1650 0500 IOB /EXECUTE 8-MODE INST
1651 0002 IOF /TURN API INTERRUPT SYS OFF
1652 1020 LOA I /AC =
1653 0020 20 /20
1654 0500 IOB /EXECUTE 8-MODE INST
1655 0772 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 0
1656 0011 CLR /CLEAR AC LINC AND MQ
1657 0500 IOB /EXECUTE 8-MODE INST
1660 0773 RMLV /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
1661 1460 SAE I /AC =
1662 7777 7777 /7777
1663 0000 HLT /ERR
1664 0002 PDP /8-MODE
PMODE

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1075 /
1076 /TURN API ON
1077 /TEST THAT BITS 3-14 OF THE STACK ADDRESS
1078 /CANNOT BE ALTERED IN 8MODE WITH API ON
1079 /TURN API OFF
1080 /TEST THAT STACK ADD CAN BE ALTERED
1081 /
1082 T30,
1083 JMS I IOPRES /GENERATE IO PRESET
1084 APION /TURN AP INTERRUPT SYS ON
1085 SSTK /TRY TO SET STACK ADDRESS REGISTER = 0
1086 CLA /CLEAR AC
1087 RSTK /READ STACK ADDRESS REGISTER INTO AC
1088 SZA /AC = 0
1089 HLT /ERR
1090 IOP /TURN API INTERRUPT SYS OFF
1091 CLA CLL /CLEAR AC AND LINC
1092 SSTK /SET STACK ADDRESS REGISTER = 0
1093 CLA /CLEAR AC
1094 RSTK /READ STACK ADDRESS REGISTER INTO AC
1095 CMA /COMPLIMENT AC
1096 SZA /AC = 0
1097 HLT /ERR

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1098 /
1099 /TURN API ON
1100 /TEST THAT STACK ADDRESS BITS 3-14
1101 /CANNOT BE ALTERED IN LINC MODE WITH API ON
1102 /TURN API OFF
1103 /TEST THAT STACK ADDRESS CAN BE ALTERED
1104 /
1105
1106 T31,
1107 JMS I IOPRES /GENERATE IO PRESET
1108 SSTK /SET STACK ADDRESS REGISTER = 0
1109 LINC /LINC MODE
1110 LMODE
1111 IOB
1112 APION /EXECUTE 8-MODE INST
1113 LDA I /TURN API INTERRUPT SYS ON
1114 7777 /AC =
1115 IOB /7777
1116 SSTK /EXECUTE 8-MODE INST
1117 CLR /TRY TO SET STACK ADDRESS REG = 7777
1118 IOB /CLEAR AC LINC AND MQ
1119 RSTK /EXECUTE 8-MODE INST
1120 SAE I /READ STACK ADDRESS REG INTO AC
1121 7777 /AC =
1122 HLT /7777
1123 IOB /ERR
1124 ION /EXECUTE 8-MODE INST
1125 LDA I /TURN API OFF BY TURNING NORMAL INTERRUPT SYS ON
1126 7777 /AC =
1127 IOB /7777
1128 SSTK /EXECUTE 8-MODE INST
1129 CLR /SET STACK ADDRESS REGISTER = 7777
1130 IOB /CLEAR AC LINC AND MQ
1131 RSTK /EXECUTE 8-MODE INST
1132 SAE I /READ STACK ADDRESS REGISTER INTO AC
1133 0 /AC =
1134 HLT /0
1135 PDP /ERR
1136 PMODE /8-MODE
1137 IOB /TURN NORMAL INTERRUPT SYS OFF
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1140 1704 4422
1141 1705 6776
1142 1706 6141
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1144 1707 0500
1145 1710 0006
1146 1711 1020
1147 1712 7777
1148 1713 0500
1149 1714 0776
1150 1715 0011
1151 1716 0500
1152 1717 0774
1153 1720 1460
1154 1721 7777
1155 1722 0000
1156 1723 0500
1157 1724 0001
1158 1725 1020
1159 1726 7777
1160 1727 0500
1161 1730 0776
1162 1731 0011
1163 1732 0500
1164 1733 0774
1165 1734 1460
1166 1735 0000
1167 1736 0000
1168 1737 0002
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1170 1740 6002

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/TURN API ON
/TEST THAT VECTOR ADDRESS
/CANNOT BE ALTERED IN 8MODE WITH API ON
/TURN API OFF
/TEST THAT VECTOR ADDRESS CAN BE ALTERED
/
T32,
1741 4406 JMS I SETUP /SETUP API REGISTERS
1742 6777 SVEC /SET VECTOR ADDRESS REGISTER = 0
1743 6006 APION /TURN API INTERRUPT SYS ON
1744 1153 TAD K7740 /AC = 7740
1745 6777 SVEC /TRY TO SET VECTOR ADDRESS REG = 7740
1746 7200 CLA /AC = 0
1747 6775 RVEC /READ VECTOR ADDRESS REGISTER INTO AC
1750 3002 DCA /IMAGE OF AC
1751 1002 TAD IMAGE
1752 7041 CIA /GET IMAGE
1753 1154 TAD /COMPLIMENT AND INC
1754 7440 SZA /IMAGE SHOULD = 7741
1755 7402 HLT /AC = 0
1756 6002 IOP /ERR
1757 1153 TAD /TURN API INTERRUPT SYS OFF
1760 6777 SVEC /AC = 7740
1761 7200 CLA /SET VECTOR ADDRESS = 7740
1762 6775 RVEC /AC = 0
1763 3002 DCA /READ VECTOR ADDRESS REGISTER INTO AC
1764 1002 TAD IMAGE
1765 7041 CIA /IMAGE OF AC
1766 1043 TAD /GET IMAGE
1767 7440 SZA /COMPLIMENT AND INC
1770 7402 HLT /IMAGE SHOULD = 1
1771 5772 JMP I /AC = 0
1772 2001 T33 /ERR
/TEST T33

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/TURN API ON
/TEST THAT VECTOR ADDRESS
/CANNOT BE ALTERED IN LINC MODE WITH API ON
/TURN API OFF
/TEST THAT VECTOR ADDRESS CAN BE ALTERED
/
      *2001
      CLA CLL          /CLEAR AC AND LINC
      JMS I            /SETUP API REGISTERS
      SVEC             /SET VECTOR ADDRESS REG = 0
      LINC             /LINC MODE
      LMODE
      IOB
      APION
      LDA I            /EXECUTE 8-MODE INST
      7740             /TURN API INTERRUPT SYS ON
      IOB              /AC =
      SVEC             /7740
      CLR              /EXECUTE 8-MODE INST
      IOB              /TRY TO SET VECTOR ADDRESS REG = 7740
      RVEC I           /CLEAR AC LINC AND MQ
      7741             /EXECUTE 8-MODE INST
      HLT              /READ VECTOR ADDRESS REGISTER INTO AC
      IOB              /AC =
      IOF              /7741
      CLR              /ERR
      LDA I            /EXECUTE 8-MODE INST
      7740             /TURN API INTERRUPT SYS OFF
      IOB              /CLEAR AC LINC AND MQ
      SVEC             /AC =
      CLR              /7740
      LDA I            /EXECUTE 8-MODE INST
      7740             /SET VECTOR ADDRESS REGISTER = 7740
      IOB              /CLEAR AC LINC AND MQ
      SVEC             /EXECUTE 8-MODE INST
      CLR              /READ VECTOR ADDRESS REGISTER INTO AC
      IOB              /AC =
      RVEC I           /1
      SAE I            /ERR
      1                /8-MODE
      PDP
      PMODE

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/EXECUTE IN 8 MODE
/TEST PUSHJ AND RES IOTS WITH API ON
/
T34,
2037 4407 JMS I CLRSTK /CLEAR STACK
2040 4406 JMS I SETUP /SET UP API REGISTERS
2041 7060 CML CMA /SET LINC = 1 AND AC = 7777
2042 3000 DCA I /LOC 0 = 7777
2043 3405 DCA I /LOC UT37A = 0
2044 6006 APION /TURN API INTERRUPT SYS ON
2045 6760 PJAI0 /PUSH JUMP WITH FIELD BITS = 0
2046 2053 TSTPJ2 /TO LOC TSTPJ2
2047 7402 HLT /ERR = PUSHJ SKIPPED
2050 7402 HLT
2051 0000 AND /RETURN FROM RESTORE IOT (DECODES AS HLT IN LINC MODE)
2052 9305 JMP /CONTINUE WITH TEST
2053 0000 AND TSTPJ2, AND /TEST THAT ARRIVAL FROM PUSHJ IOT WAS IN 8-MODE
2054 7440 SZA /AC = 0
2055 7402 HLT /ERR
2056 6774 RSTK /READ STACK ADDRESS REGISTER INTO AC
2057 3002 DCA IMAGE /IMAGE OF AC
2060 1002 TAD IMAGE
2061 7041 CIA /COMPLIMENT AND INC
2062 1115 TAD /IMAGE SHOULD = 2172
2063 7440 SZA /AC = 0
2064 7402 HLT /ERR
2065 1436 TAD I STACK+4 /GET UF IF AND OF STORED ON STACK BY PUSH JUMP IOT
2066 7041 CIA /COMPLIMENT AND INC
2067 1045 TAD K3 /STACK+4 SHOULD = 3
2070 7440 SZA /AC = 0
2071 7402 HLT /ERR
2072 7300 CLA CLL /CLEAR AC AND LINC
2073 1433 TAD I STACK+1 /GET PC STORED ON STACK BY PUSH JUMP IOT
2074 7041 CIA /COMPLIMENT AND INC
2075 1031 TAD PJPC2 /STACK+1 SHOULD = PJPC2
2076 7440 SZA /AC = 0
2077 7402 HLT /ERR
2100 1433 TAD I STACK+1 /GET PC STORED ON STACK BY PUSH JUMP IOT
2101 1044 TAD K2 /INCREMENT BY 2
2102 3433 DCA I STACK+1 /SOPE BACK IN LOC STACK+1
2103 6771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2104 7402 HLT /ERR - RESTORE SKIPPED
2105 7420 HLT /LINC = 1
2106 7402 HLT /ERR
2107 7440 SZA /AC = 0
2110 7402 HLT /ERR
2111 6774 RSTK /READ STACK ADDRESS REGISTER INTO AC
2112 3002 DCA IMAGE /IMAGE OF AC
2113 1002 TAD IMAGE /GET IMAGE
2114 7041 CIA /COMPLIMENT AND INC
2115 1116 TAD K2177 /IMAGE SHOULD = 2177
2116 7440 SZA /AC = 0
2117 7402 HLT /ERR

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2120 5721
2121 2200

/
/TEST MAINTENANCE IOT SIMULATION OF
/A LEVEL 0 INTERRUPT IN LINC MODE
/--FIRST TIME AN INTERRUPT HAS BEEN SIMULATED--
/

JMP I .+1 /JMP TO
T35 /TEST 35

2200
PMODE /CLEAR STACK
JMS I CLRSTK /GENERATE I0 PRESET
TAD IOPRES /AC = 7000 OR 8-MODE NOP
DCA I K7000 /ENABLE INTERRUPT
TAD TSTA /AC = 37
SMLV K37 /SET MACHINE LEVEL = 17
CLA CLL CMA /AC = 7777
DCA LOC /LOC 0 = 7777
TAD LOC /AC = 5600
SSTK K5600 /SET STACK ADDRESS REGISTER = 5600
CLA /AC = 0
TAD K6000 /AC = 6000
SVEC /SET VECTOR ADDRESS REGISTER = 6000
LINC /LINC MODE
LMODE
CLR /CLEAR AC LINK AND M0
LDA I /AC =
6000I135A*1 /JMP TO T35A*1
STA /STORE IN
1402 /LOC 1402 OF CURRENT INSTRUCTION FIELD
CLR /CLEAR AC LINK AND M0
FLO I /FLO = 0
HLT /ERR-FLO NOT = 0
IOB /EXECUTE 8-MODE IOT
APION /TURN API INTERRUPT SYS ON
LDA I /AC =
4000 /4000
IOB /EXECUTE 8-MODE INST
MAIN1 /SIMULATE API INTERRUPT = LEV 0
NOP /EXECUTES ONE MORE INST BEFORE INTERRUPT
HLT /ERR

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T35A,
7402
1460
4000
0000
0452
0000
0005
0261
0475
6252
1620
0001
1460
0000
0000
0474
0000
0002
6774
3002
1002
7041
1116
7440
7402
6773
3002
1002
7041
1155
7440
7402

JMP
SAE I
4000
HLT
LZE
HLT
QAC
ROL I
OL2 I
JMP
BSE I
1
SAE I
0
HLT
FLO I
HLT
PDP
PMODE
RSTK
DCA
YAD
CIA
TAD
SEA
HLT
RMLV
DCA
TAD
CIA
TAD
SEA
HLT

1
. *3

UT37

/RETURN FROM LEV 0 INTERRUPT IN LINC MODE
/AC =
/4000
/ERR-AC NOT = 4000
/LINC = 0
/ERR-LINC NOT = 0
/MO 0-10 TO AC 1=11
/ROTATE LEFT 1
/SKIP IF MO 11 = 1
/MO BIT 11 = 0
/SET AC BIT 0
/TO 1
/AC =
/0
/ERR-AC NOT = 0 THEREFORE MO WAS NOT = 0
/FLO = 0
/ERR-FLO NOT = 0
/8-MODE

/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INC
/IMAGE SHOULD = 2177
/AC = 0
/ERR

/READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INC
/IMAGE SHOULD = 7760
/AC = 0
/ERR

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1344 /
1345 /TEST NON EXECUTION OF MAINTENANCE IOTS
1346 /WITH API OFF AND FOR NO INTERRUPT
1347 /OCCURRING WHEN MAINTENANCE IOTS ARE ISSUED
1348 /WITH THE AC = 0 AND API ON
1349 /
1350 /
1351 /36,
1352 JMS I CLRSTK /CLEAR STACK
1353 JMS I SETUP /SET UP API REGISTERS
1354 DCA I UT16A /LOC UT16A = 0
1355 TAD K37 /AC = 37
1356 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
1357 CLA CLL CMA /AC = 7777
1358 MAIN1 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0 WITH API OFF
1359 NOP /DELAY
1360 NOP /DELAY
1361 MAIN2 /TRY TO SIMULATE AN API INTERRUPT WITH API OFF
1362 NOP /DELAY
1363 NOP /DELAY
1364 CLA CLL /AC = 0
1365 SMLV /SET MACHINE LEVEL = 0
1366 CLA CLL CMA /AC = 7777
1367 APION /TURN API INTERRUPT SYS ON
1368 MAIN1 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0
1369 NOP /MACHINE LEVEL OF 0 SHOULD PREVENT IT
1370 NOP /DELAY
1371 MAIN2 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 12
1372 NOP /MACHINE LEVEL OF 0 SHOULD PREVENT IT
1373 NOP /DELAY
1374 CLA CLL K37 /AC = 0
1375 TAD /AC = 37
1376 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
1377 CLA CLL /AC = 0
1378 MAIN1 /TRY TO SIMULATE AN API INTERRUPT
1379 NOP /AC = 0 (NO INTERRUPT LEVEL SET) SHOULD PREVENT IT
1380 NOP /DELAY
1381 MAIN2 /TRY TO SIMULATE AN API INTERRUPT
1382 NOP /AC = 0 (NO INTERRUPT LEVEL SET) SHOULD PREVENT IT
1383 JMP I .+1 /DELAY
1384 T37 /TEST T37

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//
//TEST INTERRUPTS TO EACH LEVEL
//
//
//TEST MAINTENANCE MODE SIMULATION OF
//A LEVEL 0 INTERRUPT TO LOC 6100 WITH MACHINE LEVEL = 17
//AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600-5604
//AFTER SERVICING INTERRUPT RESTOR IOT RETURNS
//PROGRAM SEQUENCE TO START OF T40 TEST
/
      2400
      4407
      3405
      4406
      1053
      6772
      7300
      1142
      3561
      6006
      1125
      6051
      7000
      7402
2400  *2400
2401  JMS I CLRSTK /CLEAR STACK
2402  DCA I UT37A /LOC UT37A = 0
2403  JMS I SETUP /SET UP API REGISTERS
2404  TAD K37 /AC = 37
2405  SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
2406  CLA CLL /CLEAR AC AND LINC
2407  TAD K7000 /AC = 7000 OR 8-MODE NOP
2408  DCA I VEC0 /ENABLE LEVEL 0 INTERRUPT TO LOC VECT0
2409  APION /TURN API INTERRUPT SYSTEM ON
2410  TAD K4000 /AC = 4000
2411  MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 0 INTERRUPT
2412  NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2413  NOP
2414  HLT /ERR
/

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```

//TEST MAINTENANCE MODE SIMULATION OF
//A LEVEL 1 INTERRUPT TO LOC 6102 WITH MACHINE LEVEL = 17
//AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
//AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
//RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T41 TEST
/
      7000
      4407
      1142
      3562
      1076
      6051
      7000
      7402
2415  NOP
2416  JMS I CLRSTK /CLEAR STACK
2417  TAD K7000 /AC = 7000 OR 8-MODE NOP
2418  DCA I VEC1 /ENABLE LEVEL 1 INTERRUPT TO LOC VECT1
2419  TAD K2000 /AC = 2000
2420  MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 1 INTERRUPT
2421  NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2422  HLT /ERR
/

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//TEST MAINTENANCE MODE SIMULATION OF
//A LEVEL 2 INTERRUPT TO LOC 6104 WITH MACHINE LEVEL = 17
//AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
//AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
//RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T42 TEST
/
      7000
      4407
      1142
      3563
      1071
      6051
      7000
      7402
2425  NOP
2426  JMS I CLRSTK /CLEAR STACK
2427  TAD K7000 /AC = 7000 OR 8-MODE NOP
2428  DCA I VEC2 /ENABLE LEVEL 2 INTERRUPT TO LOC VECT2
2429  TAD K1000 /AC = 1000
2430  MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 2 INTERRUPT
2431  NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2432  HLT /ERR
/

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1440 /
1441 /TEST MAINTENANCE MODE SIMULATION OF
1442 /A LEVEL 3 INTERRUPT TO LOC 6106 WITH MACHINE LEVEL = 17
1443 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1444 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
1445 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T43 TEST
1446 /
1447 T42,
1448 NOP 7000
1449 JMS I CLRSTK /CLEAR STACK
1450 TAD K7000 /AC = 7000 OR 8-MODE NOP
1451 DCA I VEC3 /ENABLE LEVEL 3 INTERRUPT TO LOC VECT3
1452 TAD K400 /AC = 400
1453 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 3 INTERRUPT
1454 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1455 HLT /ERR
1456
1457 /
1458 /TEST MAINTENANCE MODE SIMULATION OF
1459 /A LEVEL 4 INTERRUPT TO LOC 6110 WITH MACHINE LEVEL = 17
1460 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1461 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
1462 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T44 TEST
1463 /
1464 T43,
1465 NOP 7000
1466 JMS I CLRSTK /CLEAR STACK
1467 TAD K7000 /AC = 7000 OR 8-MODE NOP
1468 DCA I VEC4 /ENABLE LEVEL 4 INTERRUPT TO LOC VECT4
1469 TAD K200 /AC = 200
1470 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 4 INTERRUPT
1471 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1472 HLT /ERR
1473
1474 /
1475 /TEST MAINTENANCE MODE SIMULATION OF
1476 /A LEVEL 5 INTERRUPT TO LOC 6112 WITH MACHINE LEVEL = 17
1477 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1478 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
1479 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T45 TEST
1480 /
1481 T44,
1482 NOP 7000
1483 JMS I CLRSTK /CLEAR STACK
1484 TAD K7000 /AC = 7000 OR 8-MODE NOP
1485 DCA I VEC5 /ENABLE LEVEL 5 INTERRUPT TO LOC VECT5
1486 TAD K100 /AC = 100
1487 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 5 INTERRUPT
1488 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1489 HLT /ERR

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1486 /
1487 /TEST MAINTENANCE MODE SIMULATION OF
1488 /A LEVEL 6 INTERRUPT TO LOC 6114 WITH MACHINE LEVEL = 17
1489 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1490 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
1491 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T46 TEST
1492 /
1493 /
1494 /
1495 /
1496 /
1497 /
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1499 /
1500 /
1501 /
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1505 /
1506 /
1507 /
1508 /

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2465 7000 NOP
2466 4407 JMS I CLRSTK /CLEAR STACK
2467 1142 TAD K7000 /AC = 7000 OR 8-MODE NOP
2470 3567 DCA I VEC6 /ENABLE LEVEL 6 INTERRUPT TO LOC VECT6
2471 1054 TAD K40 /AC = 40
2472 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 6 INTERRUPT
2473 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2474 7402 HLT /ERR

```

```

/TEST MAINTENANCE MODE SIMULATION OF
/A LEVEL 7 INTERRUPT TO LOC 6116 WITH MACHINE LEVEL = 17
/AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
/AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
/RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T47 TEST
/
T45,

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2475 7000 NOP
2476 4407 JMS I CLRSTK /CLEAR STACK
2477 1142 TAD K7000 /AC = 7000 OR 8-MODE NOP
2500 3570 DCA I VEC7 /ENABLE LEVEL 7 INTERRUPT TO LOC VECT7
2501 1052 TAD K20 /AC = 20
2502 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 7 INTERRUPT
2503 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2504 7402 HLT /ERR

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/TEST MAINTENANCE MODE SIMULATION OF
/A LEVEL 8 INTERRUPT TO LOC 6120 WITH MACHINE LEVEL = 17
/AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600-5604
/AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
/RESTOR IOT RETURNS PROGRAM SEQUENCE TO T50 TEST
/
T46,

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2505 7000 NOP
2506 4407 JMS I CLRSTK /CLEAR STACK
2507 1142 TAD K7000 /AC = 7000 OR 8-MODE NOP
2510 3571 DCA I VEC10 /ENABLE LEVEL 8 INTERRUPT TO LOC VECT10
2511 1050 TAD K10 /AC = 10
2512 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 8 INTERRUPT
2513 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
2514 7402 HLT /ERR

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1532 /
1533 /TEST MAINTENANCE MODE SIMULATION OF
1534 /A LEVEL 9 INTERRUPT TO LOC 6122 WITH MACHINE LEVEL = 17
1535 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1536 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1537 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T51 TEST
1538 /
1539
1540 T50,
1541 2515 NOP
1542 2516 JMS I CLRSTK /CLEAR STACK
1543 2517 TAD K7000 /AC=7000 OR 8-MODE NOP
1544 2520 DCA I VEC11 /ENABLE LEVEL 9 INTERRUPT TO LOC VECT11
1545 2521 TAD K4 /AC=4
1546 2522 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 9 INTERRUPT
1547 2523 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1548 2524 HLT /ERR
1549
1550 /TEST MAINTENANCE MODE SIMULATION OF
1551 /A LEVEL 10 INTERRUPT TO LOC 6124 WITH MACHINE LEVEL=17
1552 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1553 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1554 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T52 TEST
1555 /
1556
1557 T51,
1558 2525 NOP
1559 2526 JMS I CLRSTK /CLEAR STACK
1560 2527 TAD K7000 /AC=7000 OR 8-MODE NOP
1561 2530 DCA I VEC12 /ENABLE LEVEL 10 INTERRUPT TO LOC VECT12
1562 2531 TAD K2 /AC=2
1563 2532 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 10 INTERRUPT
1564 2533 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1565 2534 HLT /ERR
1566
1567 /TEST MAINTENANCE MODE SIMULATION OF
1568 /A LEVEL 11 INTERRUPT TO LOC 6126 WITH MACHINE LEVEL=17
1569 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1570 /AFTER SERVICING THE INTERRUPT AND INC PC IN LOC STACKK+1
1571 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T53 TEST
1572 /
1573
1574 T52,
1575 2535 NOP
1576 2536 JMS I CLRSTK /CLEAR STACK
1577 2537 TAD K7000 /AC=7000 OR 8-MODE NOP
1578 2540 DCA I VEC13 /ENABLE LEVEL 11 INTERRUPT TO LOC VECT13
1579 2541 TAD K1 /AC=1
1580 2542 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 11 INTERRUPT
1581 2543 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1582 2544 HLT /ERR

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1578 /TEST MAINTENANCE MODE SIMULATION OF
1579 /A LEVEL 12 INTERRUPT TO LOC 6130 WITH MACHINE LEVEL=17
1580 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1581 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1582 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T54 TEST
1583
1584
1585

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2545 NOP          /CLEAR STACK
1586 JMS I         /AC=7000 OR 8-MODE NOP
1587 TAD          /ENABLE LEVEL 12 INTERRUPT TO LOC VECT14
1588 K7000
1589 DCA I       /AC=4
2550 VEC14
2551 K4
1590 TAD
1591 MAIN2
2552 /MAINTENANCE MODE SIMULATION OF LEVEL 12 INTERRUPT
1592 NOP          /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1593 HLT
2554 JMP I      ,+1
1594 T54
2555 /JMP TO
2556 /TEST T54

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1596
1597 /TEST MAINTENANCE MODE SIMULATION OF
1598 /A LEVEL 13 INTERRUPT TO LOC 6132 WITH MACHINE LEVEL=17
1599 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1600 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1601 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T54 TEST
1602
1603
1604

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2600 *2600
1605 NOP
2601 JMS I         /CLEAR STACK
1606 TAD          /AC=7000 OR 8-MODE NOP
2602 DCA I       /ENABLE LEVEL 13 INTERRUPT TO LOC VECT15
1607 VEC15
2603 K2
1608 TAD
2604 MAIN2
1609 /MAINTENANCE MODE SIMULATION OF A LEVEL 13 INTERRUPT
2605 NOP          /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1610 HLT
2606
2607

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1611
1612 /TEST MAINTENANCE MODE SIMULATION OF
1613 /A LEVEL 14 INTERRUPT TO LOC 6134 WITH MACHINE LEVEL=17
1614 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1615 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1616 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T56 TEST
1617
1618
1619

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2610 NOP
1619 JMS I         /CLEAR STACK
1620 TAD          /AC=7000 OR 8-MODE NOP
1621 DCA I       /ENABLE LEVEL 14 INTERRUPT TO LOC VECT16
2613 VEC16
1622 K1
2614 /AC=1
1623 /MAINTENANCE MODE SIMULATION OF A LEVEL 14 INTERRUPT
2615 NOP          /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1626 HLT
2616
2617

```

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/ MAINTENANCE MODE SIMULATION OF MULTIPLE LEVEL INTERRUPTS
/ TESTS THAT HIGHEST PRIORITY INTERRUPT IS ACCEPTED
/ VECTOR ADDRESS REGISTER IS SET TO 6300
/ A MAXIMUM OF 16 OCTAL LEVELS OF INTERRUPTS ARE SAVED ON THE STACK
/
T56,
2620 7300      CLA CLL
2621 1134      TAD      K5577
2622 3010      DCA      10
2623 1152      TAD      K7600
2624 3011      DCA      11
2625 3410      DCA I   10
2626 2011      ISZ     11
2627 5225      JMP      .+2
2628 4406      JMS I   SETUP
2629 1053      TAD      K37
2630 6772      SMLV
2631 7200      CLA
2632 1141      TAD      K6300
2633 6777      SVEC
2634 7300      CLA CLL
2635 6775      RVEC
2636 3002      DCA
2637 1002      TAD
2638 7041      CIA
2639 1072      TAD
2640 7440      SZA
2641 7402      HLT
2642 1142      TAD
2643 3671      DCA I   MV16
2644 1043      TAD      K1
2645 0006      APION
2646 6052      MAIN2
2647 7000      NOP
2648 7402      HLT
2649 7300      CLA CLL
2650 6774      RSTK
2651 3002      DCA
2652 1002      TAD
2653 7041      CIA
2654 1116      TAD
2655 7440      SZA
2656 7402      HLT
2657 1150      TAD
2658 3671      DCA I   MV16
2659 3670      JMP I   .+1
2660 3000      T57
2661 6400      MV16,
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/AC=0
/AC=5577
/LOC 10=5577
/AC=7600
/LOC11=7600
/CLEAR STACK STARTING WITH LOCATION 5600
/DONE 200 TIMES
/NO-DO IT AGAIN
/SET UP API REGISTERS
/AC=37
/CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
/AC=0
/AC = 6300
/SET VECTOR ADDRESS REGISTER=6400
/CLEAR AC AND LINC
/READ VECTOR ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 1441
/AC=0
/ERR
/AC=7000 OR 8-MODE NOP
/ENABLE LEVEL 16 INTERRUPT TO LOC MLV16
/AC=1
/TURN API INTERRUPT SYSTEM ON
/MAINTENANCE MODE SIMULATION OF A LEVEL 14 INTERRUPT
/EXECUTES 1 MORE INSTRUCTION AFTER MAIN2 IOT
/ERR
/AC=0--RESTORE OCCURS TO HERE FROM MV16 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD=2177
/AC=0
/ERR
/AC=7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV16
/JMP TO
/TEST T57

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1677 /
1678 /TEST THAT DJR LIF AND CIF INSTRUCTIONS
1679 /WHEN ISSUED WILL PREVENT API INTERRUPTS
1680 /UNTIL A JMP INSTRUCTION IS EXECUTED
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3000 *3000 /CLEAR AC AND LINC
3001 CLA CLL /CLEAR STACK
3002 JMS I CLRSTK /SET UP API REGISTERS
3003 JMS I SETUP /AC=37
3004 TAD K37 /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
3005 SMLV /AC=7777 /LINC MODE
3006 CLA CLL CMA /TURN API INTERRUPT SYSTEM ON
3007 APION /LINC MODE
3010 LMODE /DISABLE JMP RETURN
3011 DJR /TRY TO EXECUTE 8-MODE INST
3012 IOB /TRY TO SIMULATE API INTERRUPT TO LEVEL 0
3013 MAIN1 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3014 NOP /DJR SHOULD PREVENT INTERRUPT
3015 0016 /EXECUTE 8-MODE INST
3016 0500 IOB /TRY TO SIMULATE API INTERRUPT TO LEVEL 12
3017 0052 MAIN2 /EXECUTE 1 MORE INST AFTER MAIN2 IOT
3020 0016 NOP /DJR SHOULD PREVENT INTERRUPT
3021 1020 LDA I /AC=
3022 7000 /7000 OR 8-MODE NOP
3023 1040 STA /STORE IN LOC
3024 6140 VECT0 /VECT0
3025 7026 JMP /ENABLE INTERRUPTS BY ISSUING A JMP INST
3026 0016 NOP /EXECUTE 1 INST THAN INTERRUPT TO LEVEL 0 (HIGHEST PRIORITY)
3027 0000 HLT /ERR-DID NOT INTERRUPT
3030 0601 LIF /SET INSTRUCTION FIELD 1--RETURN AFTER RESTORE IOT
3031 1020 LDA I /AC=
3032 7777 7777 /7777
3033 0500 IOB /EXECUTE AN 8-MODE INST
3034 0001 MAIN1 /TRY TO SIMULATE API INTERRUPT TO LEVEL 0
3035 0016 NOP /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3036 0016 LIF /LIF INST SHOULD PREVENT INTERRUPT
3037 0500 IOB /EXECUTE AN 8-MODE INST
3040 0052 MAIN2 /TRY TO SIMULATE API INTERRUPT TO LEVEL 12
3041 0016 NOP /EXECUTE 1 MORE INST AFTER MAIN2 IOT
3042 0016 LIF /LIF INST SHOULD PREVENT INTERRUPT
3043 7044 JMP /2 JMP INSTRUCTIONS ARE NECESSARY AFTER A LIF INST IN LINC MODE
3044 0016 NOP /TO ENABLE THE INTERRUPTS AGAIN
3045 1020 LDA I /AC=
3046 7000 /7000 OR AN 8-MODE NOP

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1724						
1725	3047	1040	STA			/STORE IN LOC
1726	3050	6140	VECT0			/VECT0
1727	3051	7052	JMP	.+1		/ENABLE INTERRUPTS BY ISSUING A JMP INST
1728	3052	0000	HLT			/ERR-DID NOT INTERRUPT IMMEDIATELY AFTER JMP
1729	3053	0002	PDP			/8-MODE
1730			PMODE			
1731	3054	7340	CLA CLL CMA			/AC=7777
1732	3055	6202	CIF			/CHANGE INSTRUCTION FIELD
1733	3056	6051	MAIN1			/TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0
1734	3057	7000	NOP			/EXECUTE 1 MORE INST AFTER MAIN1 IOT
1735	3060	7000	NOP			/CIF INST SHOULD PREVENT INTERRUPT
1736	3061	6052	MAIN2			/TRY TO SIMULATE AN API INTERRUPT TO LEVEL 12
1737	3062	7000	NOP			/EXECUTE 1 MORE INST AFTER MAIN2 IOT
1738	3063	7000	NOP			/CIF INST SHOULD PREVENT INTERRUPT
1739	3064	1142	TAD	K7000		/AC=7000 OR 8-MODE NOP
1740	3065	3561	DCA I	VECT0		/STORE IN LOC VECT0
1741	3066	0267	JMP	.+1		/ENABLE INTERRUPTS BY ISSUING A JMP INST
1742	3067	7000	NOP			/EXECUTE 1 MORE INST THAN INTERRUPT TO LEVEL 0 (HIGHEST PRIORITY)
1743	3070	7402	HLT			/ERR

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/TEST THAT A DJR INSTRUCTION WHEN ISSUED AND
/NORMAL OPERATION IS REENABLED WITH A JMP INST
/WILL ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
/
T60,          CLA CLL          /CLEAR AC AND LINC
              JMS I          CLRSTK    /CLEAR STACK
              JMS I          SETUP    /SET UP API REGISTERS
              TAD           K37      /AC=37
              SMLV          /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
              CLA           /AC=0
              APION         /TURN API INTERRUPT SYSTEM ON
              LINC         /LINC MODE
              LMODE
              IOB           /EXECUTE 8-MODE INST
              PJAI0        /PUSH JUMP WITH FIELD BITS=0
              2000:160A    /TO LOC T60A
              HLT          /ERR--RESTORE IOT RET PROG SEQ TO THIS LOC AFTER ALTERED TO A NOP
              PDP          /8-MODE
              PHODE
              CLA          /AC = 0
              DCA         /CHANGE LOC .-3 BACK TO A LINC MODE HLT
              JMP I       .+1
              T61        /TEST T61
              LMODE
              CLR         /ARRIVE HERE FROM PUSH JUMP
              POP        /8-MODE
              PHODE
              TAD I      STACK+1  /GET PC STORED IN STACK+1
              CIA       T60AM6   /COMPLIMENT AND INC
              TAD       /PC STORED IN STACKK+1 SHOULDDET60A-6
              SZA       /AC=0
              HLT      /ERR
              LINC     /LINC MODE
              LMODE
              DJR      /DISABLE JUMP RETURN
              LDA I   /AC=
              16     /16 OR A LINC MODE NOP
              STA     /STORE IN LOC
              T60A-6 /T60A-6
              JMP     .+1       /ENABLE RESTORE IOT BY ISSUING A JMP INST
              IOB    /EXECUTE 8-MODE INST
              RES    /RESTORE
              HLT   /ERR
              PHODE
              T60AM6, T60A-6

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1792 /
1793 /TEST THAT A LIF INSTRUCTION WHEN ISSUED
1794 /AND NORMAL OPERATION IS REENABLED WITH A JMP INST
1795 /WILL ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
1796 /
1797
1798 *3200
1799 CLA CLL /CLEAR AC AND LINC
1800 JMS I CLRSTK /CLEAR STACK
1801 JMS I SETUP /SET UP API REGISTERS
1802 TAD K37 /AC=37
1803 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
1804 CLA /AC=0
1805 APION /TURN API INTERRUPT SYSTEM ON
1806 LINC /LINC MODE
1807 LMODE
1808
1809 /EXECUTE 8-MODE INST
1810 PJA I0 /PUSH JUMP WITH FIELD BITS=0
1811 2000:T61A /TO LOC T61A
1812 HLT /ERR--RESTORE IOT RET PROG SEQ TO THIS LOC AFTER ALTERED BY A NOP
1813 PDP /8-MODE
1814
1815 /AC = 0
1816 DCA /CHANGE LOC .-3 BACK TO A LINC MODE HLT
1817 JMP T62 /EXECUTE TEST T62
1818
1819 /ARRIVE HERE FROM PUSH JUMP
1820 /8-MODE
1821
1822 STACK+1 /GET PC STORED IN LOC STACKK+1
1823 CIA /COMPLIMENT AND INC
1824 TAD T61AM5 /PC STORED IN LOC STACKK+1 SHOULD BE T61A-3
1825 SZA /AC=0
1826 HLT /ERR
1827 LINC /LINC MODE
1828 LMODE
1829
1830 /LOAD INSTRUCTION FIELD 1
1831 LIF .+1 /2 JMP INSTRUCTIONS ARE NECESSARY AFTER A LIP INST IN LINC MODE
1832 JMP .+1 /TO ENABLE RESTORE IOT
1833 NOP /AC =
1834 LDA I /16 OR A LINC MODE NOP
1835 STA /STORE NOP IN LOC
1836 T61A-5 /T61A-5
1837 JMP .+1 /ENABLE RESTOR IOT BY DOING A JMP INST
1838 IOB /EXECUTE 8-MODE INST
1839 RES /RESTORE
1840 HLT /ERR

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/TEST THAT A CIF INSTRUCTION WHEN ISSUED
/AND NORMAL OPERATION IS REENABLED WITH A JMP INST
/WILL ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
/

T62,
3243 7300 PMODE
3244 4407 CLA CLL /CLEAR AC AND LINC
3245 4406 JMS I CLRSTK /CLEAR STACK
3246 1053 JMS I SETUP /SET UP API REGISTERS
3247 6772 TAD K37 /AC=37
3250 7200 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
3251 6006 CLA /AC=0
3252 6760 APION /TURN API INTERRUPT SYSTEM ON
3253 3262 PJAI0 /EXECUTE A PUSHJ IOT WITH FIELD BITS=0
3254 7402 T62A /TO LOC T62A
3255 7200 HLT /ERR
3256 1150 CLA /RESTOR IOT RETURNS PROGRAM SEQUENCE TO THIS POINT
3257 3254 TAD K7402 /AC = 7402 OR AN 8-MODE HLT
3260 5661 DCA .-3 /STORE IN LOC .-3
3261 3403 JMP I .+1 /CONTINUE WITH
3262 7300 T63 /TEST T63
3263 1433 CLA CLL /ARRIVE HERE FROM PUSHJ IOT
3264 7041 TAD I STACK+1 /GET PC STORED IN LOC STACKK+1
3265 1276 CIA /COMPLIMENT AND INC
3266 6202 TAD T62AM6 /PC STORED IN LOC STACKK+1 SHOULD=762A-6
3267 7300 CIF CLL /CHANGE INSTRUCTION FIELD
3270 1142 TAD K7000 /AC = 7000 OR AN 8-MODE NOP
3271 3254 DCA T62A-6 /DEPOSIT IN LOC T62A-6
3272 5273 JMP .+1 /ENABLE RESTORE IOT BY DOING A JMP INST
3273 6771 RES /RESTORE
3274 7402 HLT /ERR

T62A,
3275 3213 /T61AM5, T61A-5
3276 3254 /T62AM6, T62A-6

/IF AN 0 MODE HALT WERE ENCOUNTERED IN
/LINC MODE IT WOULD DECODE AS A JMP TO
/LOC 1402 OF THE CURRENT LINC INSTRUCTION FIELD
/THUS TO HALT THE PROGRAM IN THIS EVENT
/WE SET LOC 1402 = 0 A LINC MODE HALT

3402 *3402
0 LMODE
0 PMODE

UT37,
3402 0000

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3420

5403
7300
4407
4406
6006
1053
6772
7200
1142
3501
1125
6001
7410
7402
7402

*3403
CLA CLL
JMS I CLRSTK
JMS I SETUP
APION
TAD K37
SMLV
CLA
TAD K7000
DCA I VEC0
TAD K4000
MAIN1
SKP
HLT

/CLEAR AC AND LINC
/CLEAR STACK
/SET UP API REGISTERS
/TURN API INTERRUPT SYSTEM ON
/AC = 37
/CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
/AC = 0
/AC = 7000 OR AN 8-MODE NOP
/ENABLE LEVEL 0 INTERRUPT TO LOC VECT0
/AC = 4000
/MAINTENANCE MODE SIMULATION OF A LEVEL 0 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN1 IOT
/ERR-STATUS OF SKIP FLIP FLOP WAS NOT SAVED

/TEST THAT STATUS OF SKIP FLIP FLOP IS SAVED
/WHEN AN INTERRUPT OCCURS AT THE SAME TIME AS A SKIP INSTRUCTION
/
T63,

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3421 7300
 3422 4407
 3423 4406
 3424 6006
 3425 1123
 3426 6777
 3427 7340
 3430 3355
 3431 1047
 3432 3356
 3433 3010
 3434 7300
 3435 1147
 3436 3011
 3437 1010
 3440 6772
 3441 4424
 3442 1025
 3443 0355
 3444 6051
 3445 7000
 3446 7000
 3447 7200
 3450 1026
 3451 0356
 3452 6052
 3453 7000
 3454 7000
 3455 2011
 3456 0241
 3457 7300
 3460 1355
 3461 7010
 3462 3355
 3463 7430
 3464 5273
 3465 7300
 3466 1356
 3467 7010
 3470 3356
 3471 7420
 3472 5336
 3473 7300
 3474 2010
 3475 5234
 3476 7402

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/
/ RUN RANDOM NUMBER PATTERN WITH MAIN1 AND MAIN2 IOTS
/ TO TEST THAT INHIBIT OF INTERRUPTS BY THE SMLV IOT
/ WORKS FOR A RANDOM SELECTION OF NUMBERS
/ 400 RANDOM NUMBER COMBINATIONS WILL BE TESTED FOR
/ EACH POSSIBLE SETTING OF THE MACHINE LEVEL REGISTER
/
T64,
  CLA CLL
  JMS I CLRSTK
  JMS I SETUP
  APION K3000
  TAD
  SVEC
  CLA CLL CMA
  DCA MASK1
  TAD K7
  DCA MASK2
  DCA LOC 10 = 0
  CLA CLL
  TAD K7400
  DCA 11
  TAD 10
  SMLV
  JMS I RAN
  TAD RAN1
  AND MASK1
  MAIN1
  NOP
  NOP
  CLA
  TAD RAN2
  AND MASK2
  MAIN2
  NOP
  NOP
  ISE
  JMP MORE=5
  CLA CLL
  TAD
  RAR
  DCA
  SEL
  JMP
  CLA CLL
  TAD
  RAR
  DCA
  SNL
  JMP
  CLA CLL
  ISE
  JMP
  HLT
  /CLEAR AC AND LINC
  /CLEAR STACK
  /SET UP API REGISTERS
  /TURN API INTERRUPT SYSTEM ON
  /AC = 3500
  /SET VECTOR ADDRESS REGISTER = 3500
  /AC = 7777
  /MASK FOR RAN1
  /AC = 7
  /MASK FOR RAN2
  /LOC 10 = 0
  /CLEAR AC AND LINC
  /AC = 7400
  /LOC 11 = 7400
  /AC = LOC 10
  /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = LOC 10
  /GENERATE RANDOM NUMBERS RAN1 AND RAN2
  /AC = RAN1
  /MASK
  /TRY TO SIMULATE A MAINTENANCE MODE INTERRUPT
  /AC = 0
  /AC = RAN2
  /MASK
  /TRY TO SIMULATE A MAINTENANCE MODE INTERRUPT
  /INC LOC 11--IF 0 DONE 400 RANDOM COMBINATIONS ON PARTICULAR MACHINE LEVEL SETTING
  /NOT DONE
  /CLEAR AC AND LINC
  /AC = MASK1
  /ROTATE AC RIGHT 1
  /STORE BACK IN LOC MASK1
  /SKIP IF LINC = 0
  /DO NOT ALTER MASK2 YET
  /CLEAR AC AND LINC
  /AC = MASK2
  /ROTATE AC RIGHT 1
  /STORE BACK IN LOC MASK2
  /SKIP IF LINC = 1
  /DONE GO TO TEST T65
  /CLEAR AC AND LINC
  /INCREMENT LOC 10
  /CHANGE MACHINE LEVEL AND DO AGAIN
  /SHOULD NEVER GET HERE

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 2010

/ VECTOR ADDRESS POINTER TABLE
 / USED WITH TEST T65
 / SHOULD NEVER GET AN INTERRUPT
 /

3500	*3500		
7402	HLT		
3501	HLT		
7402	HLT		
3502	HLT		
7402	HLT		
3503	HLT		
7402	HLT		
3504	HLT		
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3507	HLT		
7402	HLT		
3510	HLT		
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3511	HLT		
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3512	HLT		
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3513	HLT		
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7402	HLT		
3516	HLT		
7402	HLT		
3517	HLT		
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3527	HLT		
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3530	HLT		
7402	HLT		
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7402	HLT		
3532	HLT		
7402	HLT		
3533	HLT		
7402	HLT		
3534	HLT		
7402	HLT		
3535	HLT		
7402	HLT		
765,	CLA CLL	/CLEAR AC AND LINC	
	LINC	/LINC MODE	
	LMODE		
7300	SNS 0	/SENSE SWITCH 0 # 1	
6141	JMP T66	/NO-GO TO NEXT TEST	
0440	PDP	/CHANGE TO 8-MODE	
7545	PMODE		
0002	JMS 1	/GO TO EXTME (EXTENDED MEMORY TEST SECTION) ROUTINE	
4757	JMP T66	/GO TO TEST T66	
3536			
3537			
3540			
3541			
3542			
3543			
3544			
9345			

/ UPPER MEMORY INTERRUPT AND PUSH JUMP-RESTORE TEST
 / ONLY EXECUTES IF SNS SW 0 IS SET AND
 / RSW BITS 0-11 NOT = 0 OR SET TO NUMBER OF SEQUENTIAL 4K SECTIONS TO BE TESTED
 /


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2088
2089
2090 4043 1775 TAD I EALTPJ /AC=LOC EALTPJ
2091 4044 0156 TAD K7770 /MASK OFF BITS 9-11
2092 4045 1366 TAD EXTCTR /ADD LOC EXTCTR
2093 4046 3775 DCA I EALTPJ /DEPOSIT IN LOC EALTP
2094 4047 1366 TAD EXTCTR /AC=LOC EXTCTR
2095 4050 7006 RTL /ROTATE LEFT 2
2096 4051 7004 RAL /ROTATE LEFT 1
2097 4052 3773 DCA I EIFI /DEPOSIT IN LOC EIF
2098 4053 1772 TAD I EXXXI /AC=LOC EXXX
2099 4054 1001 TAD K200 /ADD 200 TO AC
2100 4055 3772 DCA I EXXXI /DEPOSIT IN LOC EXXX
2101 4056 1774 TAD I EFLOI /AC=LOC EFLO
2102 4057 1363 TAD K1100 /ADD 1100 TO AC
2103 4060 3774 DCA I EFLOI /DEPOSIT IN LOC EFLO
2104 4061 1364 TAD K0377 /AC=4377
2105 4062 3010 DCA I 10 /DEPOSIT IN LOC 10
2106 4063 1364 TAD K0377 /AC=4377
2107 4064 3011 DCA I 11 /DEPOSIT IN LOC 11
2108 4065 1192 TAD K7600 /AC=7600
2109 4066 3012 DCA I 12 /DEPOSIT IN LOC 12
2110 4067 1364 TAD K0377 /AC=4377
2111 4070 3013 DCA I 13 /DEPOSIT IN LOC 13
2112 4071 1366 TAD EXTCTR /AC=LOC EXTCTR
2113 4072 7006 RTL /ROTATE LEFT 2
2114 4073 7004 RAL /ROTATE LEFT 1
2115 4074 1371 TAD K00F /ADD LOC K00F TO AC
2116 4075 3304 DCA I ALT0 /DEPOSIT CREATED COP INST IN LOC ALT0
2117 4076 1304 TAD ALY0 /AC = LOC ALT0
2118 4077 3340 DCA I ALT1 /DEPOSIT CREATED COP INST IN LOC ALT1
2119 4100 1304 TAD ALT0 /AC = LOC ALT0
2120 4101 3344 DCA I ALT2 /DEPOSIT CREATED COP INST IN LOC ALT2
2121 4102 7300 CLA GLL /CLEAR AC AND LINC
2122 4103 1410 TAD I 10 /AC = CONTENTS OF ADD SPECIFIED BY LOC 10
2123 4104 0000 DCA I 11 /CREATED COP INST
2124 4105 3411 DCA I 11 /DEPOSIT AC IN LOC SPECIFIED BY CONTENTS LOC 11 AND DATA FIELD SEL
2125 4106 0201 COP /SET DF = 0
2126 4107 3413 DCA I 13 /DEPOSIT AC IN ADDRESS +1 SPECIFIED BY LOC 13
2127 4110 2012 ISB 12 /INCREMENT LOC 12 AND SKIP IF = 0
2128 4111 3302 JMP .-7 /LOC 12 NOT = 0--MOVE NEXT LOC TO FIELD TO BE TESTED
2129 4112 0006 APION /TURN API INTERRUPT SYSTEM ON
2130 4113 1125 TAD K4000 /AC=4000
2131 4114 0051 MAIN1 /SIMULATE AN API INTERRUPT TO LEVEL 0
2132 4115 7000 NOP /EXECUTE 1 MORE INST AFTER MAIN1 IOY
2133 4116 7402 HLT /ERR-MAIN1 IOY DID NOT EXECUTE

```


2134	4117	7300	EXTRET,	CLA CLL				/CLEAR AC AND LINC--RESTORE FROM EXTENDED FIELD OCCURS TO HERE
2135	4120	6774		RSTK				/READ STACK ADDRESS REGISTER INTO AC
2136	4121	3022		DCA	IMAGE			/IMAGE OF AC
2137	4122	1022		TAD	IMAGE			/GET IMAGE
2138	4123	7001		IAC				/INCREMENT
2139	4124	1367		TAD	EXTX			/STACK ADDRESS SHOULD=EXTX
2140	4125	7440		SEA				/AC=0
2141	4126	7402		HLT				/ERR--STACK ADDRESS REG NOT=3236
2142	4127	1364		TAD	K4377			/AC=4377
2143	4130	3010		DCA	10			/LOC 10 = 4377
2144	4131	1364		TAD	K4377			/AC = 4377
2145	4132	3011		DCA	11			/LOC 11=4377
2146	4133	1152		TAD	K7600			/AC=7600
2147	4134	3012		DCA	12			/LOC 10=7600
2148	4135	1364		TAD	K4377			/AC=4377
2149	4136	3013		DCA	13			/LOC 13=4377
2150	4137	7300		CLA CLL				/CLEAR AC AND LINC
2151	4140	0000	ALT1,	0000 I	10			/CHANGE DATA FIELD TO EXTENDED MEMORY FIELD JUST TESTED
2152	4141	1410		TAD I				/LOAD AC WITH LOC OF SEGMENT OF PROGRAM TO FIELD JUST TESTED
2153	4142	6201		GDP				/CHANGE TO DATA FIELD 0
2154	4143	3411		DCA I	11			/DEPOSIT AC IN ADDRESS+1 SPECIFIED BY LOC 11
2155	4144	0000	ALT2,	0000 I	13			/CHANGE DATA FIELD TO EXTENDED MEMORY FIELD JUST TESTED
2156	4145	3413		DCA I				/CLEAR EXTENDED MEMORY LOC+1 SPECIFIED BY LOC 13
2157	4146	6201		GDP				/CHANGE TO DATA FIELD 0
2158	4147	2012		ISE	12			/INC LOC 12 AND SKIP IF = 0
2159	4150	9340		JMP	ALT1			/LOC 12 NOT = 0--DO AGAIN
2160	4151	1366		TAD	EXTCTR			/AC = LOC EXTCTR
2161	4152	7001		IAC				/INCREMENT
2162	4153	3366		DCA	EXTCTR			/DEPOSIT IN LOC EXTCTR
2163	4154	2365		ISE	EXTCIA			/INCREMENT LOC EXTCIA AND SKIP IF = 0
2164	4155	9235		JMP	EXTGO			/LOC EXTCIA NOT = 0--TEST ANOTHER MEMORY FIELD
2165	4156	6141		LINC				/LINC MODE
2166	4157	0643		LMODE	3			/SET DATA FIELD = 3
2167	4160	0002		LDF				/0--MODE
2168	4161	4777		POP				
2169	4162	5601		PNODE	FLD1PT			/RETURN TO MAIN PROGRAM IN TEST T65
2170	4163	1100		JMS I	EXTMEM			
2171	4164	4377		JMP I				
2172	4165	0000		EXTSTK				
2173	4166	0000		EXTCTR,				
2174	4167	4542		EXTX,				
2175	4170	0000		KMLV,				
2176	4171	6201		KCDF,				
2177	4172	4571		EXXXI,				
2178	4173	4574		EIFI,				
2179	4174	4572		EFLDI,				
2180	4175	4502		EALTPJ,				
2181	4176	4542		EXTSTK,				
2182	4177	4200		FLD1PT,				

```

/TEST STACK INCREMENTING THROUGH ALL 4096 LOC
/OF MEMORY FIELD 1
/THE STACK ADDRESS REGISTER IS INITIALLY SET TO 0000
/WITH THE STACK FIELD BITS SET TO 1
/AFTER EACH PUSH JUMP IN FIELD 0 THE MACHINE STATUS
/SAVED ON THE STACK IS VERIFIED
/
*4200
FLD1P, 0
CLA CLL
IOF
TAD K1020
SMLV
CLA
SSTK K6000
TAD
SVEC
CLA CMA 10
OGA 11
LINC
LMODE
CLR
POP
PHODE
CLA CLL 11
TAD K9
TAD K9 11
OGA 6760
PJA10
ESTKT
SEA
HLT
SEL
HLT
RSTK
OGA
TAD
TAD
TAD
SEA
HLT
CLR
LINC
LMODE
FLO I
HLT
GAC
ROL I 1
GLE I
JMP
BSE I 1
1
4200 0000
4201 7300
4202 6002
4203 1361
4204 6772
4205 7200
4206 6776
4207 1137
4210 6777
4211 7240
4212 3010
4213 3011
4214 6141
4215 0011
4216 0002
4217 7300
4220 1011
4221 1397
4222 3011
4223 6760
4224 4302
4225 7440
4226 7402
4227 7430
4230 7402
4231 6774
4232 3002
4233 1002
4234 1043
4235 1011
4236 7440
4237 7402
4240 7100
4241 6141
4242 0474
4243 0000
4244 0005
4245 0261
4246 0475
4247 6252
4250 1620
4251 0001
1
/CONTAINS RETURN JMP ADD TO EXTREM ROUTINE
/CLEAR AC AND LINC
/TURN ALL INTERRUPT SYSTEMS OFF
/AC=1020
/SET STACK FIELD BITS = 1
/AC = 0
/SET STACK ADDRESS REGISTER = 0
/AC = 6000
/SET VECTOR ADDRESS REGISTER = 6000
/AC = 7777
/LOC 10 = 7777
/LOC 11 = 0000
/LINC MODE
/CLEAR AC LINC AND MG
/8-MODE
/CLEAR AC AND LINC
/AC = CONTENTS OF LOC 11
/ADD 5
/DEPOSIT BACK IN LOC 11
/PUSH JUMP IN FIELD 0
/TO LOC ESTKT
/AC = 0--RESTORES OCCURE TO HERE--
/ERR-AC NOT = 0
/LINC = 0
/ERR-LINC NOT = 0
/READ STACK ADDRESS REGISTER INTO AC
/AC TO LOC IMAGE
/GET IMAGE
/ADD 1
/STACK ADD SHOULD = CONTENTS LOC 11
/AC = 0
/ERR-STACK ADD NOT = CONTENTS LOC 11
/CLEAR LINC
/LINC MODE
/FLO = 0
/ERR-FLO NOT = 0
/MG 0-10 TO AC 1-11
/ROTATE LEFT 1
/SKIP IF MG 11 = 1
/CONTINUE-AC = MG
/SET AC BIT 0
/TO 1

```

2243	4252	1460	SAE I	/SKIP IF AC
2244	4253	0000	0	/E 0
2245	4254	0000	HLT	/ERR-MQ RESTORED FROM STACK NOT = 0
2246	4255	0500	IOB	/EXECUTE 8-MODE INST
2247	4256	0224	RIF	/READ INSTRUCTION FIELD
2248	4257	1460	SAE I	/AC =
2249	4260	0004	4	/4
2250	4261	0000	HLT	/ERR-INSTRUCTION FIELD 2 NOT SET
2251	4262	0011	CLR	/CLEAR AC LINC AND MQ
2252	4263	0500	IOB	/EXECUTE 8-MODE INST
2253	4264	0214	ROF	/READ DATA FIELD
2254	4265	1460	SAE I	/AC =
2255	4266	0006	6	/6
2256	4267	0000	HLT	/ERR-DATA FIELD 0 NOT SET
2257	4270	0011	CLR	/CLEAR AC LINC AND MQ
2258	4271	0002	RDP	/B-MODE
2259			PHODE	
2260	4272	0774	RSTK	/READ STACK ADDRESS REG INTO AC
2261	4273	3002	DCA	/IMAGE OF AC
2262	4274	1002	TAD	/GET IMAGE
2263	4275	1043	TAD	K1
2264	4276	1011	TAD	11
2265	4277	7440	SEA	/CONTENTS OF LOC 11 SHOULD=STACK ADD
2266	4300	7402	HLT	/AC=0
2267	4301	9347	HLT	/ERR-STACK ADD NOT = CONTENTS LOC 11
2268	4302	0774	JMP	/RESTOR ANOTHER LEVEL OF STATUS ON STACK
2269	4303	3002	RSTK	/READ STACK ADDRESS REGISTER INTO AC
2270	4304	1002	DCA	/IMAGE OF AC
2271	4305	1043	TAD	/GET IMAGE
2272	4306	1011	TAD	ADD 1
2273	4307	7440	TAD	/CONTENTS OF LOC 11 SHOULD = STACK ADD
2274	4310	7402	SEA	/AC = 0
2275	4311	0211	HLT	/ERR-STACK ADD NOT = CONTENTS LOC 11
2276	4312	1410	ODF	/CHANGE DATA FIELD TO 1
2277	4313	7440	TAD I	/AC FROM STACK TO AC
2278	4314	7402	SEA	/AC = 0
2279	4315	1410	HLT	/ERR-AC STORED ON STACK NOT = 0
2280	4316	3002	TAD I	/PC FROM STACK TO AC
2281	4317	1002	DCA	/IMAGE OF AC
2282	4320	7041	TAD	/GET IMAGE
2283	4321	1363	CIA	/COMPLIMENT AND INCREMENT
2284	4322	7440	TAD	/ADDRESS OF RET PC ON STACK SHOULD = ERES
2285	4323	7402	SEA	/AC = 0
2286	4324	1410	HLT	/ERR-RETURN PC ON STACK NOT = ADD ERES
2287	4325	7440	TAD I	/MODE FLO LINC AND MACH LEVEL FROM STACK TO AC
2288	4326	7402	SEA	/AC = 0
2289	4327	7440	HLT	/ERR-EXAMINE AC
2290	4327	1410	TAD I	/MQ FROM STACK TO AC
2291	4331	7402	SEA	/AC = 0
2292	4332	1410	HLT	/ERR-MQ STORED ON STACK NOT = 0
2293	4333	3002	TAD I	/UP IF AND DF FROM STACK TO AC
2294	4334	1002	DCA	/IMAGE OF AC
2295	4335	7041	TAD	/GET IMAGE
2296	4336	1360	CIA	/COMPLIMENT AND INC
2297	4337	7440	TAD	/LOC IMAGE SHOULD = 103
			SEA	/AC = 0

NAME OF PAGE W/AC-C
% CBT - FA

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2298 4340 7402 HLT /ERR-EXAMINE LOC IMAGE
2299 4341 6201 CDF /CHANGE TO DATA FIELD 0
2300 4342 7300 CLA CLL /CLEAR AC AND LINC
2301 4343 1010 TAD 10 /CONTENTS OF LOC 10 TO AC
2302 4344 1357 TAD K5 /ADD 5
2303 4345 7420 SNL /SKIP IF LINC SET
2304 4346 9217 JMP DO /DO AGAIN-HAVE NOT FILLED FIELD 1
2305 4347 7300 CLA CLL /CLEAR AC AND LINC
2306 4350 1011 TAD 11 /CONTENTS OF LOC 11 TO AC
2307 4351 1362 TAD M5 /SUBTRACT 5
2308 4352 7440 S2A /IF AC = 0
2309 4353 5600 JMP I FLD1P /RETURN TO EXTREM ROUTINE
2310 4354 3011 DCA 11 /DEPOSIT BACK IN LOC 11
2311 4355 0771 RES /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
2312 4356 7402 HLT /ERR-RES IOT DID NOT EXECUTE
2313
2314 4357 0005 /
2315 4360 0103 K5, 5
2316 4361 1020 K103, 103
2317 4362 7773 K1020, 1020
2318 4363 0225 M5, -5
2319 ERE, ERES
2320 /EXTENDED MEMORY TEST
2321 /RELOCATEABLE PORTION OF TEST T65
2322 /
2323 4400
2324 4400 7300 CLA CLL EXTST
2325 4401 9236 JMP /LEVEL 0 INTERRUPT
2326 4402 7402 HLT /JMP TO SUBROUTINE
2327 4403 7402 HLT /LEVEL 1 INTERRUPT
2328 4404 7402 HLT /LEVEL 2 INTERRUPT
2329 4405 7402 HLT /LEVEL 3 INTERRUPT
2330 4406 7402 HLT /LEVEL 4 INTERRUPT
2331 4407 7402 HLT /LEVEL 5 INTERRUPT
2332 4410 7402 HLT /LEVEL 6 INTERRUPT
2333 4411 7402 HLT /LEVEL 7 INTERRUPT
2334 4412 7402 HLT /LEVEL 8 INTERRUPT
2335 4413 7402 HLT /LEVEL 9 INTERRUPT
2336 4414 7402 HLT /LEVEL 10 INTERRUPT
2337 4415 7402 HLT /LEVEL 11 INTERRUPT
2338 4416 7402 HLT /LEVEL 12 INTERRUPT
2339 4417 7402 HLT /LEVEL 13 INTERRUPT
2340 4420 7402 HLT /LEVEL 14 INTERRUPT
2341 4421 7402 HLT
2342 4422 7402 HLT
2343 4423 7402 HLT
2344 4424 7402 HLT
2345 4425 7402 HLT
2346 4426 7402 HLT
2347 4427 7402 HLT
2348 4430 7402 HLT
2349 4431 7402 HLT
2350 4432 7402 HLT
2351 4433 7402 HLT
2352 4434 7402 HLT

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2353	4435	7402	HLT		
2354	4436	6224	EXTYST, RIF		
2355	4437	7041	CIA		/READ INSTRUCTION FIELD INTO AC
2356	4440	1374	TAD		/COMPLIMENT AND INC
2357	4441	7440	SEA		/INSTRUCTION FIELD SHOULD=LOC EIF
2358	4442	7402	HLT		/AC=0
2359	4443	6214	RDP		/ERR - INSTRUCTION FIELD NOT=0
2360	4444	7440	SEA		/READ DATA FIELD INTO AC
2361	4445	7402	HLT		/AC=0
				EIF	/ERR - DATA FIELD NOT=0

2362	4446	RMLV	/READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
2363	4447	TAD	/LOC EFLD SHOULD = 2'S COMPLIMENT OF AC
2364	4450	SEA	/AC=0
2365	4451	HLT	/ERR -
2366	4452	JMS	/ROUTINE TO TEST STACK ADDRESS REGISTER
2367	4453	TAD	/AC=CONTENTS OF LOC EXTSTK
2368	4454	CIA	/COMPLIMENT AND INC
2369	4455	TAD	/LOC EXTSTK SHOULD=4000
2370	4456	SEA	/AC=0
2371	4457	HLT	/ERR-LOC EXTSTK NOT=4000
2372	4460	TAD	/AC=CONTENTS OF LOC EXTSTK+1
2373	4461	CIA	/COMPLIMENT AND INC
2374	4462	TAD	/LOC EXTSTK+1 SHOULD=CONTENTS OF LOC EXTRM1
2375	4463	SEA	/AC=0
2376	4464	HLT	/ERR-LOC EXTSTK+1 NOT=CONTENTS OF LOC EXTRM1
2377	4465	TAD	/AC=CONTENTS OF LOC EXTSTK+2
2378	4466	CIA	/COMPLIMENT AND INC
2379	4467	TAD	/LOC EXTSTK+2 SHOULD = 17
2380	4470	SEA	/AC=0
2381	4471	HLT	/ERR-LOC EXTSTK+2 NOT = 17
2382	4472	TAD	/AC=CONTENTS OF LOC EXTSTK+3
2383	4473	SEA	/AC=0
2384	4474	HLT	/ERR-LOC EXTSTK+3 NOT = 0
2385	4475	TAD	/AC=CONTENTS OF LOC EXTSTK+4
2386	4476	CIA	/COMPLIMENT AND INC
2387	4477	TAD	/LOC EXTSTK+4 SHOULD = 102
2388	4477	SEA	/AC=0
2389	4500	HLT	/ERR-LOC EXTSTK+4 NOT = 102
2390	4501	HLT	/ALTERED INST--PUSH JUMP WITHIN CURRENT FIELD
2391	4502	PJAJ10	/TO LOC EXTPJ
2392	4503	EXTPJ	/ERR-PJA IOT DID NOT EXECUTE
2393	4504	HLT	/TEST STACK ADDRESS REGISTER
2394	4505	JMB	/AC = CONTENTS OF LOC EXTSTK+1
2395	4506	TAD	/INC
2396	4507	IAC	/DEPOSIT BACK IN LOC EXTSTK+1
2397	4510	DCA	/RESTORE MACHINE TO LAST LEVEL SAVED ON STACK
2398	4511	RES	/ERR-RESTORE IOT DID NOT EXECUTE
2399	4512	HLT	/AC SHOULD = 0
2400	4513	SEA	/ERR-AC NOT = 0
2401	4514	HLT	/AC SAVED ON STACK AT LOC EXTSTK+3 = 0
2402	4515	TAD	/SKIP IF = 0
2403	4516	SEA	/ERR--AC SAVED ON STACK NOT = 0
2404	4517	HLT	/AC=CONTENTS OF LOC EXTSTK+6
2405	4520	TAD	/COMPLIMENT AND INC
2406	4521	CIA	/LOC EXTSTK+6 SHOULD=ADDRESS EXTPJ-7
2407	4522	TAD	/AC=0
2408	4523	SEA	/ERR-
2409	4524	HLT	/AC=CONTENTS OF LOC EXTSTK+7
2410	4525	TAD	/AC=0
2411	4526	SEA	/ERR-CONTENTS OF LOC EXTSTK+7 NOT=0
2412	4527	HLT	

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2413
2414      4530      1353      /AC=CONTENTS OF LOC EXTSTK*11
2415      4531      7041      /COMPLIMENT AND INC
2416      4532      1371      /LOC EXTSTK*11 SHOULD = CONTENTS OF LOC EXXX
2417      4533      7440      /AC=0
2418      4534      7402      /ERR-CONTENTS OF LOC EXTSTK*11 NOT=LOC CONTENTS OF EXXX
2419      4535      1390      /AC=CONTENTS OF LOC EXTSTK*6
2420      4536      7001      /INC
2421      4537      3350      /DEPOSIT BACK IN LOC EXTSTK*6
2422      4540      6771      /RESTORE MACHINE TO LAST LEVEL SAVED ON STACK
2423      4541      7402      /ERR-RES JOY DID NOT EXECUTE
2424
2425      /STACK AREA USED WITH EXTENDED MEMORY INTERRUPT TEST
2426
2427
2428      4542      0000      EXTSTK, 0
2429      4543      0000      0
2430      4544      0000      0
2431      4545      0000      0
2432      4546      0000      0
2433      4547      0000      0
2434      4550      0000      0
2435      4551      0000      0
2436      4553      0000      0
2437

```

/COMMON ROUTINE USED 2 TIMES IN EXTENDED MEM BANK INTERRUPT TEST
/TTESTS FOR CORRECT CONTENTS IN THE STACK ADDRESS REGISTER

```

2441      4554      0000      STKTST, 0
2442      4555      7300      CLA CLL
2443      4556      6774      RSTK
2444      4557      3373      DCA EXIM
2445      4560      1377      TAD EXIM
2446      4561      1377      TAD EXTKP5
2447      4562      7040      CMA
2448      4563      7440      SZA
2449      4564      7402      HLT
2450      4565      0754      JMP I STKTST
2451
2452      4566      0017      17
2453      4567      0102      102
2454      4570      4000      4000
2455      4571      0000      EXXX, 0
2456      4572      0000      EFLD, 0
2457      4573      0000      EXIM, 0
2458      4574      0000      EIF, 0
2459      4575      4116      EXTRM1, EXTRET-1
2460      4576      4504      EXTPM7, EXTPJ-7
2461      4577      6547      EXTKP5, EXTSTK*5

```

/CONTAINS RETURN JMP ADDRESS TO EXTSTST ROUTINE
/CLEAR AC AND LINC
/READ STACK ADDRESS REGISTER INTO AC
/DEPOSIT IN EXIM
/GET EXIM
/STACK ADDRESS SHOULD=COMPLIMENT OF ADD EXTSTK*5
/COMPLIMENT THE AC
/AC=0
/ERR - LOC EXIM NOT = ADDRESS EXTSTK*5
/RETURN TO EXTSTST ROUTINE

```

2462 /
2463 /STACK ADDRESS TABLE
2464 /LOCATION 5600 TO 5777
2465 /
2466
2467     5600
2468
2469 /FIRST LEVEL SAVED ON STACK
2470 /
2471 /STACK, 0 /AC
2472 /0 /PC
2473 /0 /MODE FLO LINC MACHINE LEVEL
2474 /0 /MG
2475 /0 /UF IF DF
2476
2477 /SECOND LEVEL SAVED ON STACK
2478 /
2479 /
2480 /STK1P, 0 /AC
2481 /0 /PC
2482 /0 /MODE FLO LINC MACHINE LEVEL
2483 /0 /MG
2484 /0 /UF IF DF
2485
2486 /THIRD LEVEL SAVED ON STACK
2487 /
2488 /
2489 /STK2P, 0 /AC
2490 /0 /PC
2491 /0 /MODE FLO LINC MACHINE LEVEL
2492 /0 /MG
2493 /0 /UF IF DF
2494
2495 /FOURTH LEVEL SAVED ON STACK
2496 /
2497 /
2498 /STK3P, 0 /AC
2499 /0 /PC
2500 /0 /MODE FLO LINC MACHINE LEVEL
2501 /0 /MG
2502 /0 /UF IF DF
2503
2504 /FIFTH LEVEL SAVED ON STACK
2505 /
2506 /
2507 /STK4P, 0 /AC
2508 /0 /PC
2509 /0 /MODE FLO LINC MACHINE LEVEL
2510 /0 /MG
2511 /0 /UF IF DF

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2509 /
2509 /SIXTH LEVEL SAVED ON STACK
2510 /
2510 STK5P, 0 0 /AC
2511 5631 0000 /PC
2512 5632 0000 /MODE FLO LINC MACHINE LEVEL
2513 5633 0000 /MQ
2514 5634 0000 /UF IF DF
2515 5635 0000
2516 /
2516 /SEVENTH LEVEL SAVED ON STACK
2517 /
2517 STK6P, 0 0 /AC
2518 5636 0000 /PC
2519 5637 0000 /MODE FLO LINC MACHINE LEVEL
2520 5640 0000 /MQ
2521 5641 0000 /UF IF DF
2522 5642 0000
2523 /
2523 /EIGHTH LEVEL SAVED ON STACK
2524 /
2524 STK7P, 0 0 /AC
2525 5643 0000 /PC
2526 5644 0000 /MODE FLO LINC MACHINE LEVEL
2527 5645 0000 /MQ
2528 5646 0000 /UF IF DF
2529 5647 0000
2530 /
2530 /NINTH LEVEL SAVED ON STACK
2531 /
2531 STK10P, 0 0 /AC
2532 5650 0000 /PC
2533 5651 0000 /MODE FLO LINC MACHINE LEVEL
2534 5652 0000 /MQ
2535 5653 0000 /UF IF DF
2536 5654 0000
2537 /
2537 /TENTH LEVEL SAVED ON STACK
2538 /
2538 STK11P, 0 0 /AC
2539 5655 0000 /PC
2540 5656 0000 /MODE FLO LINC MACHINE LEVEL
2541 5657 0000 /MQ
2542 5660 0000 /UF IF DF
2543 5661 0000
2544 /
2544 /ELEVENTH LEVEL SAVED ON STACK
2545 /
2545 STK12P, 0 0 /AC
2546 5662 0000 /PC
2547 5663 0000 /MODE FLO LINC MACHINE LEVEL
2548 5664 0000 /MQ
2549 5665 0000 /UF IF DF
2550 5666 0000
2551 /
2551 /
2552 /
2552 /
2553 /
2553 /
2554 /
2554 /
2555 /
2555 /
2556 /
2556 /

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2557 /TWELVETH LEVEL SAVED ON STACK
2558 /
2559 STK13P, 0 /AC
2560 5667 0000 /PC
2561 5670 0000 /MODE FLO LINC MACHINE LEVEL
2562 5671 0000 /MQ
2563 5672 0000 /UF IF DP
2564 5673 0000
2565 /
2566 /THIRTEENTH LEVEL SAVED ON STACK
2567 /
2568 STK14P, 0 /AC
2569 5674 0000 /PC
2570 5675 0000 /MODE FLO LINC MACHINE LEVEL
2571 5676 0000 /MQ
2572 5677 0000 /UF IF DP
2573 5700 0000
2574 /
2575 /FOURTEENTH LEVEL SAVED ON STACK
2576 /
2577 STK15P, 0 /AC
2578 5701 0000 /PC
2579 5702 0000 /MODE FLO LINC MACHINE LEVEL
2580 5703 0000 /MQ
2581 5704 0000 /UF IF DP
2582 5705 0000
2583 /
2584 /FIFTEENTH LEVEL SAVED ON STACK
2585 /
2586 STK16P, 0 /AC
2587 5706 0000 /PC
2588 5707 0000 /MODE FLO LINC MACHINE LEVEL
2589 5710 0000 /MQ
2590 5711 0000 /UF IF DP
2591 5712 0000
2592 /THE LENGTH OF THE STACK IS ONLY
2593 /LIMITED BY CORE -- THIS DIAGNOSTIC
2594 /USES ONLY FIFTEEN LEVELS ON STACK

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/
/ENTERED FROM T35 TEST
/FIRST EXECUTION OF SIMULATED INTERRUPT
/TO LEVEL 0 ONLY
/
  6000
  6000 AND
  6001 JMP
  6002 HLT
  6003 HLT
  6004 HLT
  6005 HLT
  6006 HLT
  6007 HLT
  6008 HLT
  6009 HLT
  6010 HLT
  6011 HLT
  6012 HLT
  6013 HLT
  6014 HLT
  6015 HLT
  6016 HLT
  6017 HLT
  6020 HLT
  6021 HLT
  6022 HLT
  6023 HLT
  6024 HLT
  6025 HLT
  6026 HLT
  6027 HLT
  6030 HLT
  6031 HLT
  6032 HLT
  6033 HLT
  6034 HLT
  6035 HLT

  6000 *6000
  6000 AND
  6001 JMP
  6002 HLT
  6003 HLT
  6004 HLT
  6005 HLT
  6006 HLT
  6007 HLT
  6008 HLT
  6009 HLT
  6010 HLT
  6011 HLT
  6012 HLT
  6013 HLT
  6014 HLT
  6015 HLT
  6016 HLT
  6017 HLT
  6020 HLT
  6021 HLT
  6022 HLT
  6023 HLT
  6024 HLT
  6025 HLT
  6026 HLT
  6027 HLT
  6030 HLT
  6031 HLT
  6032 HLT
  6033 HLT
  6034 HLT
  6035 HLT

  6000 LOC0
  6000 TST

  6000 /LEVEL 0 INTERRUPT
  6001 /JMP TO SUBROUTINE
  6002 /LEVEL 1 INTERRUPT
  6003 /LEVEL 2 INTERRUPT
  6004 /LEVEL 3 INTERRUPT
  6005 /LEVEL 4 INTERRUPT
  6006 /LEVEL 5 INTERRUPT
  6007 /LEVEL 6 INTERRUPT
  6008 /LEVEL 7 INTERRUPT
  6009 /LEVEL 8 INTERRUPT
  6010 /LEVEL 9 INTERRUPT
  6011 /LEVEL 10 INTERRUPT
  6012 /LEVEL 11 INTERRUPT
  6013 /LEVEL 12 INTERRUPT
  6014 /LEVEL 13 INTERRUPT
  6015 /LEVEL 14 INTERRUPT
  6016 /LEVEL 14 INTERRUPT
  6017 /LEVEL 14 INTERRUPT
  6020 /LEVEL 14 INTERRUPT
  6021 /LEVEL 14 INTERRUPT
  6022 /LEVEL 14 INTERRUPT
  6023 /LEVEL 14 INTERRUPT
  6024 /LEVEL 14 INTERRUPT
  6025 /LEVEL 14 INTERRUPT
  6026 /LEVEL 14 INTERRUPT
  6027 /LEVEL 14 INTERRUPT
  6030 /LEVEL 14 INTERRUPT
  6031 /LEVEL 14 INTERRUPT
  6032 /LEVEL 14 INTERRUPT
  6033 /LEVEL 14 INTERRUPT
  6034 /LEVEL 14 INTERRUPT
  6035 /LEVEL 14 INTERRUPT

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2632 /
2633 /TEST API REGISTERS AND PC STORED IN LOC STACK+1
2634 /INCREMENT PC IN LOC STACK+1 AND ISSUE RESTOR IOT
2635 /
2636 /TST,
2637 HLT 7402 /LEVEL 0 INTERRUPT FROM LOC 6001 (LOC CHANGED TO NOP)
2638 CLA CLL /AC=0
2639 TAD K7402 /AC = 7402 OR AN 8-MODE HLT
2640 DCA TST /LOC TST = HLT
2641 RVEC IMAGE /READ VECTOR ADDRESS REGISTER INTO AC
2642 DCA IMAGE /IMAGE OF AC
2643 TAD K1741
2644 CIA /COMPLIMENT AND INC
2645 TAD /IMAGE SHOULD = 1741
2646 SZA /AC = 0
2647 HLT /ERR
2648 RSTK /READ STACK ADDRESS REGISTER INTO AC
2649 DCA IMAGE /IMAGE OF AC
2650 TAD IMAGE
2651 CIA K2172 /COMPLIMENT AND INC
2652 TAD /IMAGE SHOULD = 2172
2653 SZA /AC = 0
2654 HLT /ERR
2655 RMLV /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
2656 DCA IMAGE
2657 TAD IMAGE
2658 CMA /IMAGE SHOULD = 7777
2659 SZA /AC = 0
2660 HLT /ERR
2661 TAD I STACK+1 /GET PC STORED IN LOC STACK+1
2662 CIA /COMPLIMENT AND INC
2663 TAD T35AM1 /PC STORED IN STACK+1 SHOULD = T35A-1
2664 SZA /AC = 0
2665 HLT /ERR
2666 JMS I INC /INC PC STORED IN LOC STACK+1
2667 RES /RESTORE MACHINE TO PREVIOUS STATUS STORED ON STACK
2668 HLT /ERR
2669 /
2670 T35AM1, T35A-1
2675 6076 2235

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/VECTOR ADDRESS POINTER TABLE
/LOCATION 6100 TO 6135
/USED TO TEST A SINGLE INTERRUPT AND THEN RESTORE
/
        6100      *6100
        0000      AND
        5561      JMP I
        0000      AND
        5562      JMP I
        0000      AND
        5563      JMP I
        0000      AND
        5564      JMP I
        0000      AND
        5565      JMP I
        0000      AND
        5566      JMP I
        0000      AND
        5567      JMP I
        0000      AND
        5570      JMP I
        0000      AND
        5571      JMP I
        0000      AND
        5572      JMP I
        0000      AND
        5573      JMP I
        0000      AND
        5574      JMP I
        0000      AND
        0575      JMP I
        0000      AND
        5576      JMP I
        0000      AND
        5577      JMP I
        6100      /LEVEL 0 INTERRUPT
        6101      /JMP TO SERVICE ROUTINE
        6102      /LEVEL 1 INTERRUPT
        6103      /JMP TO SERVICE ROUTINE
        6104      /LEVEL 2 INTERRUPT
        6105      /JMP TO SERVICE ROUTINE
        6106      /LEVEL 3 INTERRUPT
        6107      /JMP TO SERVICE ROUTINE
        6110      /LEVEL 4 INTERRUPT
        6111      /JMP TO SERVICE ROUTINE
        6112      /LEVEL 5 INTERRUPT
        6113      /JMP TO SERVICE ROUTINE
        6114      /LEVEL 6 INTERRUPT
        6115      /JMP TO SERVICE ROUTINE
        6116      /LEVEL 7 INTERRUPT
        6117      /JMP TO SERVICE ROUTINE
        6120      /LEVEL 8 INTERRUPT
        6121      /JMP TO SERVICE ROUTINE
        6122      /LEVEL 9 INTERRUPT
        6123      /JMP TO SERVICE ROUTINE
        6124      /LEVEL 10 INTERRUPT
        6125      /JMP TO SERVICE ROUTINE
        6126      /LEVEL 11 INTERRUPT
        6127      /JMP TO SERVICE ROUTINE
        6130      /LEVEL 12 INTERRUPT
        6131      /JMP TO SERVICE ROUTINE
        6132      /LEVEL 13 INTERRUPT
        6133      /JMP TO SERVICE ROUTINE
        6134      /LEVEL 14 INTERRUPT
        6135      /JMP TO SERVICE ROUTINE

```

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2708 /
2709 /VECTOR INTERRUPT SERVICE ROUTINES
2710 /FIRST INSTRUCTION EXECUTED IN THE
2711 /EXPECTED SERVICE ROUTINE WAS CHANGED
2712 /FROM A HLT TO A NOP
2713 /ALL OTHER ROUTINES HAVE A HLT
2714 /
2715 /LEVEL 0 INTERRUPT FROM LOCATION 6101
2716 /
2717 /
2718 /
2719 /CHANGED FROM HLT TO NOP FOR LEVEL 0 INTERRUPT
2720 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2721 /DEPOSIT 8-MODE HLT IN LOC VECT0
2722 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2723 /ERR
2724 /
2725 /
2726 /
2727 /LEVEL 1 INTERRUPT FROM LOCATION 6103
2728 /
2729 /
2730 /CHANGED FROM HLT TO NOP FOR LEVEL 1 INTERRUPT
2731 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2732 /DEPOSIT 8-MODE HLT IN LOC VECT1
2733 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2734 /ERR
2735 /
2736 /
2737 /LEVEL 2 INTERRUPT FROM LOCATION 6105
2738 /
2739 /
2740 /CHANGED FROM HLT TO NOP FOR LEVEL 2 INTERRUPT
2741 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2742 /DEPOSIT 8-MODE HLT IN LOC VECT2
2743 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2744 /ERR
2745 /
2746 /
2747 /LEVEL 3 INTERRUPT FROM LOCATION 6107
2748 /
2749 /
2750 /CHANGED FROM HLT TO NOP FOR LEVEL 3 INTERRUPT
2751 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2752 /DEPOSIT 8-MODE HLT IN LOC VECT3
2753 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2754 /ERR
2755 /
2756 /
2757 /LEVEL 4 INTERRUPT FROM LOCATION 6111
2758 /
2759 /
2760 /CHANGED FROM HLT TO NOP FOR LEVEL 4 INTERRUPT
2761 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2762 /DEPOSIT 8-MODE HLT IN LOC VECT4
2763 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2764 /ERR
2765 /

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2805 /

        6200      6205      6212      6217      6224      6231
        7402      4403      4403      4403      4403      4403
        4403      3205      3212      3217      3224      3231
        3200      6771      6771      6771      6771      6771
        7402      7402      7402      7402      7402      7402

/LEVEL 5 INTERRUPT FROM LOCATION 6113
/
*6200
VECT5,  HLT          /CHANGED FROM HLT TO NOP FOR LEVEL 5 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT5
        VECT5    /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

/LEVEL 6 INTERRUPT FROM LOCATION 6115
/
VECT6,  HLT          /CHANGED FROM HLT TO NOP FOR LEVEL 6 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT6
        VECT6    /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

/LEVEL 7 INTERRUPT FROM LOCATION 6117
/
VECT7,  HLT          /CHANGED FROM HLT TO NOP FOR LEVEL 7 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT7
        VECT7    /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

/LEVEL 8 INTERRUPT FROM LOCATION 6121
/
VECT10, HLT         /CHANGED FROM HLT TO NOP FOR LEVEL 8 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT10
        VECT10   /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

/LEVEL 9 INTERRUPT FROM LOCATION 6123
/
VECT11, HLT         /CHANGED FROM HLT TO NOP FOR LEVEL 9 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT11
        VECT11   /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

/LEVEL 10 INTERRUPT FROM LOCATION 6125
/
VECT12, HLT        /CHANGED FROM HLT TO NOP FOR LEVEL 10 INTERRUPT
        JMS I      /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
        INC       /DEPOSIT 8-MODE HLT IN LOC VECT12
        VECT12   /RESTORE MACHINE TO LAST STATUS STORED ON STACK
        RES      /ERR
        HLT

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7402
4403
3236
3236
6771
7402

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```

/LEVEL 11 INTERRUPT FROM LOCATION 6127
/CHANGED FROM HLT TO NOP FOR LEVEL 11 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT13
/DEPOSIT 8-MODE HLT IN LOC VECT13
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

```

```

7402
4403
3244
6771
7402

```

```

/LEVEL 12 INTERRUPT FROM LOCATION 6131
/CHANGED FROM HLT TO NOP FOR LEVEL 12 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT14
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

```

```

7402
4403
3251
6771
7402

```

```

/LEVEL 13 INTERRUPT FROM LOCATION 6133
/CHANGED FROM HLT TO NOP FOR LEVEL 13 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT15
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

```

```

7402
4403
3256
6771
7402

```

```

/LEVEL 14 INTERRUPT FROM LOCATION 6135
/CHANGED FROM HLT TO NOP FOR LEVEL 14 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT16
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

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6300 6300 /LEVEL 0 INTERRUPT
0000 AND
0000 JMP I
5736 MLV0A /JMP TO MLV0 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 1 INTERRUPT
5737 MLV1A /JMP TO MLV1 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 2 INTERRUPT
5740 MLV2A /JMP TO MLV2 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 3 INTERRUPT
5741 MLV3A /JMP TO MLV3 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 4 INTERRUPT
5742 MLV4A /JMP TO MLV4 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 5 INTERRUPT
5743 MLV5A /JMP TO MLV5 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 6 INTERRUPT
5744 MLV6A /JMP TO MLV6 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 7 INTERRUPT
5745 MLV7A /JMP TO MLV7 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 8 INTERRUPT
5746 MLV10A /JMP TO MLV10 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 9 INTERRUPT
5747 MLV11A /JMP TO MLV11 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 10 INTERRUPT
5750 MLV12A /JMP TO MLV12 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 11 INTERRUPT
5751 MLV13A /JMP TO MLV13 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 12 INTERRUPT
5752 MLV14A /JMP TO MLV14 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 13 INTERRUPT
5753 MLV15A /JMP TO MLV15 SUBROUTINE
0000 AND
0000 JMP I /LEVEL 14 INTERRUPT
5754 MLV16A /JMP TO MLV16 SUBROUTINE

6336 6336 /MLV0A,
7512 MLV1
6337 7445 MLV1
6340 7400 MLV2
6341 7312 MLV3
6342 7245 MLV4
6343 7200 MLV5
6344 7112 MLV6
6345 7045 MLV7
6346 7000 MLV10A,
6347 6712 MLV11A,
6350 6645 MLV12A,
6351 6600 MLV13A,
6352 6512 MLV14A,
6353 6445 MLV15A,
6354 6400 MLV16A,

```

/ VECTOR ADDRESS POINTER TABLE
/ LOCATION 6300 TO 6335
/ USED IN MULTIPLE LEVEL INTERRUPT TESTS

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*6400
HLT
CLA CLL
TAD
DCA
TAD
DCA
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
TAD I
CIA
TAD
JMP
T56A-1
SEA
HLT
TAD I
IAC
DCA I
TAD
MAIN2
NOP
HLT
CLA CLL
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
RES
HLT

/CHANGED FROM HLT TO NOP FOR LEVEL 14 INTERRUPT
/CLEAR AC AND LINC
/AC = 7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV16
/AC = 7000 OR 8-MODE NOP
/ENABLE LEVEL 13 INTERRUPT BY SETTING LOC MLV15 = NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2172
/AC = 0
/ERR
/GET PC STORED IN LOG STACKK+1
/COMPLIMENT AND INC
/PC STORED IN STACKK+1 SHOULD = T56A-1
/AC = 0
/ERR
/GET PC STORED IN LOG STACKK+1
/INCREMENT
/STORE BACK IN LOG STACKK+1
/AC = 3
/MAINTENANCE MODE SIMULATION OF A LEVEL 13 INTERRUPT
/EXECUTE 3 MORE INST AFTER MAIN2 IOT
/AC = 0--RESTORE OCCURES TO HERE FROM MLV15 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2172
/AC = 0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

MLV16,
MLV16B,

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6445 7402
6446 7300
6447 1150
6450 3245
6451 1142
6452 3312
6453 6774
6454 3002
6455 1002
6456 7041
6457 1114
6460 7440
6461 7402
6462 1760
6463 7041
6464 1266
6465 5267
6466 6432
6467 7440
6470 7402
6471 1760
6472 7001
6473 3760
6474 1047
6475 6052
6476 7000
6477 7402
6500 7300
6501 6774
6502 3002
6503 1002
6504 7041
6505 1114
6506 7440
6507 7402
6510 6771
6511 7402

/
/MULTIPLE LEVEL INTERRUPT-LEVEL 13
/PREVIOUS STATUS SAVED ON STACK LOC 5605 TO 5611
/
MLV15, HLT
CLA CLL
TAD
DCA
TAD
DCA
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
TAD I
CIA
TAD
JMP
MLV16B
SEA
HLT
TAD I
IAC I
DCA I
TAD
MAIN2
NOP
MLV15B, HLT
CLA CLL
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
RES
HLT

7402
7300
1150
3245
1142
3312
6774
3002
1002
7041
1114
7440
7402
1760
7041
1266
5267
6432
7440
7402
1760
7001
3760
1047
6052
7000
7402
7300
6774
3002
1002
7041
1114
7440
7402
6771
7402

/CHANGED FROM HLT TO NOP FOR LEVEL 13 INTERRUPT
/CLEAR AC AND LINC
/AC = 7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV15
/AC = 7000 OR 8-MODE NOP
/ENABLE LEVEL 12 INTERRUPT BY SETTING LOC MLV14 = NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2165
/AC = 0
/ERR
/GET PC STORED IN LOC STK1P
/COMPLIMENT AND INC
/PC STORED IN LOC STK1P SHOULD = MLV16B
/AC = 0
/ERR
/GET PC STORED IN LOC STK1P
/INCREMENT
/STORE BACK IN LOC STK1P
/AC = 7
/MAINTENANCE MODE SIMULATION OF A LEVEL 12 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN2 IOT
/AC = 0--RESTORE OCCURES TO HERE FROM MLV14 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2165
/AC = 0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

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2978 /
2979 /MULTIPLE LEVEL INTERRUPT - LEVEL 12
2980 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5612 TO 5616
2981 /
2982 /
2983 /CHANGED FROM HLT TO NOP FOR LEVEL 12 INTERRUPT
2984 /CLEAR AC AND LINC
2985 /AC = 7402 OR 8-MODE HLT
2986 /DEPOSIT IN LOC MLV14
2987 /AC = 7000 OR 8-MODE NOP
2988 /ENABLE LEVEL 11 INTERRUPT BY SETTING LOC MLV13=NOP
2989 /READ STACK ADDRESS REGISTER INTO AC
2990 /IMAGE OF AC
2991 /COMPLIMENT AND INC
2992 /IMAGE SHOULD = 2160
2993 /AC = 0
2994 /ERR
2995 /GET PC STORED IN LOC STK2P
2996 /COMPLIMENT AND INC
2997 /PC STORED IN LOC STK2P SHOULD = MLV15B
2998
2999 /AC = 0
3000 /ERR
3001 /GET PC STORED IN LOC STK2P
3002 /INCREMENT
3003 /STORE BACK IN LOC STK2P
3004 /AC = 1
3005 /MAINTENANCE MODE SIMULATION OF A LEVEL 11 INTERRUPT
3006 /EXECUTE 1 MORE TEST INST AFTER MAIN1 IOT
3007
3008 /AC = 0--RESTOR OCCURS TO HERE FROM MLV13 ROUTINE
3009 /READ STACK ADDRESS REGISTER INTO AC
3010 /IMAGE OF AC
3011 /COMPLIMENT AND INC
3012 /IMAGE SHOULD = 2160
3013 /AC = 0
3014 /ERR
3015 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3016
3017 /
3018 /MLV13I, MLV13
3019 /STK1, STK1P
3020 /STK2, STK2P
3021
3022
3023

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3024 /
3025 /
3026 /MULTIPLE LEVEL INTERRUPT - LEVEL 11
3027 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5617 TO 5623
3028 /
3029 /
3030 /CHANGED FROM HLT TO NOP FOR LEVEL 11 INTERRUPT
3031 /CLEAR AC AND LINC
3032 /AC = 7402 OR 8-MODE HLT
3033 /DEPOSIT IN LOC MLV13
3034 /AC = 7000 OR 8-MODE NOP
3035 /ENABLE LEVEL 10 INTERRUPT BY SETTING LOC MLV12 = NOP
3036 /READ STACK ADDRESS REGISTER INTO AC
3037 /IMAGE OF AC
3038 /COMPLIMENT AND INC
3039 /IMAGE SHOULD = 2153
3040 /AC = 0
3041 /ERR
3042 /GET PC STORED IN LOC STK3
3043 /COMPLIMENT AND INC
3044 /PC STORED IN LOC STK3 SHOULD = MLV14B
3045 /AC = 0
3046 /ERR
3047 /GET PC STORED IN LOC STK3
3048 /INCREMENT
3049 /STORE BACK IN LOC STK3
3050 /AC = 3
3051 /MAINTENANCE MODE SIMULATION OF LEVEL 10 INTERRUPT
3052 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3053 /AC = 0
3054 /ERR
3055 /GET PC STORED IN LOC STK3
3056 /INCREMENT
3057 /STORE BACK IN LOC STK3
3058 /AC = 0
3059 /ERR
3060 /GET PC STORED IN LOC STK3
3061 /COMPLIMENT AND INC
3062 /IMAGE SHOULD = 2153
3063 /AC = 0
3064 /ERR
3065 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3066 /ERR

```

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3067 /
3068 /MULTIPLE LEVEL INTERRUPT - LEVEL 10
3069 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5624 TO 5630
3070 /
3071 /
3072 /
3073 /
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3075 /
3076 /
3077 /
3078 /
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3080 /
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3107 /
3108 /

6645 7402 HLT
6646 7300 CLA CLL
6647 1150 TAD
6650 3245 DCA K7402
6651 1142 TAD MLV12
6652 3312 DCA K7000
6653 6774 RSTK MLV11
6654 3002 DCA IMAGE
6655 1002 TAD IMAGE
6656 7041 CIA
6657 1111 TAD
6660 7440 SZA
6661 7402 HLT
6662 1761 TAD I
6663 7041 CIA
6664 1266 TAD
6665 5267 JMP
6666 6632 MLV13B
6667 7440 SEA
6670 7402 HLT
6671 1761 TAD I
6672 7001 IAC
6673 3761 DCA I
6674 1047 TAD
6675 6051 MAIN1
6676 7000 NOP
6677 7402 HLT
6700 7300 CLA CLL
6701 6774 RSTK
6702 3002 DCA
6703 1002 TAD
6704 7041 CIA
6705 1111 TAD
6706 7440 SZA
6707 7402 HLT
6710 6771 RES
6711 7402 HLT

/CHANGED FROM HLT TO NOP FOR LEVEL 10 INTERRUPT
/CLEAR AC AND LINC
/AC = 7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV12
/AC = 7000 OR 8-MODE
/ENABLE LEVEL 9 INTERRUPT BY SETTING LOC MLV11 = NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2146
/AC = 0
/ERR
/GET PC STORED IN LOC STK4P
/COMPLIMENT AND INC
/PC STORED IN LOC STK3P SHOULD = MLV13B
/AC = 0
/ERR
/GET PC STORED IN LOC STK4P
/INCREMENT
/STORE BACK IN LOC STK4P
/AC = 7
/MAINTENANCE MODE SIMULATION OF A LEVEL 9 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN1 10T
/AC = 0--RESTORE OCCURS TO HERE FROM MLV11 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2146
/AC = 0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

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3109 /
3110 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 9
3111 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5631 TO 5635
3112 /
3113 /
3114 /CHANGED FROM HLT TO NOP FOR LEVEL 9 INTERRUPT
3115 /CLEAR AC AND LINC
3116 /AC = 7402 OR 8-MODE HLT
3117 /DEPOSIT IN LOC MLV11
3118 /AC = 7000 OR 8-MODE HLT
3119 /ENABLE LEVEL 8 INTERRUPT BY SETTING LOC MLV10 = NOP
3120 /READ STACK ADDRESS REGISTER INTO AC
3121 /IMAGE OF AC
3122 /COMPLIMENT AND INC
3123 /IMAGE SHOULD = 2141
3124 /AC = 0
3125 /ERR
3126 /GET PC STORED IN LOC STK5P
3127 /COMPLIMENT AND INC
3128 /PC STORED IN LOC STK5P
3129
3130
3131
3132 /AC = 0
3133 /ERR
3134 /GET PC STORED IN LOC STK5P
3135 /INCREMENT
3136 /STORE BACK IN LOC STK5P
3137 /AC = 17
3138 /MAINTENANCE MODE SIMULATION OF A LEVEL 8 INTERRUPT
3139 /EXECUTE 1 MORE TEST AFTER MAIN1 IOT
3140
3141 /AC = 0--RESTORE OCCURS HERE FROM MLV10 ROUTINE
3142 /READ STACK ADDRESS REGISTER INTO AC
3143 /IMAGE OF AC
3144
3145 /COMPLIMENT AND INC
3146 /IMAGE SHOULD = 2141
3147 /AC = 0
3148 /ERR
3149 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3150 /ERR
3151
3152
3153
3154
3155

```

Address	Instruction	Hex	Label
6712	HLT	7402	MLV11,
6713	CLA	7300	CLL
6714	TAD	1150	K7402
6715	DCA	3312	MLV11
6716	TAD	1142	K7000
6717	DCA	3757	MLV10I
6720	RSTK	6774	IMAGE
6721	DCA	3002	IMAGE
6722	TAD	1002	IMAGE
6723	CIA	7041	K2141
6724	TAD	1110	STK5
6725	SEA	7440	.+2
6726	HLT	7402	.+2
6727	TAD	1762	MLV12B
6730	CIA	7041	SEA
6731	TAD	1333	HLT
6732	JMP	5334	TAD I
6733	MLV12B	6677	IAC
6734	SEA	7440	DCA I
6735	HLT	7402	TAD
6736	TAD	1762	MAIN1
6737	IAC	9001	NOP
6740	DCA	3762	CLA
6741	TAD	1051	CLL
6742	MAIN1	6051	RSTK
6743	NOP	7000	DCA
6744	HLT	7402	TAD
6745	CLA	7300	CIA
6746	RSTK	6774	TAD
6747	DCA	3002	SEA
6750	TAD	1002	HLT
6751	CIA	7041	RES
6752	TAD	1110	HLT
6753	SEA	7440	RES
6754	HLT	7402	HLT
6755	RES	0771	MLV10I,
6756	HLT	7402	STK3,
6757	MLV10I,	7000	STK4,
6760	STK3,	5620	STK5,
6761	STK4,	5625	
6762	STK5,	5632	

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3156 /
3157 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 8
3158 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5636 TO 5642
3159 /
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3162 /
3163 /
3164 /
3165 /
3166 /
3167 /
3168 /
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3199 /

7000 *7000
7402 HLT
7300 CLA CLL
1150 TAD K7402
3200 DCA MLV10
1142 TAD K7000
3245 DCA MLV7
4774 RSTK
9002 DCA IMAGE
1002 TAD IMAGE
7041 CIA IMAGE
1107 TAD K2134
7013 SEA
7014 HLT
7015 TAD I STK6
1760 CIA .*2
7041 TAD .*2
1221 JMP
5222 HLV118
6744 SEA
7440 HLT
7402 TAD I STK6
1760 IAC STK6
7001 DCA I STK6
3740 TAD K37
1053 MAIN1
6051 NOP
7000 HLT
7402 HLT

7000 CLA CLL
7001 RSTK
7002 DCA IMAGE
7003 TAD IMAGE
7004 CIA K2134
7005 TAD
7006 SEA
7007 HLT
7010 HLT
7011 HLT
7012 HLT
7013 HLT
7014 HLT
7015 HLT
7016 HLT
7017 HLT
7020 HLT
7021 HLT
7022 HLT
7023 HLT
7024 HLT
7025 HLT
7026 HLT
7027 HLT
7030 HLT
7031 HLT
7032 HLT

7033 HLT
7034 HLT
7035 HLT
7036 HLT
7037 HLT
7040 HLT
7041 HLT
7042 HLT
7043 HLT
7044 HLT

/CHANGED FROM HLT TO NOP FOR LEVEL 8 INTERRUPT
/CLEAR AC AND LINC
/AC=7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV10
/AC=7000 OR 8-MODE NOP
/ENABLE LEVEL 7 INTERRUPT BY SETTING LOC MLV7=NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2134
/AC = 0
/ERR
/GET PC STORED IN LOC STK6P
/COMPLIMENT AND INC
/PC STORED IN LOC STK6P SHOULD=MLV118
/AC = 0
/ERR
/GET PC STORED IN LOC STK6P
/INCREMENT
/STORE BACK IN LOC STK6P
/AC = 37
/MAINTENANCE MODE SIMULATION OF A LEVEL 7 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN1 107
/AC = 0--RESTORE OCCURS TO HERE FROM MLV7 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2134
/AC = 0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

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3200 /
3201 /MULTIPLE LEVEL INTERRUPT TEST = LEVEL 7
3202 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5643 TO 5647
3203 /
3204 /
3205 /CHANGED FROM HLT TO NOP FOR LEVEL 7 INTERRUPT
3206 /CLEAR AC AND LINC
3207 /AC = 7402 OR 8-MODE HLT
3208 /DEPOSIT IN LOC MLV7
3209 /AC = 7000 OR 8-MODE NOP
3210 /ENABLE LEVEL 6 INTERRUPT BY SETTING LOC MLV6 = NOP
3211 /READ STACK ADDRESS REGISTER INTO AC
3212 /IMAGE OF AC
3213 /COMPLIMENT AND INC
3214 /IMAGE SHOULD = 2127
3215 /AC = 0
3216 /ERR
3217 /GET PC STORED IN LOC STK7P
3218 /COMPLIMENT AND INC
3219 /PC STORED IN LOC STK7P SHOULD = MLV100
3220 /
3221 /AC = 0
3222 /ERR
3223 /GET PC STORED IN LOC STK7P
3224 /INCREMENT
3225 /STORE BACK IN LOC STK7P
3226 /AC = 77
3227 /MAINTENANCE MODE SIMULATION OF A LEVEL 6 INTERRUPT
3228 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3229 /
3230 /AC = 0--RESTORE OCCURS TO HERE FROM MLV6 ROUTINE
3231 /READ STACK ADDRESS REGISTER INTO AC
3232 /IMAGE OF AC
3233 /COMPLIMENT AND INC
3234 /IMAGE SHOULD = 2127
3235 /AC = 0
3236 /ERR
3237 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3238 /
3239 /
3240 /
3241 /

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7110
7111

MLV7,
CLA CLL
TAD
DCA
TAD
DCA
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
TAD I
CIA
TAD
JMP
MLV100
SEA
HLT
TAD I
IAC
DCA I
TAD
MAIN1
NOP
HLT
CLA CLL
RSTK
DCA
TAD
CIA
TAD
SEA
HLT
RES
HLT

K7402
MLV7
K7000
MLV6
IMAGE
IMAGE
K2127
STK7
.02
.02
STK7
STK7
K77
IMAGE
IMAGE
K2127

```

3242 /
3243 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 6
3244 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5650 TO 5654
3245 /
3246 /
3247 /CHANGED FROM HLT TO NOP FOR LEVEL 6 INTERRUPT
3248 /CLEAR AC AND LINC
3249 /AC = 7402 OR 8-MODE HLT
3250 /DEPOSIT IN LOC MLV6
3251 /AC = 7000 OR 8-MODE NOP
3252 /ENABLE LEVEL 5 INTERRUPT BY SETTING LOC MLV5 = NOP
3253 /READ STACK ADDRESS REGISTER INTO AC
3254 /IMAGE OF AC
3255 /COMPLIMENT AND INCREMENT
3256 /IMAGE SHOULD = 2122
3257 /AC = 0
3258 /ERR
3259 /GET PC STORED IN LOC STK10P
3260 /COMPLIMENT AND INC
3261 /PC STORED IN LOC STK10P SHOULD = MLV7B
3262 /
3263 /AC = 0
3264 /ERR
3265 /GET PC STORED IN LOC STK10P
3266 /INCREMENT
3267 /STORE BACK IN LOC STK10P
3268 /AC = 177
3269 /MAINTENANCE MODE SIMULATION OF A LEVEL 5 INTERRUPT
3270 /EXECUTE 1 MORE INST AFTER MAIN1 JOY
3271 /
3272 /AC = 0--RESTORE OCCURS TO HERE FROM MLV5 ROUTINE
3273 /READ STACK ADDRESS REGISTER INTO AC
3274 /IMAGE OF AC
3275 /COMPLIMENT AND INC
3276 /IMAGE SHOULD = 2122
3277 /AC = 0
3278 /ERR
3279 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3280 /
3281 /
3282 /
3283 /
3284 /
3285 /MLV51, MLV5
3286 /STK6, STK6P
3287 /STK7, STK7P
3288 /STK10, STK10P

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3289 /
3290 /
3291 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 5
3292 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 9655 TO 9661
3293 /
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3295 /
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3297 /
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3300 /
3301 /
3302 /
3303 /
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7200 *7200
7402 HLT
7300 CLA CLL
7201 TAD
7202 DCA
7203 DCA
7204 DCA
7205 RSTK
7206 DCA
7207 TAD
7210 TAD
7211 CIA
7212 CIA
7213 SEA
7214 HLT
7215 TAD I
7216 CIA
7217 CIA
7220 JMP
7221 MLV6B
7222 SEA
7223 HLT
7224 TAD I
7225 IAC
7226 DCA I
7227 TAD
7230 MAIN1
7231 NOP
7232 HLT
7233 CLA CLL
7234 RSTK
7235 DCA
7236 TAD
7237 CIA
7240 TAD
7241 SEA
7242 HLT
7243 RES
7244 HLT

7200
7402
7300
7201
7202
7203
7204
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7241
7242
7243
7244

K7402
MLV5
K7000
MLV4
IMAGE
IMAGE
K2115
STK11
.*2
.*2
STK11
STK11
K377
IMAGE
IMAGE
K2115

/CHANGED FROM HLT TO NOP FOR LEVEL 5 INTERRUPT
/CLEAR AC AND LINC
/AC=7402 OR 8 MODE HLT
/DEPOSIT IN LOC MLV5
/AC=7000 OR 8-MODE NOP
/ENABLE LEVEL 4 INTERRUPT BY SETTING LOC MLV4=NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2115
/AC=0
/ERR
/GET PC STORED IN LOC STK11P
/COMPLIMENT AND INC
/PC STORED IN LOC STK11P SHOULD = MLV6B
/AC=0
/ERR
/GET PC STORED IN LOC STK11P
/INCREMENT
/STORE BACK IN LOC STK11P
/AC=377
/MAINTENANCE MODE SIMULATION OF A LEVEL 4 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN1 IOT
/AC=0--RESTORE OCCURS TO HERE FROM MLV4 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2115
/AC=0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

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/MULTIPLE LEVEL INTERRUPT TEST - LEVEL 4
/PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5662 TO 5666
/
MLV4, HLT 7402 /CHANGED FROM HLT TO NOP FOR LEVEL 4 INTERRUPT
CLA CLL 7406 /CLEAR AC AND LINC
TAD 7300 /AC=7402 OR 8-MODE HLT
DCA K7402 /DEPOSIT IN LOC MLV4
MLV4 7247 /AC=7000 OR 8-MODE NOP
K7000 7250 /ENABLE LEVEL 3 INTERRUPT BY SETTING LOC MLV3=NOP
MLV3 7251 /READ STACK ADDRESS REGISTER INTO AC
RSTK 7253 /IMAGE OF AC
DCA 7254 /COMPLIMENT AND INC
DCA 7255 /IMAGE SHOULD = 2110
TAD 7256 /AC=0
CIA 7257 /ERR
TAD 7260 /GET PC STORED IN LOC STK12P
STK12 7261 /COMPLIMENT AND INC
I 7262 /PC STORED IN LOC STK12P SHOULD = MLV50
:02 7263
:02 7264
JMP 7265
MLV50 7266 /AC=0
SEA 7267 /ERR
HLT 7270 /GET PC STORED IN LOC STK12P
STK12 7271 /INCREMENT
IAC 7272 /STORE BACK IN LOC STK12P
DCA I 7273 /AC=777
TAD 7274 /MAINTENANCE MODE SIMULATION OF A LEVEL 3 INTERRUPT
MAIN1 7275 /EXECUTE 1 MORE INST AFTER MAIN1 JOT
NOP 7276
MLV40, HLT 7277 /AC=0--RESTORE OCCURS TO HERE FROM MLV5 ROUTINE
CLA CLL 7300 /READ STACK ADDRESS REGISTER INTO AC
RSTK 7301 /IMAGE OF AC
DCA 7302 /COMPLIMENT AND INC
TAD 7303 /IMAGE SHOULD=2110
CIA 7304 /AC=0
TAD 7305 /ERR
SEA 7306 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
HLT 7307
RES 7310
HLT 7311

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7300
1150
3312
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3757
6774
5002
1002
7041
1102
7440
7402
1762
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9334
7277
7440
7402
1762
7041
1333
9334
7277
7334
7440
7335
1762
7001
9762
1075
6091
7000
7402
7300
6774
5002
1002
7041
1102
7440
7402
6774
7402
7400
9656
9663
9670

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/ MLV3, HLT 7402 /CHANGED FROM HLT TO NOP FOR LEVEL 3 INTERRUPT
CLA CLL 7300 /CLEAR AC AND LINC
TAD 7314 /AC=7402 OR 8-MODE HLT
DCA 7315 /DEPOSIT IN LOC MLV3
TAD 7316 /AC=7000 OR 8-MODE NOP
DCA I 7317 /ENABLE LEVEL 2 INTERRUPT BY SETTING LOC MLV2=NOP
RSTK 7320 /READ STACK ADDRESS REGISTER INTO AC
DCA IMAGE 7321 /IMAGE OF AC
TAD IMAGE 7322
CIA IMAGE 7323
TAD IMAGE 7324
DCA K2103 7325 /COMPLEMENT AND INC
SZA K2103 7326 /IMAGE SHOULD =2103
HLT 7327 /AC=0
TAD I 7328 /ERR
CIA 7329 /GET PC STORED IN LOC STK13P
TAD 7330 /COMPLIMENT AND INC
DCA 7331 /PC STORED IN LOC STK13P SHOULD = MLV48
JMP 7332
MLV48 7333
SZA 7334
HLT 7335
TAD I 7336 /AC=0
IAC 7337 /ERR
DCA I 7338 /GET PC STORED IN LOC STK13P
TAD 7339 /INCREMENT
MAIN1 7340 /STORE BACK IN LOC STK13P
NOP 7341 /AC=1777
HLT 7342 /MAINTENANCE MODE SIMULATION OF A LEVEL 2 INTERRUPT
MLV3B, HLT 7343 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
CLA CLL 7344 /AC=0
RSTK 7345 /RESTORE OCCURS TO HERE FROM MLV2 ROUTINE
DCA IMAGE 7346 /READ STACK ADDRESS REGISTER INTO AC
CIA IMAGE 7347 /IMAGE OF AC
TAD IMAGE 7348
SZA K2103 7349 /COMPLIMENT AND INC
HLT 7350 /IMAGE SHOULD=2103
RES 7351 /AC=0
HLT 7352 /ERR
MLV21, HLT 7353 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
STK11, 7354
STK12, 7355
STK13, 7356

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3505

/ MULTIPLE LEVEL INTERRUPT TEST - LEVEL 1
/ PREVIOUS MACHINE STATUS SAVED ON STACK LOC 9700 TO 9704
/
MLV1.
7445 HLT 7402
7446 CLA CLL
7447 TAD K7402
7450 DCA MLV1
7451 TAD K7000
7452 DCA MLV0
7453 RSTK IMAGE
7454 DCA IMAGE
7455 TAD
7456 CIA
7457 TAD K2071
7460 SEA
7461 HLT
7462 TAD I
7463 CIA
7464 TAD
7465 JMP
7466 HLV20
7467 SEA
7470 HLT
7471 TAD I
7472 IAC
7473 DCA I
7474 CLA CLL
7475 MAINI
7476 NOP
7477 HLT
7500 CLA CLL
7501 RSTK
7502 DCA
7503 TAD
7504 CIA
7505 TAD
7506 SEA
7507 HLT
7510 RES
7511 HLT

/ CHANGED FROM HLT TO NOP FOR LEVEL 1 INTERRUPT
/ CLEAR AC AND LINC
/ AC=7402 OR 8-MODE HLT
/ DEPOSIT IN LOC MLV1
/ AC=7000 OR 8-MODE NOP
/ ENABLE LEVEL 0 INTERRUPT BY SETTING LOC MLV0=NOP
/ READ STACK ADDRESS REGISTER INTO AC
/ IMAGE OF AC
/ COMPLIMENT AND INC
/ IMAGE SHOULD = 2071
/ AC=0
/ ERR
/ GET PC STORED IN LOC STK15P
/ COMPLIMENT AND INC
/ PC STORED IN LOC STK15P SHOULD = MLV20
/ AC=0
/ ERR
/ GET PC STORED IN LOC STK15P
/ INCREMENT
/ STORE BACK IN LOC STK15P
/ AC=7777
/ MAINTENANCE MODE SIMULATION OF A LEVEL 0 INTERRUPT
/ EXECUTE 1 MORE INST AFTER MAINI IOT
/ AC=0 -- RESTORE OCCURS TO HERE FROM MLV1 ROUTINE
/ READ STACK ADDRESS REGISTER INTO AC
/ IMAGE OF AC
/ COMPLIMENT AND INC
/ IMAGE SHOULD = 2071
/ AC=0
/ ERR
/ RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ ERR

```

3506 /
3507 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 0
3508 /PREVIOUS MACHINE STATUS
3509 /
3510 MLV0,

7402 /CHANGED FROM HLT TO NOP FOR LEVEL 0 INTERRUPT
7512 CLA CLL
7513 TAD K7402
3512 MLV0
7514 DCA
3513 RSTK
7515 DCA
3514 DCA
7516 RSTK
3515 DCA
7517 TAD
3516 CIA
7518 TAD
3517 SEA
7519 HLT
3518 TAD I
7520 CIA
3519 TAD
7521 TAD
3520 SEA
7522 HLT
3521 TAD I
7523 CIA
3522 TAD
7524 SEA
3523 HLT
7525 TAD I
3524 CIA
7526 TAD
3525 JMP
7527 MLV18
3526 SEA
7528 HLT
3527 TAD I
7529 TAD
3528 IAC
7530 DCA
3529 REG
7531 HLT
7532
7533
7534
7535
7536
7537
7538
7539
7540

2064 /
8679 STK14,
8702 STK15P,
8707 STK16P
0

DIAL10	V003	15-SEP-71	0150	PAGE	77-3	MLV13B	6632
AGAIN	1074	K1000	0071	K5241	0131	MLV13B	6632
ALT0	4104	K1020	4361	K5252	0132	MLV13I	6557
ALT1	4140	K1103	4360	K5400	0133	MLV14	6512
ALT2	4144	K1100	4163	K5577	0134	MLV14A	6352
APION	6006	K1441	0072	K5600	0135	MLV14B	6544
CLRSTK	0230	K1641	0073	K5777	0136	MLV15	6445
CLRSK	0007	K17	0051	K60	0055	MLV15A	6353
CNT	1123	K1741	0074	K6000	0137	MLV15B	6477
COUNT	0023	K177	0060	K6100	0140	MLV16	6400
DO	4217	K1777	0075	K6300	0141	MLV16A	6354
E102	4597	K2	0044	K7	0047	MLV16B	6432
E17	4566	K20	0052	K7000	0142	MLV1A	6337
E4000	4570	K200	0061	K7017	0143	MLV1B	7477
EALTP	4502	K2000	0076	K7020	0144	MLV2	7400
EALTPJ	4175	K2020	0077	K7077	0145	MLV2A	6340
EFLD	4572	K2064	7541	K720	0067	MLV2B	7432
EFLDI	4174	K207	0062	K7277	0146	MLV2I	7357
EIF	4574	K2071	0100	K7400	0147	MLV3	7312
EIFI	4173	K2076	0101	K7402	0150	MLV3A	6341
ERE	4363	K2103	0102	K7577	0151	MLV3B	7344
ERES	4347	K2110	0103	K7600	0152	MLV4	7245
EREST	4302	K2115	0104	K77	0056	MLV4A	6342
ESTKT	4573	K2122	0105	K7740	0153	MLV4B	7277
EXIM	4165	K2127	0106	K7741	0154	MLV5	7200
EXICIA	4166	K2134	0107	K7760	0155	MLV5A	6343
EXICTR	4035	K2141	0110	K777	0070	MLV5B	7232
EXIC0	4577	K2146	0111	K7770	0156	MLV5I	7157
EXTM5	4577	K2153	0112	KCDF	4171	MLV6	7112
EXTME	4001	K2160	0113	KMLV	4170	MLV6A	6344
EXTPJ	4513	K2165	0114	LOC0	0000	MLV6B	7144
EXTPM7	4576	K2172	0115	M16	0160	MLV7	7045
EXTM1	4176	K2177	0116	M5	4302	MLV7A	6345
EXTST	4542	K2200	0043	M6	0197	MLV7B	7077
EXTSTK	4436	K2500	0117	MAIN1	6051	MORE	3634
EXTT	4167	K2501	0120	MAIN2	6052	MV16	2671
EXXX	4571	K2525	0121	MASK1	3555	PJA	6760
EXXI	4172	K2777	0122	MASK2	3556	PJPC	0027
FLD1P	4200	K3	0045	MLV0	7512	PJPCI	0030
FLDIPT	4177	K3500	0123	MLV0A	6336	PJPC2	0031
IMAGE	0002	K37	0053	MLV1	7445	RAN	0024
INC	0003	K377	0064	MLV10	7000	RAN1	0025
INCC	0266	K4	0046	MLV10A	6346	RAN2	0026
IOF	6002	K40	0045	MLV10B	7032	RANN	0275
ION	6001	K400	0125	MLV10I	6757	RDF	6214
IOPRE	0202	K4000	4164	MLV11	6712	RES	6771
IOPRES	0222	K4377	0126	MLV11A	6347	RIF	6224
K1	0043	K4400	4357	MLV11B	6744	RMLV	6773
K10	0050	K5	0127	MLV12	6645	RSTK	6774
K100	0057	K5020	0066	MLV12A	6650	RVEC	6775
		K520	0150	MLV12B	6677	SACML	0254
		K5240		MLV13	6600	SETUP	0006
				MLV13A	6351	SETUPP	0213

SMLV	6772	T27	1632	UT16	1402
SSTK	6776	T3	0453	UT16A	0004
STACK	0032	T30	1665	UT37	3402
STACKK	5600	T31	1704	UT37A	0009
STK1	6560	T32	1741	VEC0	0161
STK10	7162	T33	2001	VEC1	0162
STK10P	5651	T34	2037	VEC10	0171
STK11	7360	T34A	2105	VEC11	0172
STK11P	5656	T35	2200	VEC12	0173
STK12	7361	T35A	2236	VEC13	0174
STK12P	5663	T35AM1	6076	VEC14	0175
STK13	7362	T36	2276	VEC15	0176
STK13P	5670	T37	2400	VEC16	0177
STK14	7542	T4	0466	VEC2	0163
STK14P	5675	T40	2415	VEC3	0164
STK15	7543	T41	2425	VEC4	0165
STK15P	5702	T42	2435	VEC5	0166
STK16	7544	T43	2445	VEC6	0167
STK16P	5707	T44	2455	VEC7	0170
STK1P	5606	T45	2465	VECT0	6140
STK2	6561	T46	2475	VECT1	6149
STK2P	5613	T47	2505	VECT10	6217
STK3	6700	T8	0501	VECT11	6224
STK3P	5620	T50	2015	VECT12	6231
STK4	6761	T51	2505	VECT13	6236
STK4P	5625	T52	2505	VECT14	6244
STK5	6762	T53	2505	VECT15	6251
STK5P	5632	T54	2600	VECT16	6256
STK6	7160	T55	2610	VECT2	6152
STK6P	5637	T56	2600	VECT3	6157
STK7	7161	T57	2605	VECT4	6164
STK7P	5644	T58A	3000	VECT5	6200
STKTST	4594	T6	0526	VECT6	6205
SVEC	6777	T60	3071	VECT7	6212
T0	0400	T60A	3112	VT0	4600
T1	0431	T60AM6	3133	XACMG	0243
T10	0625	T61	3000		
T11	0637	T61A	3220		
T12	0642	T61AM5	3275		
T13	0700	T62	3243		
T14	0726	T62A	3202		
T15	0744	T62AM6	3276		
T16	1000	T63	3403		
T17	1025	T64	3421		
T2	0441	T65	3036		
T20	1047	T66	3545		
T21	1066	T7	0600		
T22	1200	TST	6036		
T22A	1276	T3TA	0042		
T24	1403	T3PJ	1216		
T24A	1527	T3PJ1	1425		
T26	1600	T3PJ2	2053		

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ERRORS DETECTED 0
LINKS GENERATED 0
RUN-TIME: 45 SECONDS
3K CORE USED

INC	3193	3212	3234	3235	3254	3255	3276	3277	3302	3303	3324	3325	3344
	3345	3366	3386	3387	3408	3409	3434	3435	3456	3497	3476	3477	3498
	3499	3516	2720	2736	2744	2752	2762	2770	2778	2786	2794	2802	2811
	2820	2828	1027	1062	1090	1136	1159	1200	2053	2083	2200		
INCC	35	251#											
ICF	22#	290											
ION	21#	294											
IOPRE	44	169#	187	519	539	566	638	666	690	1010	1046	1083	1106
IOPRES	44#	360	492										
	1280		497	1167	1574	1623	1658	2070	2080	2228	2263	2271	3006
K1	64#	254											
K10	69#	1528											
K100	76#	1482											
K1000	86#	1436											
K1020	2201	2316#											
K103	2296	2315#											
K1100	2085	2102											
K1441	87#	1653											
K1641	88#	846											
K17	70#	692											
K1741	89#	2645											
K177	77#	3270											
K1777	90#	3402											
K2	65#	790											
K20	71#	194	1294	1589	1608	2068							
K200	78#	1467	629	1017	1028	1513							
K2000	91#	1421											
K2020	92#	442											
K2064	3510	3535#											
K207	79#	295											
K2071	93#	3479											
K2076	94#	3437											
K2103	95#	3389											
K2110	96#	3347											
K2115	97#	3305											
K2122	98#	3257											
K2127	99#	3215											
K2134	100#	3172											
K2141	101#	3124											
K2146	102#	3082											
K2153	103#	3040											
K2160	104#	2993											
K2165	105#	2951											
K2172	106#	766	2652	2909	2931								
K2177	108#	839	1334	1668									
K220	80#	602											
K2500	109#	576											
K2501	110#	619											
K2525	111#	416											
K2777	112#	423											
K3	66#	786											
K3500	113#	1918	942	1244	2922	3053							

K37	72#	873	1203	1354	1374	1400	1643	1607	1753	1802	1849	1896	2078	3185
K377	81#	3318												
K3777	114#	3450												
K4	67#	1544	1590											
K40	73#	1498												
K400	82#	1452												
K4000	115#	1406	1901	2130										
K4377	2104	2106	2110	2144										
K4400	116#	2072		2146	2150	2176#								
K5	2217	2302	2314#											
K5020	117#	413												
K520	83#	973												
K5240	118#	605												
K5241	119#	591												
K5252	120#	430	445											
K5400	121#	292												
K5577	122#	206	1635											
K5600	123#	188	1287											
K5777	124#	452												
K60	74#	405	544	643										
K6000	125#	1290	2205											
K6100	126#	191												
K6300	127#	1646												
K7	60#	1922												
K7000	128#	1281	2061	2964	3095									
	1588	1606	1403	1419	1434	1465	1496	1400	1496	1511	1526	1542	1557	1572
	3209	3251	1621	1656	1739	1899	2945	2903	2945	2987	3034	3076	3118	3166
	129#	933	3209	3341	3383	3473								
K7017	129#	933												
K7020	130#	390												
K7077	131#	536												
K720	84#	529												
K7277	132#	583												
K7400	133#	312												
K74002	134#	256												
	3297	3339	1926	1897	2639	2943	3032	2985	3032	3074	3116	3144	3207	3249
	135#	612	1671	3429	3471	2901	3032	2985	3032	3074	3116	3144	3207	3249
K7577	136#	301	3381	3429	3471	3513								
K7600	1024	1024												
K77	75#	75#												
K7740	137#	482												
K7741	138#	473												
K7760	139#	1341												
K777	85#	397												
K7770	140#	208												
KCDF	2115	2181#												
KMLV	2079	2084												
LOC0	32#	740	2087	2100#	1223	1232	2602	1286	2602	2678	2600	2602	2684	2686
	2680	2690	746	748	1233	1232	2694	2702	2602	2706	2600	2602	2684	2686
	2855	2857	2859	2861	2696	2700	2694	2702	2704	2873	2847	2849	2851	2853
	142#	675			2863	2867	2861	2869	2871	2873	2875			
M16	142#	675												
M5	2307	2317#												
M6	141#	298												
MAIN1	19#	1307	1357	1367	1377	1407	1422	1437	1453	1468	1483	1499	1514	1529
	1545	1560	1575	1695	1713	1733	1902	1933	2131	3007	3054	3096	3138	3186

T51	1555#				
T52	1570#				
T53	1586#				
T54	1595	1604#			
T55	1619#				
T56	1634#				
T56A	1663#	2916			
T57	1674	1684#			
T6	412#				
T60	1750#				
T60A	1761	1770#	1784	1791	
T60AM6	1775	1791#			
T61	1768	1799#			
T61A	1810	1818#	1834	1873	
T61AM5	1823	1873#			
T62	1816	1846#			
T62A	1854	1861#	1868	1874	
T62AM6	1864	1874#			
T63	1860	1892#			
T64	1914#				
T65	1955				
T66	2006	2002#	2016#		
T7	434	2010			
TST	63	441#	2637#	2640	
TSTA	63#	2603			
TSTPJ	49	1282	748#		
TSTPJ1	50	742	885	891#	951
TSTPJ2	51	878	1232#		
UT16	36	1227	888	891	
UT16A	36#	858#			
UT37	37	1353			
UT37A	37#	1311	1884#		
VEC0	143#	1224	1398		
VEC1	144#	1404	1740	1900	2679
VEC10	151#	1420	2681		
VEC11	152#	1527	2695		
VEC12	153#	1543	2697		
VEC13	154#	1558	2699		
VEC14	155#	1573	2701		
VEC15	156#	1589	2703		
VEC16	157#	1607	2705		
VEC2	145#	1622	2707		
VEC3	146#	1435	2603		
VEC4	147#	1451	2605		
VEC5	148#	1466	2687		
VEC6	149#	1481	2689		
VEC7	150#	1497	2691		
VECT0	143	1512	2693		
VECT1	144	1705	1726	2719#	2721
VECT10	151	2727#	2729		
VECT11	152	2785#	2787		
VECT12	153	2793#	2795		
VECT13	154	2801#	2803		
		2810#	2812		2813

VECT14	155	2819#	2821
VECT15	156	2827#	2829
VECT16	157	2835#	2837
VECT2	145	2735#	2737
VECT3	146	2743#	2745
VECT4	147	2751#	2753
VECT5	148	2761#	2763
VECT6	149	2769#	2771
VECT7	150	2777#	2779
VT0	2324#		
XACMQ	220#	757	809
			899
			972

