

D-CS-G8019-0-1	POWER DISTRIBUTION BOARD CONTROL		
D-UA-G8019-0-0	POWER DISTRIBUTION BOARD CONTROL		
D-CS-G8016-0-1	REG. BOARD 8A SEMICONDUCTOR	D-FD-KM8-A-6	FLOW CHART OPTION #2
D-CS-G8018-0-1	REG. BOARD 8A CORE	A-SP-KM8-A-7	ROM PROG. INST.
D-CS-M8315-0-1	HEX OMNIBUS CPU	A-SP-DKC8-A-1	FIELD INST. AND ACCEPTANCE PROCEDURE
D-FD-M8315-0-16	FLOW DIAGRAM INDEX	D-CS-M8316-0-1	OPTION BOARD #1
D-FD-M8315-0-17	FLOW DIAGRAM	D-UA-M8316-0-0	UNIT ASSY
D-FD-M8315-0-18	FLOW DIAGRAM	B-PL-M8316-0-0	PARTS LIST
D-FD-M8315-0-19	FLOW DIAGRAM	E-UA-KC8-A-0	BEZEL ASSY
D-FD-M8315-0-20	FLOW DIAGRAM	D-AD-7010644-0-0	KEYBOARD ASSY
D-FD-M8315-0-21	FLOW DIAGRAM	D-CS-5411241-0-1	INDICATOR DISPLAY
D-FD-M8315-0-22	FLOW DIAGRAM	D-CS-5411316-0-1	REGISTERS AND CONTROL
D-FD-M8315-0-23	FLOW DIAGRAM	A-SP-KT8-A	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-FD-M8315-0-24	FLOW DIAGRAM	D-UA-M8416-0-0	KT8-A UNIT ASSY
D-FD-M8315-0-25	FLOW DIAGRAM	B-PL-M8416-0-0	PARTS LIST
D-FD-M8315-0-26	FLOW DIAGRAM	D-CS-M8416-0-1	MEMORY MANAGEMENT OPTION
D-FD-M8315-0-27	FLOW DIAGRAM	D-CS-M9020-0-1	KT8-A TERMINATOR CARD
E-FD-PDP8/E-0-06	PROCESSOR FLOW CHART	A-SP-MS8-C-0	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-TD-PDP8/E-0-05	TIMING (PDP8/E)	D-CS-M8417-0-1	PDP-8 MOS MEMORY
E-CS-M8300-0-1	MAJOR REGISTERS	D-UA-M8417-0-0	UNIT ASSY
E-CS-M8310-0-1	MAJOR REGISTERS CONTROL	B-PL-M8417-0-0	PARTS LIST
E-CS-M8320-0-1	BUS LOADS	A-SP-MR8-F-2	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
E-CS-M8330-0-1	TIMING GENERATOR	D-CS-M8349-0-1	1K PROM
A-SP-KM8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE	A-SP-MS8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8317-0-1	OPTION BOARD #2	D-CS-M8311-0-1	4K X 12 MOS MEMORY
D-UA-M8317-0-0	UNIT ASSY		
B-PL-M8317-0-0	PARTS LIST		
D-TD-KM8-A-4	AUTO RESTART/BOOT SEQUENCE		
D-TD-KM8-A-5	BOOTSTRAP TIMING		

TITLE	SIZE	CODE	NUMBER	REV
8A SEMICONDUCTOR MEMORY FAMILY	B	TC	8A-1-1	
SHEET 2 OF 2				

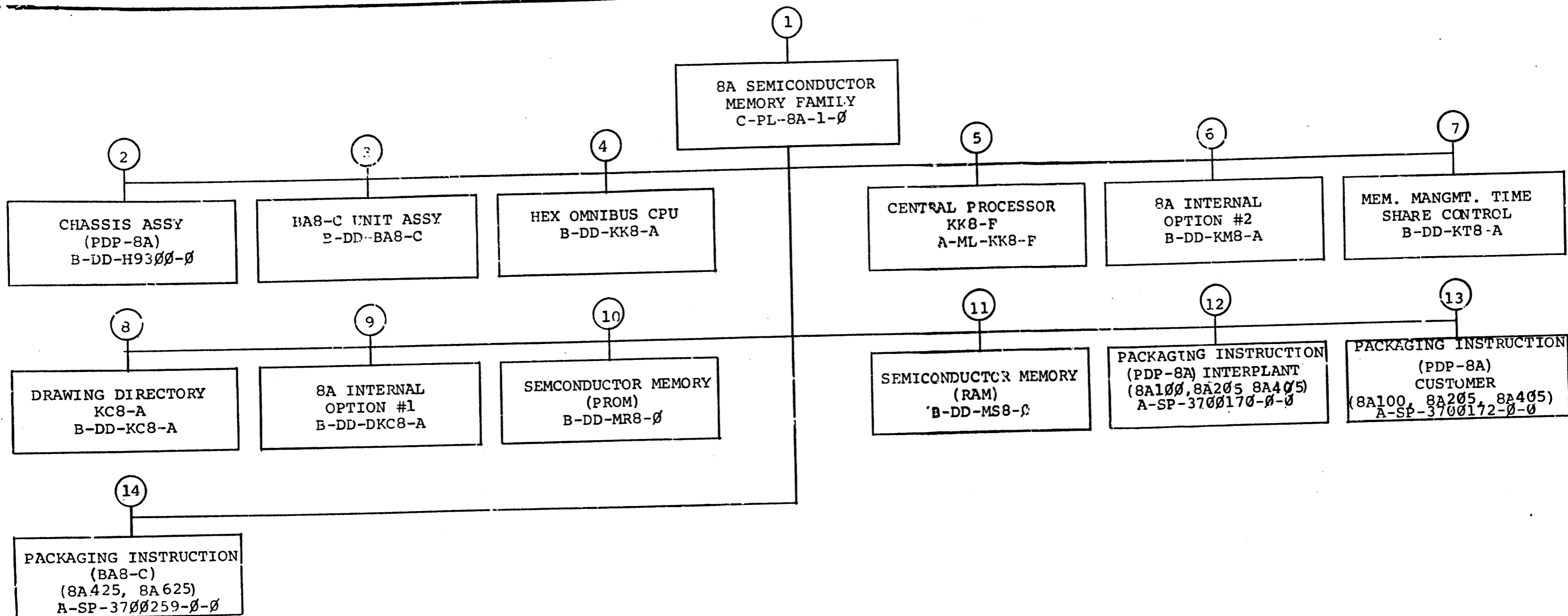
DRAWING DIRECTORY

FOR FIELD MAINTENANCE PRINT SET SEE
B-TC-8A-1-1

UNIT VARIATIONS

VAR	TITLE
8A100	12 SLOT OMNIBUS W/KK8A CPU
8A205	12 SLOT OMNIBUS W/KK8A CPU OPTION #2 AND G8016 REGULATOR
8A405	12 SLOT OMNIBUS W/KK8A CPU OPTION #2 AND G8018 REGULATOR
8A425	20 SLOT OMNIBUS W/KK8A CPU AND OPTION #2
8A625	20 SLOT OMNIBUS W/KKS-F CPU AND OPTION #2

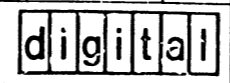
REVISIONS	REV.								
	CHANGE NO.								
	CHK								
USED ON OPTION/MODEL		DRM.	DATE	TITLE digital					
8A100	M.P. DUGGAN	12 77							
8A205	CHK'D.	11							
8A405	<i>Tammy Thorne</i>	JUL-77							
8A425	PROJ. ENG.	11							
8A625	<i>Tammy Thorne</i>	JUL-77	SIZE CODE NUMBER REV						
		PROB.	DATE	B DD	8A-1				
SHEET 1 OF 3		<i>J. J. Kane</i>	8/2/77	DIST.					



TITLE	SHEET	SIZE	CODE	NUMBER	REV
8A SEMICONDUCTOR MEMORY FAMILY	2 OF 3	B	DD	8A-1	

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
1	MP00415	FIELD MAINTENANCE PRINT SET (MP)	-	9	B-DD-DK08-A	8A INTERNAL OPTION #1	-
	B-TC-8A-1-1	FIELD MAINTENANCE PRINT SET (TC)	-				
	C-PL-8A-1-0	8A SEMICONDUCTOR MEMORY FAMILY (8A100, 8A200, 8A300, 8A320, 8A720)	E/M				
2	B-DD-H9300	CHASSIS ASSY (PDP-8A)	-	10	B-DD-MR0-0	SEMICONDUCTOR MEMORY (PROM)	-
3	B-DD-BA8-C	BA8-C UNIT ASSY	-	11	B-DD-MR0-0	SEMICONDUCTOR MEMORY (RAM)	-
4	B-DD-YK8-A	HEX OMNIBUS CPU	-	12	A-SP-3700170-0-0	PACKAGING INSTRUCTION, INTERPLANT (PDP-8A)	-
5	A-ML-KK8-F	CENTRAL PROCESSOR KK8-F	-	13	A-SP-3700172-0-0	PACKAGING INSTRUCTION, CUSTOMER (PDP-8A)	-
6	B-DD-KM8-A	8A INTERNAL OPTION #2	-	14	A-SP-3700259-0-0	PACKAGING INSTRUCTION, INTERPLANT W/CONSOLE (BA8-C)	-
7	B-DD-KT3-A	MEM MANAGEMENT AND TIME SHARE CONTROL	-				
8	B-DD-KC8-A	DRAWING DIRECTORY KC8-A	-				

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL



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DWG/PART NUMBER	DESCRIPTION	REV. 1																									
		BA100	BA105	BA110	BA115	BA120	BA125	BA130	BA135	BA140	BA145	BA150	BA155	BA160	BA165	BA170	BA175	BA180	BA185	BA190	BA195	BA200	BA205	BA210	BA215	BA220	BA225
1	D-UA-H9300-AA-0	1		1		1		1		1		1		1		1		1		1		1		1		1	
2	D-UA-H9300-AB-0		1		1		1		1		1		1		1		1		1		1		1		1		
3	D-UA-H9300-AC-0			1		1		1		1		1		1		1		1		1		1		1		1	
4	D-UA-H9300-AD-0				1		1		1		1		1		1		1		1		1		1		1		
5	E-UA-B38-CA-0																										
6	E-UA-B38-CB-0																										
7	E-UA-B38-CH-0																										
8	E-UA-B38-CJ-0																										
9	A-PL-KK8-A-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
10	A-PL-KK8-F-0																										
11	A-PL-MS8-CA-0																										
12	A-PL-MS8-CB-0																										
13	A-PL-MS8-AA-0																										
14	A-PL-MS8-AB-0																										
15	A-PL-MS8-AD-0																										
16	A-PL-RM8-FB-0																										
17	A-PL-RM8-AA-0																										
18	A-PL-DK8-AA-0																										
19	A-PL-KC8-AA-0																										
20	A-PL-KT8-A-0																										
21	A-PL-BA-1-2	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
22	A-SP-3700170-0-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
23	A-SP-3700172-0-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
24	A-SP-3700259-0-0																										
25	A-SP-3700259-01-0																										
26	A-SP-3700259-02-0																										
27	D-UA-H9300-BA-0																										
28	D-UA-H9300-BB-0																										
29	D-UA-H9300-BH-0																										
30	D-UA-H9300-BJ-0																										
31	A-PL-KM8-AB-0																										
32	A-PL-KM8-AC-0																										
33	A-PL-KM8-AD-0																										

REV. NUMBER SIZE CODE

REVISIONS
 CHK CHANGE NO. REV.
 A
 C. NORRIS
 5-11-77
 18 APR 77

DRN M.P. DUGGAN 12 MAY 77	FIRST USED ON	digital	
CHK'D J. Shuler 2 NOV 77	TITLE	BA SEMICONDUCTOR MEMORY FAMILY	
ENG. J. Shuler 3 NOV 77		BA100, BA205, BA405, BA425, BA605	
PROJ. ENG. J. Shuler 3 NOV 77			
PROD. J.V. Kane 3 NOV 77			
NEXT HIGHER ASSY.	SIZE	CODE	NUMBER
B-DD-BA-1	C	PL	BA-1-0
SCALE NONE			REV. A
SHEET 1 OF 3	DIST.		

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DWG/PART NUMBER	DESCRIPTION	REV. NUMBER																													
		BA205	BA205	BA205	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405	BA405
1	D-UA-H9300-AA-0																														
2	D-UA-H9300-AB-0																														
3	D-UA-H9300-AC-0		1																												
4	D-UA-H9300-AD-0	1																													
5	E-UA-BAB-CA-0																														
6	E-UA-BAB-CB-0																														
7	E-UA-BAB-CH-0																														
8	E-UA-BAB-CJ-0																														
9	A-PL-KKB-A-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
10	A-PL-KKB-F-0																														
11	A-PL-MSB-CA-0	1																													
12	A-PL-MSB-CB-0		1																												
13	A-PL-MSB-AA-0																														
14	A-PL-MSB-AB-0																														
15	A-PL-MSB-AD-0																														
16	A-PL-MRB-FB-0																														
17	A-PL-KMB-AA-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
18	A-PL-DKCB-AA-0	1	1	1																											
19	A-PL-KCB-AA-0	1	1	1																											
20	A-PL-KTB-A-0																														
21	A-PL-BA-1-2	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
22	A-SP-3700170-0-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
23	A-SP-3700172-0-0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
24	A-SP-3700259-0-0																														
25	A-SP-3700259-01-0																														
26	A-SP-3700259-02-0																														
27	D-UA-H9300-BA-0			1																											
28	D-UA-H9300-BB-0				1																										
29	D-UA-H9300-BH-0					1																									
30	D-UA-H9300-BJ-0						1																								
31	A-PL-KMB-AB-0																														
32	A-PL-KMB-AC-0																														
33	A-PL-KMB-AD-0																														

REV.	
CHANGE NO.	
CHK	

DRN. M. P. DUGGAN	12MAY77	FIRST USED ON		digital	
CHK'D. <i>J. M. L.</i>	8 Nov. 77	TITLE		8A SEMICONDUCTOR MEMORY FAMILY	
ENG. <i>J. M. L.</i>	8 Nov. 77			8A100, 8A205, 8A405, 8A425, 8A625	
PROD. J. V. K.	8 Nov. 77				
NEXT HIGHER ASSY.					
B-DD-BA-1			SIZE	CODE	NUMBER
SCALE NONE			C	PL	8A-1-0
SHEET 2 OF 3			DIST.		
					REV. 1

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
PARTS LIST

QUANTITY VARIATION

MADE BY M. DUGGAN	CHECKED <i>Tony Harbin</i>	SECTION 1
DATE 12 MAY 77	DATE 11-JUL-77	
ENG <i>Tony Harbin</i>	PROD <i>T. J. Kane</i>	ISSUED SECT. 1
DATE 11-JUL-77	DATE 25 OCT 77	

ITEM NO.	DWG NO./PART NO.	DESCRIPTION	QUANTITY VARIATION																	
			8A100	8A205	8A405	8A425	8A625													
1	EK-8A002-OP	PDP-8A OPERATOR'S HANDBOOK	1	1	1	0	0													
2	MP00415	8A SEMICONDUCTOR FAMILY MAINTENANCE PRINT SET																		
3	A-PL-8A-1-3	8A SEMICONDUCTOR FAMILY SOFTWARE LIST	1	1	1	1	1													
4	EK-8A002-MM	PDP-8A USER'S MANUAL	1	1	1	1	0													
5	EK-H9300-IP	H9300 ILLUSTRATED PARTS BREAKDOWN	1	1	1	0	0													
6	EK-BA8C-IP	BA8-C ILLUSTRATED PARTS BREAKDOWN	0	0	0	1	1													
7	EK-KC8A-IP	KC8-A ILLUSTRATED PARTS BREAKDOWN	0	1	1	1	1													

TITLE SHIPPING LIST 8A SEMICONDUCTOR MEMORY FAMILY (8A100, 8A205, 8A405, 8A425, 8A625)	ASSY NO. C-PL-8A-1-0	SIZE A	CODE PL	NUMBER 8A-1-2	REV	ECO NO.
	SHEET 1 OF 1	DIST				

DEC FORM
DRA. 110

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
PARTS LIST

MADE BY M.DUGGAN	CHECKED L. NARHI	SECTION 1
DATE 12-MAY-77	DATE 11-JUL-77	
ENG LARRY.NARHI	PROD J.KANE	ISSUED SECT.
DATE 11-JUL-77	DATE 25-OCT-77	

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION
1	ZF006-RB	4K BASIC SOFTWARE
2	ZF209-RB	4K OPTION #2 SOFTWARE
3	ZF208-RB	4K OPTION #1 SOFTWARE
4	ZF235-RB	MEMORY MANAGEMENT OPTION SOFTWARE

QUANTITY / VARIATION											
8A100	8A205	8A405	8A425	8A625							
0	1	1	1	1							
0	1	1	1	1							
0	1	1	1	1							
0	0	0	1	1							

TITLE SOFTWARE LIST 8A SEMICONDUCTOR MEMORY FAMILY (8A100, 8A205, 8A405, 8A425, 8A625)	ASSY NO. B-DD-8A-1	SIZE CODE A PL	NUMBER 8A-1-3	REV. A	ECO NO. 87-1 MLOO1
	SHEET 1 OF 1		DIST.		

DEC FORM DEC 16 (325) 1031 N870
DPA 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		DATE 26-apr-77				
ENGINEERING SPECIFICATION						
TITLE BA100,205,405,425,625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE						
REVISIONS						
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>S. J. Healey</i>	11-JUL-77	APPR <i>Healey</i>	11-JUL-77	SIZE	CODE	NUMBER	REV
DEC 15-1022-16-3770-107				A	SP	8A-1-4	6
DRA 107							1 of 5

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE BA100,205,405,425,625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE			
I. GENERAL			
II. UNPACKING			
III. INSPECTION			
IV. INSTALLATION PROCEDURE			
V. ACCEPTANCE PROCEDURE			

DEC FORM NO EN-01022-16-3770-108	DRA 108	SIZE	CODE	NUMBER	REV
		A	SP	8A-1-4	6
DRA 108					SHEET 2 OF 6

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE BA100,205,405,425,625 FIELD INSTALLATION ACCEPTANCE PROCEDURE			
I. GENERAL	Installation of the basic BA100,205,405,425 and 625 computer requires no special tools or equipment. Normal hand tools are all that are required.		
II. UNPACKING	Unpack and inspect the equipment using the procedure provided in the Operator's Handbook.		
III. INSPECTION	After removing the equipment packing material, inspect the equipment. <ol style="list-style-type: none"> Inspect the external surface of the chassis for surface, bezel, switch and light damage. Internally inspect the 8A enclosure and console for damage, loose nuts, bolts, screws, etc. Inventory all hardware against shipping list. Inventory all software against software list, if ordered. Inventory all prints against shipping list if ordered. 		
IV. INSTALLATION PROCEDURE	Install the equipment using the following procedure: <ol style="list-style-type: none"> Turn off the power switch of the Limited Function Console. <p style="text-align: center;"><u>WARNING</u></p> <p>DO NOT TOUCH THE COMPUTER AFTER PLUGGING IT IN UNTIL IT IS CHECKED FOR THE PROPER GROUNDING.</p> Insure that all power is received from the same source. Plug in the power cord. 		

DEC FORM NO EN-01022-16-3770-108	DRA 108	SIZE	CODE	NUMBER	REV
		A	SP	8A-1-4	6
DRA 108					SHEET 3 OF 6

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE BA100,205,405,425,625 FIELD INSTALLATION ACCEPTANCE PROCEDURE			
4.	Before touching the computer, check frame to ground to insure that no AC voltage is present.		
5.	Unplug power cord.		
6.	Turn "on" Power ON/OFF switch and set regulator circuit breaker to the "ON" position. (Behind the Limited Function Console on the BA100, BA205, and BA405 or inside the rear panel on the BA625 and BA625.)		
7.	Repeat Steps 3 and 4.		
8.	Power should now be applied to the 8A; fans should be running, and the power light on the Limited Function Console should be "ON". The light labeled "battery charging" should be illuminated on the BA625 and BA625 indicating that DC power is okay.		
9.	If none of the above occur, remove the Limited Function panel and check the Master/Slave switch located below the ON/OFF switch on the Limited Function Board.		
10.	The Run light should not be on. If it is, switch Power Off via the ON/OFF switch.		
11.	Remove the M8315 CPU module (on all except BA625) and set switches as indicated below. Then insert CPU in the first slot in the OMNIBUS and turn power back on. The Run light should remain off.		
12.	S1-1 thru S1-6, S1-8 set to "OFF" position. S1-7 set to "ON" position.		
13.	Check modules to insure they are located in their proper position in the OMNIBUS. Refer to the Operator's Handbook.		
14.	Check the operation of the Programmer's Console.		
15.	Manually load, deposit and examine to insure that memory modules are associated with the correct memory fields. If not, turn the unit off and reconfigure the memory modules to the correct fields.		

DEC FORM NO EN-01022-16-3770-108	DRA 108	SIZE	CODE	NUMBER	REV
		A	SP	8A-1-4	6
DRA 108					SHEET 4 OF 6



TITLE 8A100,205,405,425,625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

V. ACCEPTANCE PROCEDURE

Perform the acceptance tests referred to in table A. If abnormal indications are encountered, refer to the diagnostic listings for error descriptions. Refer to the operators handbook and the diagnostic listings for instructions on loading diagnostics.

Equipment required

1. 8A100,205,405,425,625 with 1-32K of semiconductor memory.
2. Programmer's Console (KC8A and DKC8A)
3. Paper tape input device.
4. Diagnostics and listings.

NOTE: If programmer's panel and paper tape input device are not available as part of the system being installed, they must be provided by the customer in good working order. If semiconductor memory is 1K PROM only, refer to the MR8-F Engineering Spec.

Table A

Acceptance 8A100,205,405,425,625

<u>Program Name</u>	<u>MAINDEC #</u>	<u>Accept Time</u>
PDP8A Central Processor Test	08-DJKKA	20 Minutes
1-32K Random Exerciser	08-DJEXA	20 Minutes
MS8-A or MS8-C/D MOS Memory	Refer to Acceptance Procedure for MS8-C/D and MS8-A.	
KT8-A Memory Management Option	Refer to Acceptance Procedure for KT8-A.	

SIZE A	CODE Sp	NUMBER 8A-1-4	REV
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TITLE 8A100,205,405,425,625 FIELD INSTALLATION ACCEPTANCE PROCEDURE

- | | |
|------------------|--|
| DKC8A Option One | Refer to Acceptance Procedure for DKC8A. |
| KM8A Option Two | Refer to Acceptance Procedure for KM8A. |

SIZE A	CODE SP	NUMBER 8A-1-4	REV
-----------	------------	------------------	-----