

NOTES:
 * USER MODE IS USED BY THE TIME SHARING OPTION ONLY; TO INHIBIT HALT, OSR, LAS, & PAUSE

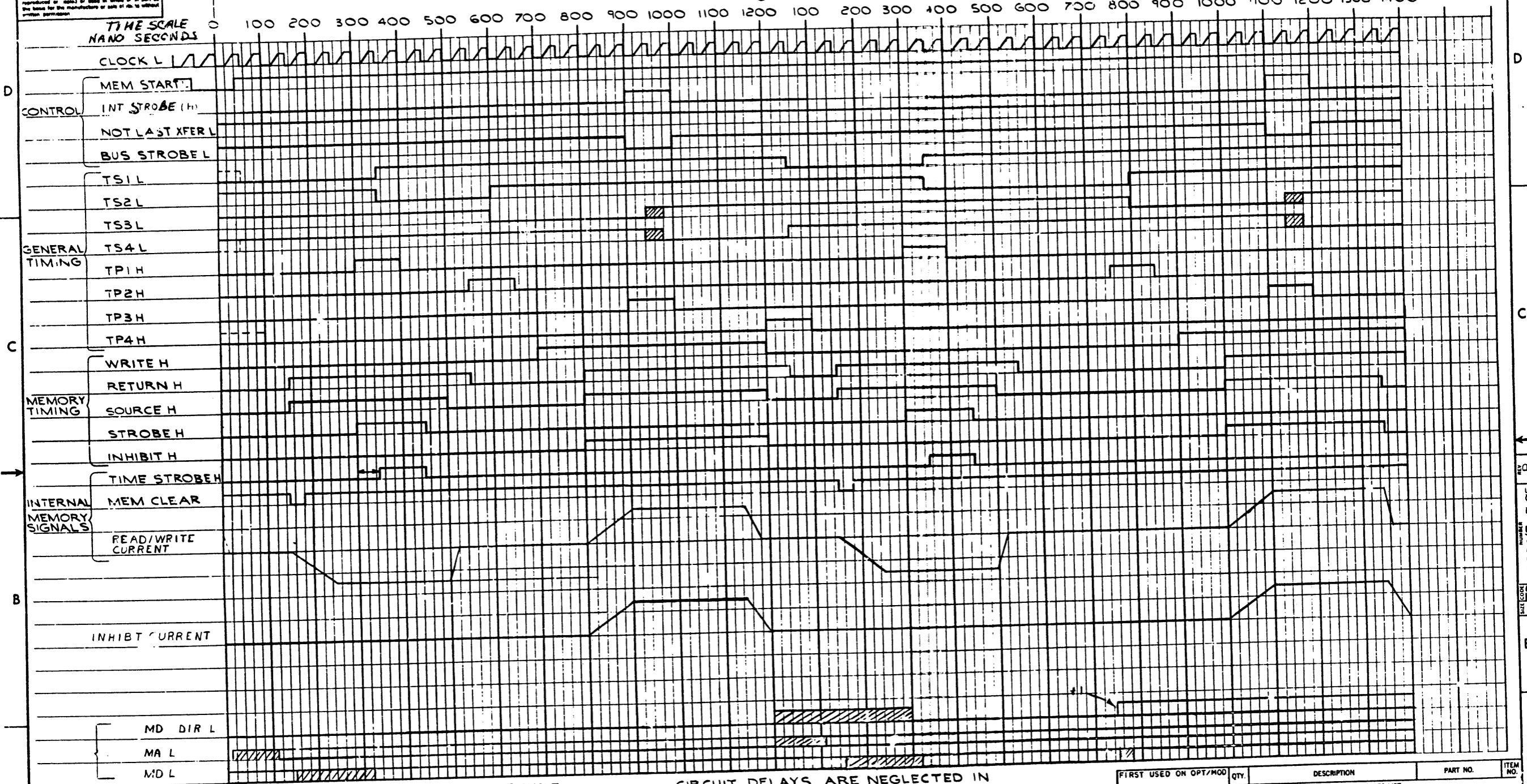
REV	DATE	BY
1	11-27-71	...
2	11-27-71	...

REVISED BY	DATE	BY																																																															
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<table border="1"> <tr> <th>QTY.</th> <th>DESCRIPTION</th> <th>PART NO.</th> <th>ITEM NO.</th> </tr> <tr> <td>1</td> <td>PROCESSOR FLOW CHART</td> <td></td> <td></td> </tr> </table>			QTY.	DESCRIPTION	PART NO.	ITEM NO.	1	PROCESSOR FLOW CHART																																																									
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PDP8/E-0-06

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FAST CYCLE # DURATION 1.2 MICRO SECONDS
SLOW CYCLE DURATION 1.4 MICRO SECONDS



*THIS PLOT SHOWS AN INITIAL FAST CYCLE
THE DOTTED LINES INDICATE A REGULAR CYCLE
*1: MD DIR GOES LOW ONLY IF F+ [D-AUTO INDEX]

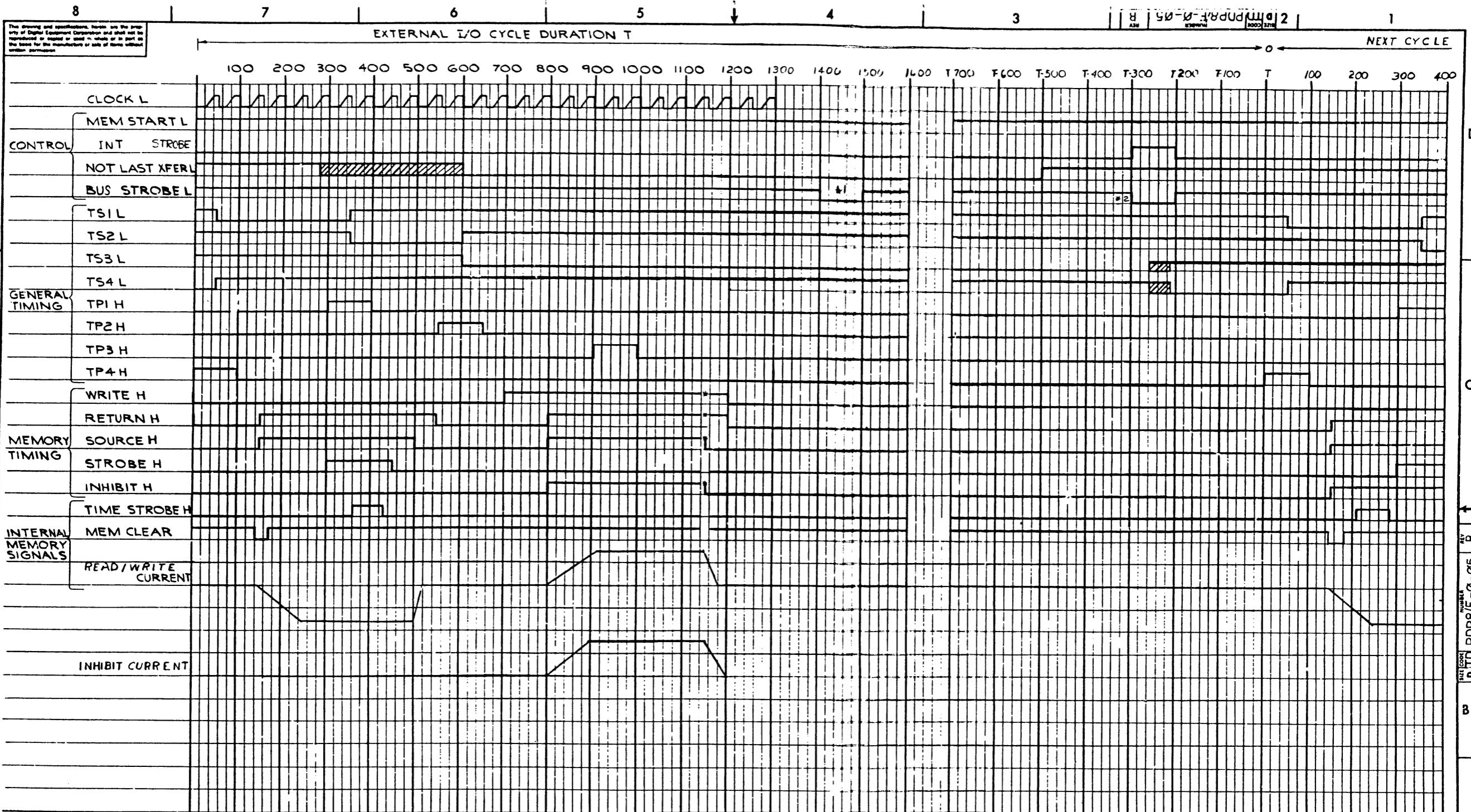
CIRCUIT DELAYS ARE NEGLECTED IN
THIS TIMING DIAGRAM

REVISIONS	
REV.	CHANGE NO.
A	1
B	1
C	1
D	1

DATE: 11-10-71
BY: KLOTZ

FIRST USED ON OPT/MOD PDP8/E	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES ± .008 ± 1/64 ± 0° 05' FINAL SURFACE QUALITY REMOVE BURS AND BREAK SHARP CORNERS	<i>P. Ferguson</i> DATE 10-71 <i>P. Ferguson</i> DATE 11-71 <i>P. Ferguson</i> DATE 11-71 <i>P. Ferguson</i> DATE 11-71	EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS		
MATERIAL + + +	NEXT HIGHER ASSY A-ML-PDP8/E-0	TITLE TIMING (PDP8/E)		
FINISH + + +	SCALE NONE	SIZE/DATE DITD PDP8/E-0-05	NUMBER 1 OF 2	REV. 1

REV. B
NUMBER
PDP8/E-0-05
DITD



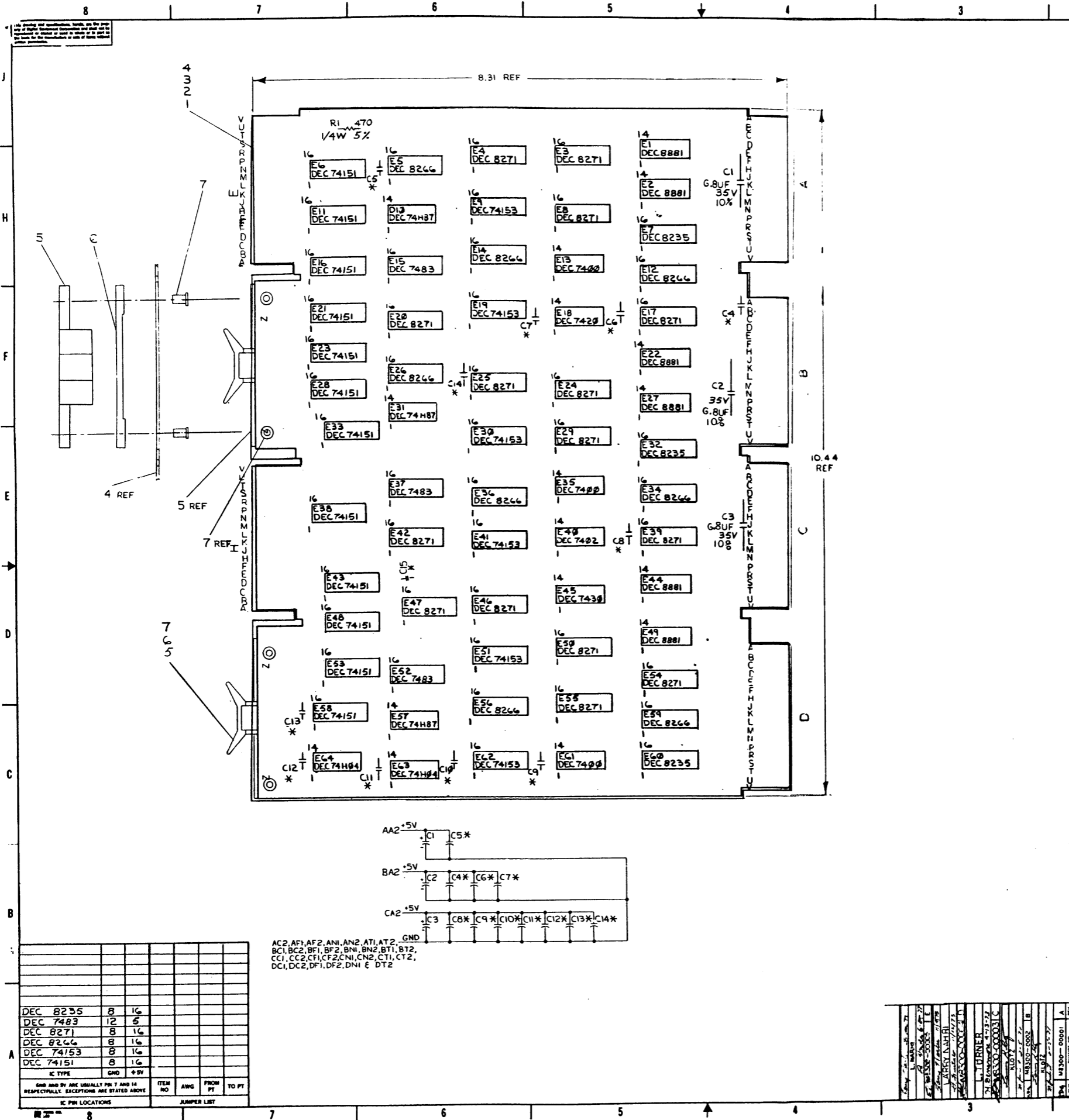
NOTE: * MEMORY SIGNALS TIME OUT, AS IN A FAST CYCLE
 * 1 GENERATED BY PERIPHERAL TO STROBE DATA
 * 2 GENERATED BY PERIPHERAL TO TERMINATE EXT. I/O CYCLE AND RESUME NORMAL OPERATION.

REV	
CHG	
CHK	

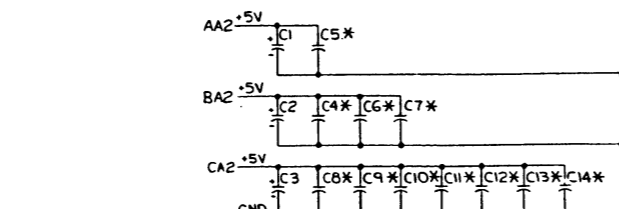
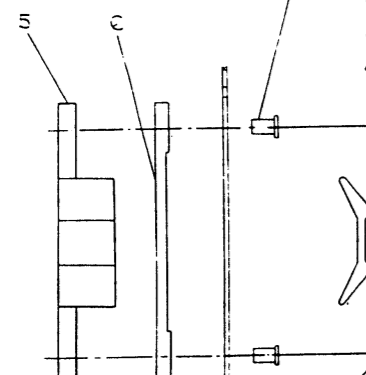
FIRST USED ON OPT/MOD PDP8/E	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED				
UNLESS OTHERWISE SPECIFIED	DATE	DATE		
DIMENSION IN INCHES	1/16	1/16		
TOLERANCES	DATE	DATE		
DECIMALS FRACTIONS ANGLES	1/32	1/32		
± .005 ± .001 ± .005	DATE	DATE		
FINAL SURFACE QUALITY	1/2	1/2		
REMOVE BURRS AND BREAK SHARP CORNERS	DATE	DATE		
MATERIAL	DATE	DATE		
FINISH	DATE	DATE		
NEXT HIGHER ASSY				
A-ML-PDP8/E-0				
SCALE NONE				
SHEET 2 OF 2				
DICT.				

EQUIPMENT CORPORATION
 MATHEWASH, MASSACHUSETTS
 TITLE
 TIMING
 (PDP8/E)
 SIZE CODE
 DTD PDP8/E-0-05
 NUMBER
 REV. 5

DEC FORM NO. 102A



NOTES:
 * THESE COMPONENTS MUST BE ON MB300
 IF KLB-A (MB319) IS USED ON SYSTEM.



AC2, AF1, AF2, AN1, AN2, AT1, AT2,
 BC1, BC2, BF1, BF2, BN1, BN2, BT1, BT2,
 CC1, CC2, CF1, CF2, CN1, CN2, CT1, CT2,
 DC1, DC2, DF1, DF2, DN1 & DT2

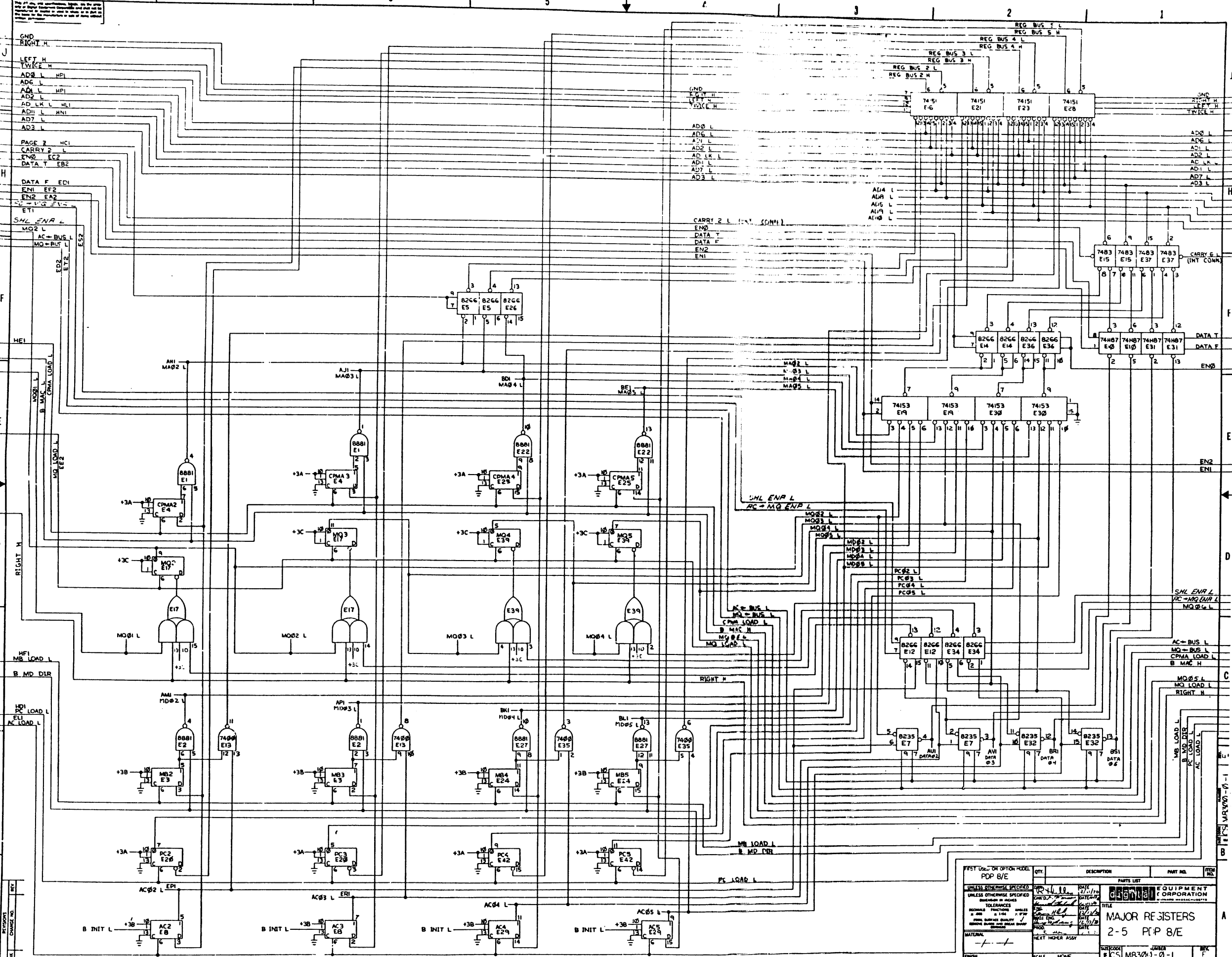
DEC 8235	8	16
DEC 7483	12	5
DEC 8271	8	16
DEC 8266	8	16
DEC 74153	8	16
DEC 74151	8	16
IC TYPE GND +5V		
GND AND BY ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE		
IC PIN LOCATIONS	JUMPER LIST	
ITEM NO	AWG	FROM PT TO PT

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	REV
1	C15	CAP 100PF D.M.	1000016	25
6	E9, E19, E30, E41, E51, E62	I.C. DEC 74153	1909937	23
12	E6, E11, E16, E21, E23, E28, E33, E38, E43, E48, E53, E58	I.C. DEC 74151	1909936	22
3	E7, E32, E60	I.C. DEC 8235	1909935	21
8	E5, E12, E14, E26, E34, E36, E56, E59	I.C. DEC 8266	1909934	20
3	E15, E37, E52	I.C. DEC 7483	1909932	19
2	E63, E64	I.C. DEC 74404	1909931	18
3	E10, E31, E57	I.C. DEC 74H87	1909927	17
6	E12, E22, E27, E44, E49	I.C. DEC 8881	1909705	16
15	E24, E25, E29, E35, E42, E46, E47, E50, E54, E55	I.C. DEC 8271	1909615	15
1	E40	I.C. DEC 7402	1909004	14
1	E45	I.C. DEC 7430	1908578	13
1	E18	I.C. DEC 7400	1908577	12
3	E13, E35, E61	I.C. DEC 8266	1908575	11
11	C4 - C14	RESISTOR 470 1/4W 5%	100274-00	9
4	C1	CAP 22UF 50V CER	100274-00	9
3	C1, C2, C3	CAP G.BUF 35V 20X5TAN	1000002	8
4	C4	EYELETS G54-11 STIMP	9006750	7
2	C5	SPACER (CABLE CLAMP)	1202704	6
1	C6	HANDLE FLIP CHIP MAGENTA	1008357	5
1	C7	ETCHED CIRCUIT BOARD	1500485	4
REF	C8	MODULE HISTORY LIST	8448800	3
REF	C9	ASSY/DRILLING HOLE LAYOUT	4448800	2
REF	C10	XY COORDINATE HOLE LOCK	4448800	1

DATE	BY	REV
11/11/71	W. TURNER	1
11/11/71	W. TURNER	2
11/11/71	W. TURNER	3
11/11/71	W. TURNER	4
11/11/71	W. TURNER	5
11/11/71	W. TURNER	6
11/11/71	W. TURNER	7
11/11/71	W. TURNER	8
11/11/71	W. TURNER	9
11/11/71	W. TURNER	10
11/11/71	W. TURNER	11
11/11/71	W. TURNER	12
11/11/71	W. TURNER	13
11/11/71	W. TURNER	14
11/11/71	W. TURNER	15
11/11/71	W. TURNER	16
11/11/71	W. TURNER	17
11/11/71	W. TURNER	18
11/11/71	W. TURNER	19
11/11/71	W. TURNER	20
11/11/71	W. TURNER	21
11/11/71	W. TURNER	22
11/11/71	W. TURNER	23
11/11/71	W. TURNER	24
11/11/71	W. TURNER	25

REV	DESCRIPTION	DATE						
1	PRINTED CIRCUIT BOARD REVISION	11/11/71						
2	CIRCUIT SCH REVISION	11/11/71						
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DEC NO.	EIA NO.	REV						
0-0A-KK8-E-0		1						
<table border="1"> <tr> <td>SCALE</td> <td>SHEET</td> <td>TOTAL SHEETS</td> </tr> <tr> <td>2/1</td> <td>1 OF 5</td> <td>5</td> </tr> </table>			SCALE	SHEET	TOTAL SHEETS	2/1	1 OF 5	5
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2/1	1 OF 5	5						
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SEMICONDUCTOR CONVERSION CHART	DATE	BY						
	11/11/71	W. TURNER						

Digital EQUIPMENT CORPORATION
 MAJOR REGISTERS (KK8/E)



FIRST USE, OR OPTION MODEL	QTY.	DESCRIPTION	PARTS LIST	PART NO.	ITEM NO.
PDP 8/E					
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES			CORVUX CORPORATION		
TOLERANCES UNLESS OTHERWISE SPECIFIED			EQUIPMENT		
DIGITAL FUNCTIONS	DATE: 10/2/70		OPERATION		
ANALOG FUNCTIONS	DATE: 10/2/70				
FORMAL SUBMIT QUALITY	DATE: 10/2/70				
REVISIONS	DATE: 10/2/70				
MATERIAL	DATE: 10/2/70				
FINISH	DATE: 10/2/70				
SCALE: NONE	SHEET: 3 OF 5	TITLE: MAJOR REGISTERS			
		SUBCODE: MCS M8301-0-1			
		REV: E			