

# MASTER DRAWING LIST

NO.	TITLE	UNIT VARIATIONS																			
		KL8-E	KL8-F3	KL8-F4	KL8-FC	KL8-FD	KL8-EE	KL8-EF	KL8-FG												
KL8-E	ASYNC DATA CONTROL	X	X	X	X	X	X	X													

REVISIONS		APP'D.	CHG. NO.	DATE

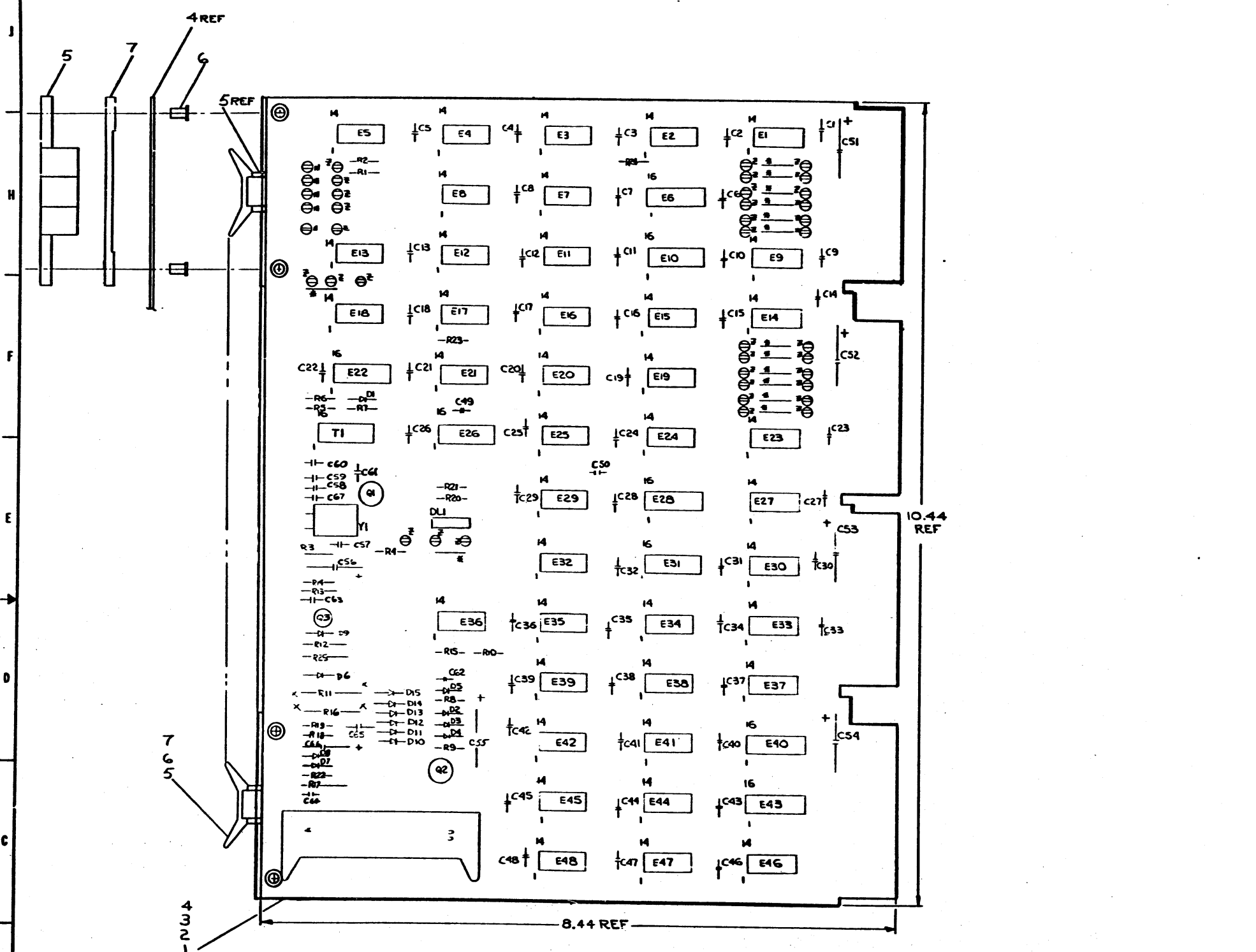
DRN.	DATE	CHK'D.	DATE	ENG.	DATE	MCNAMARA	DATE	VOGELSANG	DATE	L. SAYLOR	DATE	FIRST USED ON	PDP8/E	SCALE	SHEET	1	OF	2	DIST.	SIZE CODE	NUMBER	REV
K. GULICK	12-3-71	K. GULICK	12-3-71											A M L							KL8-E	E
<b>digital</b> CORPORATION MAYNARD, MASSACHUSETTS																						
ASYNC DATA CONTROL																						

PRINT SET		DWG. NO.			REV. NO. OF LET. SHEETS	TITLE		OPTION NO.
X	KL8E	E-CS-MB650-0-1	#	2	2	ASYNC. DATA CONTROL		
X		E-CS-MB650-YA-1	#	2	2	ASYNC. DATA CONTROL		
X		D-IA-7008360-0-0	1	1	1	CABLE ASSY		
X		D-IA-FC01V-25-0	1	1	1	CABLE ASSY		
X		A-SP-KL8-E-1	16			ENGINEERING SPECIFICATIONS		
X		A-PL-KL8-E-0	1			ASYNC. DATA CONTROL		
		A-SP-KL8-E-2	10			TEST PROCEDURE		
		A-SP-KL8-E-3	5			ACCEPTANCE		
-		LIBKIT-8E-KL8-E-02	REF	1		KIT LIST		
X		A-AL-KL8-E-4	1			ACCESSORY LIST		

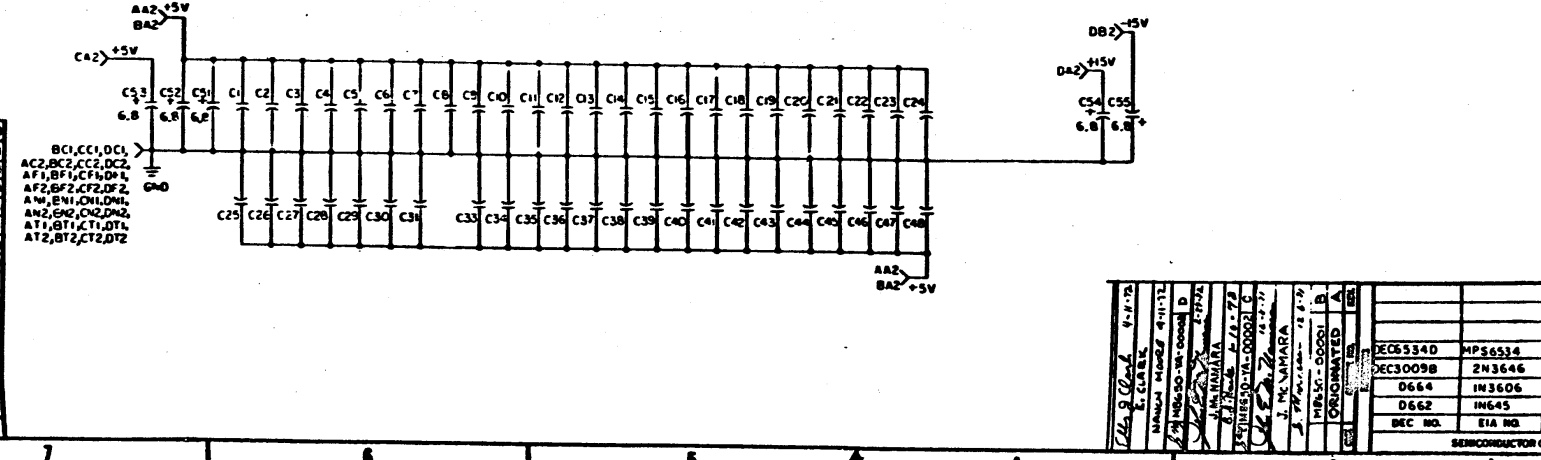
  

TITLE	ASNC. DATA CONTROL	SHEET	2	OF	2	SIZE CODE	A M L	NUMBER	KL8-E	REV	E
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See drawing and specifications for details. Do not use any of the following components unless specifically noted as being acceptable for use in this design. Do not use any of the following components unless specifically noted as being acceptable for use in this design.



IC PART LOCATIONS	QTY	DESCRIPTION	REF. NO.
DEC MC1488L	7	14-pin	
8251	8	16	
5384	1	8	
74193	8	16	
5314	1	8	
8271	8	16	
7493	10	5	
DEC 5380	1	8	
IC TYPE	QTY	DESCRIPTION	REF. NO.
8251	8	16	
5384	1	8	
74193	8	16	
5314	1	8	
8271	8	16	
7493	10	5	
DEC 5380	1	8	



**NOTES:**

- $\Delta$  : SPLIT LUGS  
 $\Delta$  : MACHINE INSERTED JUMPER  
 $\text{B}$  : 40 PIN HEADER CONNECTION
- DATA II DVI - OMNIBUS CONNECTION
- PIN F IS EIA TRANSMITTED DATA:  
 $+6V$  OR MORE = SPACE = 0  
 $-6V$  OR LESS = MARK = 1  
 PIN V IS EIA REQUEST TO SEND,  $+6V$  OR MORE = ON (PERMANENTLY).  
 PIN DD IS EIA DATA TERMINAL READY,  $+6V$  OR MORE = ON (PERMANENTLY).
- THIS DRAWING FOLLOWS DEC STANDARD 056 LOGIC SYMBOLOLOGY.  
 FLIP-FLOPS ARE NAMED FOR THE CONDITION THEY REPRESENT IN THE '1' STATE.  
 THE FOLLOWING FIGURES APPLY:

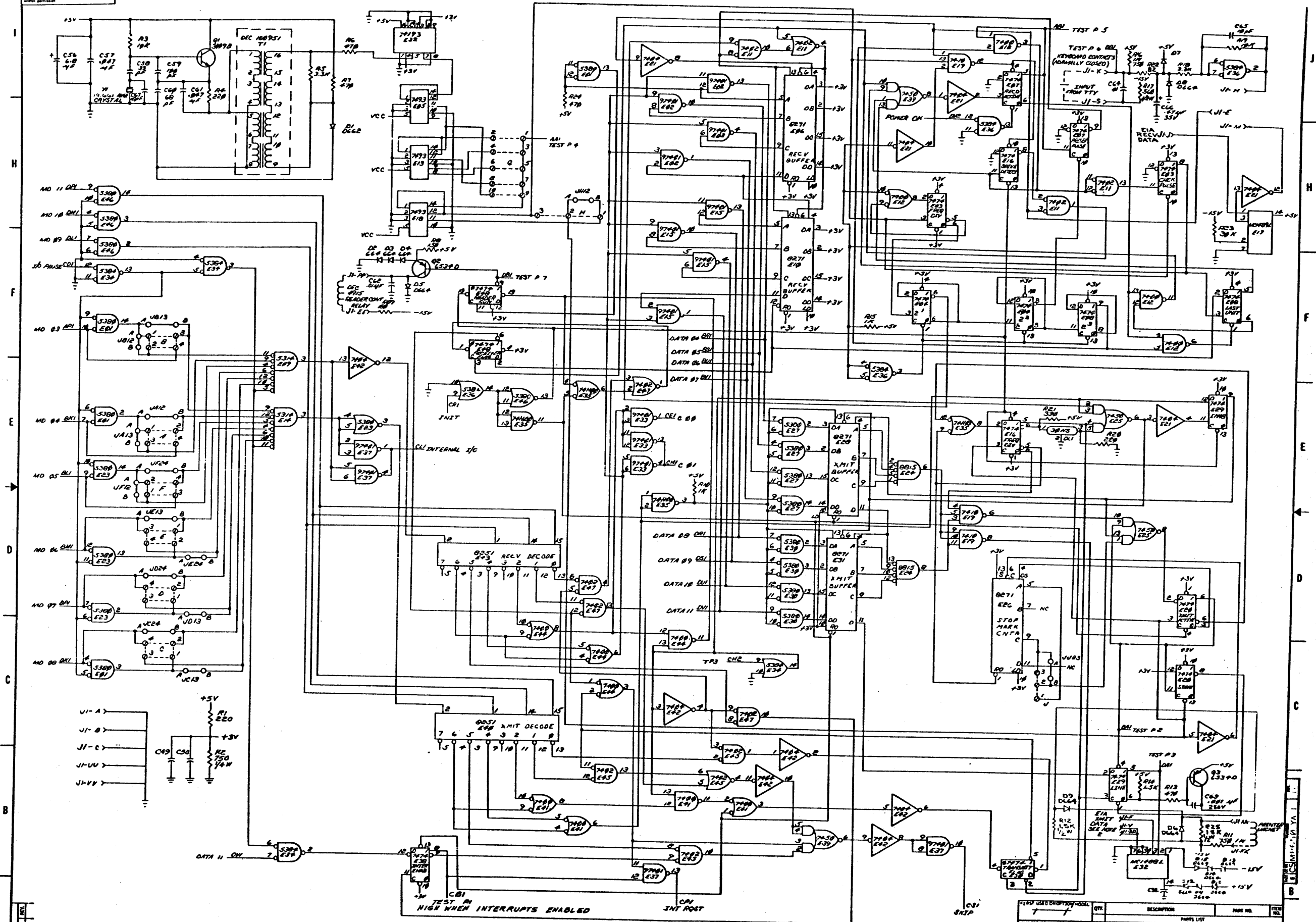
QTY	REF DESIGNATION	DESCRIPTION	PART NO.
2	R12, R25	RES. 1.5K 1/2 W 5%	1300394
1	R22	RES. 82 1/4 W 5%	1301477
5	E1, E23, E27, E30, E46	I.C. DEC 5380	1910392
4	E2, E15, E33, E37	I.C. DEC 97401	1909373
9	E3, E4, E7, E8, E16, E20, E29, E38, E48	I.C. DEC 7474	1905547
3	E5, E13, E18	I.C. DEC 7493	1909054
5	E6, E10, E26, E28, E31	I.C. DEC 8271	1909613
2	E9, E14	I.C. DEC 5314	1910391
3	E11, E43, E47	I.C. DEC 7402	1909004
3	E12, E41, E44	I.C. DEC 7400	1905575
1	E17	I.C. MC1488L EIA RECEIVER	1910323
1	E19	I.C. DEC 7410	1905576
2	E21, E42	I.C. DEC 7404	1909666
1	E22	I.C. DEC 74193	1910018
1	E24	I.C. DEC 8815	1909713
2	E25, E39	I.C. DEC 7450	1905580
1	E32	I.C. MC1488L EIA DRIVER	1910322
2	E34, E36	I.C. DEC 5384	1910394
2	E40, E43	I.C. DEC 8251	1909394
1	E35	I.C. DEC 7400D	1909056
52	C1-C50, C62, C64	CAP. .01-1K 100V 20% DISC	1001010
6	C51-C56	CAP. 6.8-1K 55V 20% TANT	1000067
2	C57, C61	CAP. .047-1K DISC	1009678
1	C58	CAP. 33PF MICA	1000009
1	C59	CAP. 100PF MICA	1000016
1	C60	CAP. 68PF MICA	1000014
1	C63	CAP. .001-1K 250V DISC	1000043
2	C65, C67	CAP. 10-1K 100V 5% MICA	1000006
1	C66	CAP. .47-1K 35V TANT	1005965
1	D1	DIODE, D662	1100113
14	D2-D15	DIODE, D664	1100114
3	R1, R4, R20	RES. 220 1/4 W 5%	1300271
1	R2	RES. 750 1/4 W 5%	1301401
2	R3, R19	RES. 10K 1/4 W 5%	1300479
2	R5, R18	RES. 3.3K 1/4 W 5%	1300439
4	R6, R13, R24	RES. 470 1/4 W 5%	1300316
1	R8	RES. 150 1/4 W 5%	1300280
2	R10, R15	RES. 1K 1/4 W 5%	1300365
2	R11, R16	RES. 750 1W 5%	1302385
1	R14	RES. 1.5K 1/4 W 5%	1300391
1	R21	RES. 330 1/4 W 5%	1300293
1	R23	RES. 30K 1/4 W 5%	1302394
1	R9	RES. 180 1/4 W 5%	1301322
1	R17	RES. 560 1/4 W 5%	1300338
1	Q1	TRANSISTOR, DEC 3009B	1503100
2	Q2, Q3	TRANSISTOR, DEC 6534D	1503409
1	TI	XFMR B010	1609651
1	DL1	DELAY LINE 30 NANO SEC	1605528
1	Y1	CRYSTAL 14.661 MHE	1809880-02
40	LUGS	5-PIN	9006735
1	CONNECTION	40 PIN	1209594
1/4	WIRE	#22 AWG SOLID BUS	9107561-01
4	SPACER	(CABLE CLAMP)	1302704
5	EYELET	GS4-11 STIMPSON	9006750
4	HANDLE	FLIP CHIP-MAGENTA	9008332-06
1	ETCHED	CIRCUIT BOARD	9002544
REF	MODULE	HISTORY LIST	B-MH-M8650-YA-1
REF	ASSY/DRILLING	HOLE LAYOUT	D-MH-M8650-YA-2
REF	XY	COORDINATE HOLE LOC.	K-CO-M8650-YA-1

ETCH BOARD REV	DATE	BY	DESCRIPTION
D			

QTY	DESCRIPTION	REF. NO.
1	ASYNCHRONOUS DATA CONTROL	
1	ECS M8650-YA-1	

SEMICONDUCTOR CONVERSION CHART	QTY	DESCRIPTION	REF. NO.
DEC 5340	1	MS6534	
DEC 3009B	1	2N3646	
D664	1	1N3606	
D662	1	1N645	

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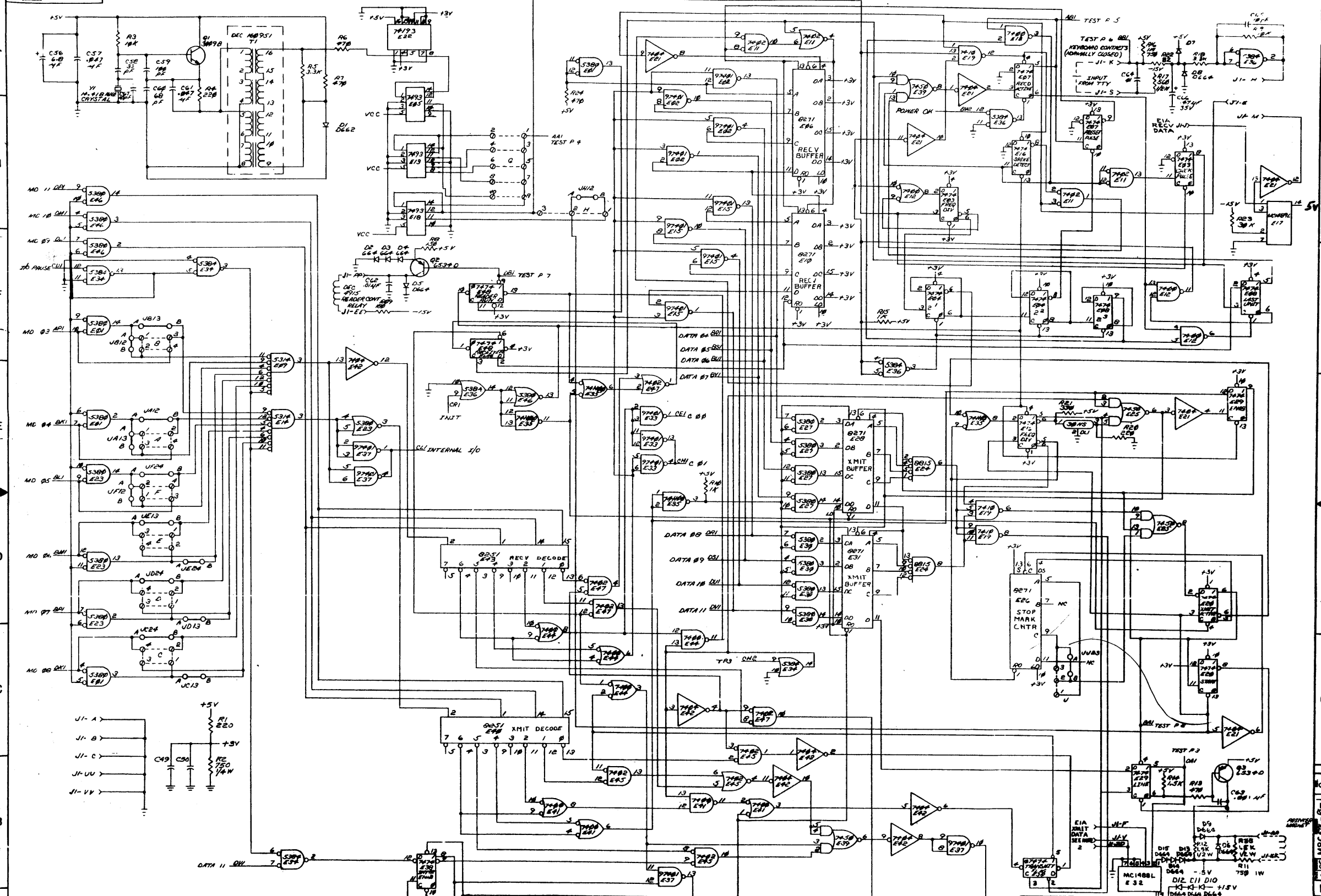
ITEM NO.	DESCRIPTION	PART NO.	QTY.
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EQUIPMENT CORPORATION

ASYNCHRONOUS DATA CONTROL

MB650-YA-1

This drawing and specifications, taken as the basis for the construction of the equipment, shall be the property of the Government and shall not be distributed outside the limits of the contract. It is to be used only for the purpose of the construction of the equipment as shown on this drawing.



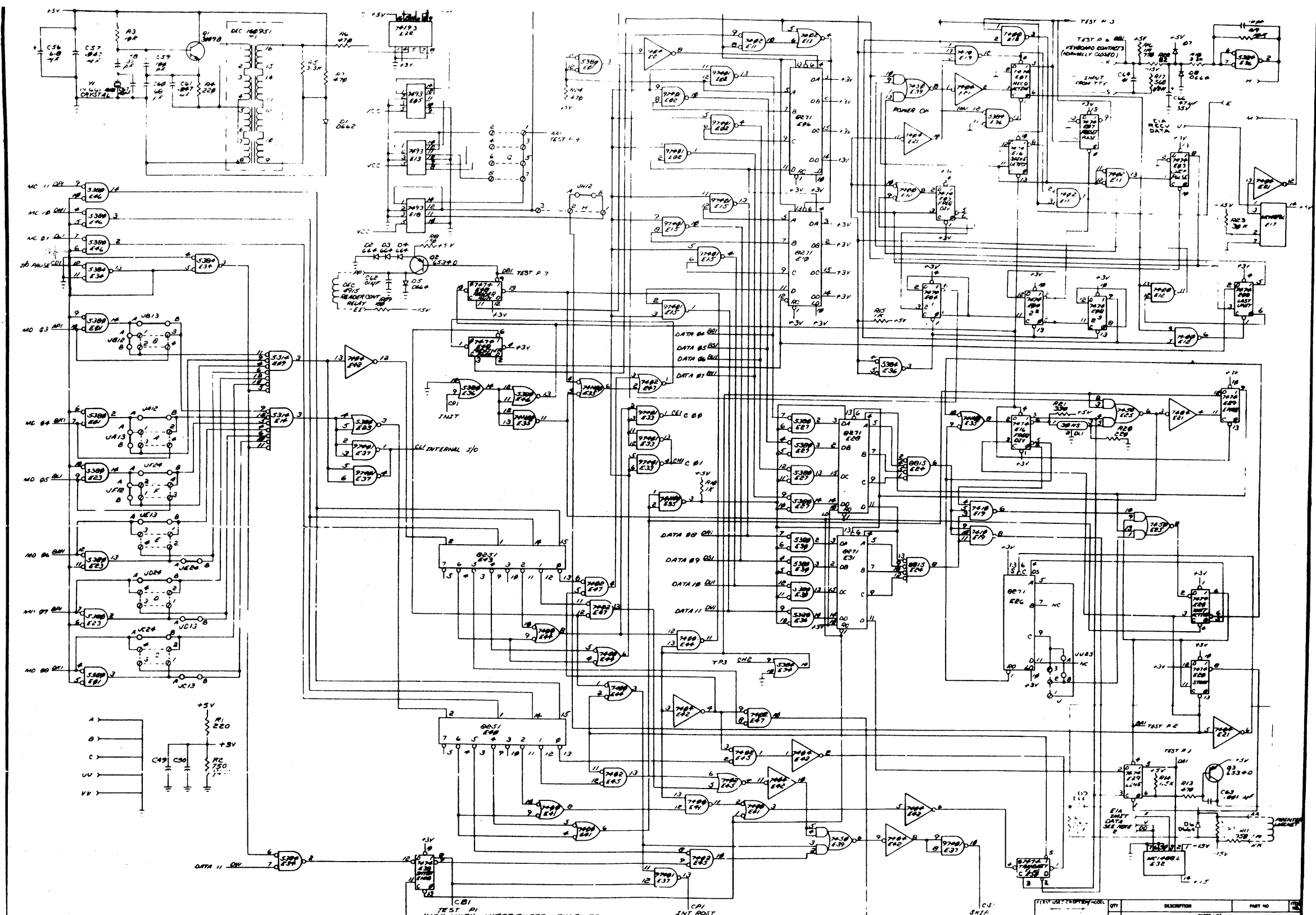
REVISIONS  
 NO. CHANGE NO. DATE

REV.	NO.	CHANGE NO.	DATE	DESCRIPTION	PART NO.	ITEM NO.

UNLESS OTHERWISE SPECIFIED		DATE	EQUIPMENT CORPORATION	
UNLESS OTHERWISE SPECIFIED		11-27-71	TITLE	
MATERIAL		DATE	ASYNCHRONOUS DATA CONTROL	
FINISH		DATE	SCALE NONE	
DRAWN		DATE	SHEET 2 OF 2	

ECS MB630-6-1



TEST POINT	DESCRIPTION	PART NO.	QTY.
TEST P 1	DATA 00	7400	1
TEST P 2	DATA 01	7400	1
TEST P 3	DATA 02	7400	1
TEST P 4	DATA 03	7400	1
TEST P 5	DATA 04	7400	1
TEST P 6	DATA 05	7400	1
TEST P 7	DATA 06	7400	1
TEST P 8	DATA 07	7400	1
TEST P 9	DATA 08	7400	1
TEST P 10	DATA 09	7400	1
TEST P 11	DATA 10	7400	1
TEST P 12	DATA 11	7400	1
TEST P 13	DATA 12	7400	1
TEST P 14	DATA 13	7400	1
TEST P 15	DATA 14	7400	1
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