IDENTIFICATION

PRODUCT CODE: MAINDEC-08-DKVTB-A-D

PRODUCT NAME: VT78 CPU DIAGNOSTIC

PRODUCT DATE: JULY 1977

MAINTAINER: DIAGNOSTIC GROUP

AUTHOR: BARRY S. POLAND

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION, DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS DOCUMENT.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITAL'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1977, BY DIGITAL EQUIPMENT CORPORATION

TABLE OF CONTENTS

1.0	ABSTRACT
2.0	REQUIREMENTS
2,1	HARDWARE
2.2	SOFTWARE
	STORAGE
2.4	PREREQUISITE SOFTWARE
3.0	RELATED DOCUMENTS
4.0	RESTRICTIONS
5.0	OPERATING INSTRUCTIONS
5.1	LOADING THE PROGRAM
5.2	PPOGRAM OPTIONS
5,2,1	SWITCH REGISTER SETTINGS HARDWARE CONFIGURATION SETTINGS
5,2,2	HARDWARE CONFIGURATION SETTINGS
5.3	CONSOLE TERMINAL PACKAGE
5.4	EXECUTION TIMES
6.0	ERROR INFORMATION
7.0	SUB-TEST SUMMARIES
7,1,1	CPU TESTS
	MEMORY EXTENSION TESTS
7.1.3	REAL TIME CLOCK TESTS
7.1.4	SERIAL LINE UNIT TESTS
7.1.5	PARALLEL INTERFACE TESTS
7.1.6	FLOPPY INTERFACE TESTS
8.0	PROGRAM LISTING

1.0 ABSTRACT

THIS DIAGNOSTIC WILL TEST ALL THE LOGIC ON THE VT78
PROCESSOR MODULE THAT CAN BE TESTED VIA PROGRAM INSTRUCTION.
THE MODULE LOGIC TESTED INCLUDES THE PDP+8 CPU, MEMORY EXTENSION
CONTROL, FLOPPY INTERFACE, THREE SERIAL LINE UNITS (SLU) AND THE PARALLEL
I/O INTERFACE. THE DIAGNOSTIC IS ORGANIZED WITH A SOFTWARE MODULE
FOR EACH OF THESE BASIC HARDWARE COMPONENTS. THE MAIN MEMORY FOR
THIS PROCESSOR IS CONTAINED ON A SEPARATE "DAUGHTER" MODULE AND
IS TESTED VIA THE VT78 MOS MEMORY DIAGNOSTIC (MAINDEC-08-DKVTA-A).

THE STANDARD PDP-8 CONSOLE PACKAGE WITH SOFTWARE CONTROLLED SWITCH REGISTER HAS BEEN INCORPORATED INTO THIS DIAGNOSTIC TO ALLOW THE PROGRAM TO RUN ON THE VT78 SYSTEM WHICH HAS NO HARDWARE SWITCH REGISTER. THIS PACKAGE PROVIDES AN INTERFACE BETWEEN THE USER AND THE DIAGNOSTIC VIA THE VIDEO DISPLAY/KEYBOARD TERMINAL.

THE VT78 PROCESSOP MODULE CONTAINS A SMALL AMOUNT OF LOGIC WHICH CAN NOT BE TESTED WITHOUT EXTERNAL DEVICES ATTACHED TO THE VT78 PERIPHERAL PORTS. FOR MANUFACTURING PURPOSES, A SPECIAL PERIPHERAL SIMULATOR PACKAGE HAS BEEN DESIGNED TO PLUG INTO THE VT78 PORTS AND PROVIDE DIAGNOSTIC ACCESS TO ALL MODULE LOGIC. THIS DIAGNOSTIC IS COMPATIBLE WITH THE HARDWARE SIMULATOR. ALL OTHER USERS OF THE DIAGNOSTIC WHICH DO NOT HAVE THE HARDWARE SIMULATOR, CAN FULLY TEST THE MODULE BY RUNNING BOTH THIS DIAGNOSTIC AND THE PERIPHERAL DIAGNOSTICS WITH RESPECTIVE PERIPHERALS ATTACHED TO VT78 SYSTEM.

THE PROGRAM IS COMPATIBLE WITH THE PDP-8 APT TEST SYSTEM.

THIS DIAGNOSTIC WILL RUN ONLY ON THE VI78 SYSTEM - IT WILL NOT PUN ON ANY OTHER PDP-8 SYSTEM.

- 2.0 REQUIREMENTS
- 2.1 HARDWARE

THE FOLLOWING HARDWARE IS REQUIRED FOR EXECUTION OF THIS PROGRAM:

VT78 VIDEO DATA PROCESSOR (INCLUDES PROCESSOR MODULE, 16K MEMORY MODULE, SIGNAL DISTRIBUTION BOARD, VIDEO DISPLAY AND KEYBOARD)

OPTIONAL EQUIPMENT INCLUDES THE PERPHERIAL SIMULATOR, THE RX01 DISKETTE SUBSYSTEM, AND THE LQP OR LA180 PRINTER.

NOTE: THE VIDEO DISPLAY AND KEYBOARD ARE NOT REQUIRED FOR TESTING UNDER APT CONTROL.

2,2 SOFTWARE

THE SOFTWARE ENVIRONMENTS IN WHICH THIS PROGRAM WILL EXECUTE INCLUDE:

- 1) STAND ALONE
- 2) UNDER OS/8 CONTROL
- 3) UNDER APT CONTROL

2.3 STORAGE

٩

THE PROGRAM OCCUPIES 8K OF MEMORY(2 FIELDS) AND MUST BE LOADED INTO FIELDS 0 AND 1.

2.4 PREFEQUISITE SOFTWARE

THE PANEL HANDLER AND BOOTSTRAP PORTIONS OF THE RESIDENT CONTROL PANEL PROGRAM MUST RUN SUCESSFULLY TO PERMIT LOADING OF THIS DIAGNOSTIC. THE RESIDENT DIAGNOSTIC SHOULD ALSO BE RUN PRIOR TO ATTEMPTING TO EXECUTE ANY VI78 SYSTEM DIAGNOSTICS.

3.0 RELATED DOCUMENTS

VT78 HARDWARE SPECIFICATION STANDARD APT SYSTEM TO PDP=8 DIAGNOSTIC INTERFACE MR78BA USER INFORMATION DOCUMENT MR78BB USER INFORMATION DOCUMENT

4.0 RESTRICTIONS

DURING MUCH OF THE SLU TEST SECTION OF THE DIAGNOSTIC, THE SLU WHICH INTERFACES TO THE VIDEO DISPLAY/KEYBOARD IS LOOPED UPON ITSELF. WHILE LOOPED, ANY ATTEMPT BY THE OPERATOR TO GAIN CONTROL VIA KEYBOARD INTERVENTION IS BLOCKED. THE OPERATOR CAN REGAIN SYSTEM CONTROL AT ANY TIME THROUGH USE OF THE START BUTTON ON THE SIDE OF THE VT78.