

IDENTIFICATION

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PRODUCT CODE: MAINDEC-08-DHTMA-A-D  
PRODUCT NAME: TMB-E CONTROL TEST PART 1  
DATE CREATED: DECEMBER 4, 1972  
MAINTAINER: DIAGNOSTIC GROUP  
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NOTE  
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THERE ARE SIX DIAGNOSTIC PROGRAMS ASSOCIATED WITH THE TM8-E DECMAGTAPE CONTROL AND ITS TRANSPORT SYSTEM. ALTHOUGH PHYSICALLY SEPARATE, THESE PROGRAMS MUST BE TREATED AS A LARGE INTEGRATED TEST, AND TO ENSURE PROPER SYSTEM OPERATION, THESE TESTS MUST BE EXECUTED IN THE ORDER DELINEATED BELOW.

IF A GIVEN TEST SHOULD FAIL AND IT APPEARS THAT A FIX HAS BEEN FOUND, ALL PROGRAMS MUST ONCE AGAIN BE RUN, ONLY WHEN ALL TESTS HAVE RUN WITHOUT ANY UNACCEPTABLE ERRORS CAN THE TM8-E SYSTEM BE CONSIDERED UP.

TM8-E DIAGNOSTIC PROGRAMS' ORDER OF EXECUTION  
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1. TM8-E CONTROL TEST PART 1 (MAINDEC-08-DHTMA)
2. TM8-E CONTROL TEST PART 2 (MAINDEC-08-DHTM8)
3. TM8-E DRIVE FUNCTION TIMER (MAINDEC-08-DHTMC)
4. TM8-E DATA RELIABILITY 9 TRACK (MAINDEC-08-DHTMD)
5. TM8-E DATA RELIABILITY 7 TRACK (MAINDEC-08-DHYME)
6. TM8-E RANDOM EXERCISER (MAINDEC-08-DHTMF)

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1. ABSTRACT  
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THE TM8-E CONTROL TEST PART 1 IS AN INTEGRATED SERIES OF SUB-TESTS DESIGNED TO AID IN THE CHECKOUT AND MAINTENANCE OF THE TM8-E DECMAGTAPE CONTROL WITHOUT DEPENDENCE ON TRANSPORT SYSTEM USE. VERSATILITY OF USAGE IS AFFORDED THROUGH A MODEST KEYBOARD MONITOR AND SWITCH REGISTER CONTROL OPTIONS.

THIS PROGRAM CONSISTS OF 12 MAJOR TESTS (TEST 01 THROUGH TEST 14) EACH OF WHICH CONSISTS OF A NUMBER OF SUBTESTS DESIGNATED BY THE LETTERS A THROUGH Z.

2. REQUIREMENTS  
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2.1 HARDWARE  
-----

PDP-8/E, 8/M, 8/F  
TELETYPE OR COMPATIBLE DEVICE (TTY)  
TM8-E DECMAGTAPE CONTROL

2.2 MEMORY  
-----

THIS PROGRAM REQUIRES 4K OF MEMORY AND MAY RESIDE IN ANY MEMORY FIELD; ALL OF EXISTING MEMORY IS USED TO TEST THE ABILITY OF THE TM8-E TO ADDRESS CORRECTLY DURING DMA TRANSFERS.

2.3 PRELIMINARY PROGRAMS  
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ALL PROCESSOR/MEMORY DIAGNOSTICS

3, CONVENTIONS  
-----

IN THE DESCRIPTION OF ANY KEYBOARD COMMANDS GIVEN IN THIS MANUAL, THE BACK ARROW (←) CORRESPONDS TO DEPRESSING THE RETURN KEY, AND THE NUMBER SIGN (#) CORRESPONDS TO DEPRESSING THE LINE-FEED KEY.

THE PROGRAM PRINTS A LEFT BRACKET (L) WHEN IT IS READY TO ACCEPT A KEYBOARD COMMAND.

4, PROGRAM LOADING PROCEDURE  
-----

LOAD THE PROGRAM INTO ANY DESIRED MEMORY FIELD USING THE STANDARD BINARY LOADER TECHNIQUE.

5, PROGRAM STARTING PROCEDURE  
-----

- A. LOAD ADDRESS 0200.
- B. LOAD THE EXTENDED ADDRESS WITH THE PROGRAM FIELD.
- C. SET THE SR TO 4000.
- D. DEPRESS CLEAR, THEN CONTINUE.
- E. THE PROGRAM WILL PRINT ITS TITLE AND MAINDEC NUMBER, THEN ASK "EXTENDED MEMORY?(0-7)". TYPE THE NUMBER OF THE HIGHEST EXISTING MEMORY FIELD, THEN THE PROGRAM MONITOR WILL ASSUME CONTROL. GO TO THE STANDARD TEST PROCEDURE IN PARAGRAPH 6.

NOTE: THE PROGRAM MAY BE RESTARTED AT ANY TIME AT ADDRESS 0201. IN THIS CASE THE PROGRAM BYPASSES ALL PROGRAM INITIALIZATION AND GOES DIRECTLY TO THE PROGRAM MONITOR TO ACCEPT KEYBOARD COMMANDS.

6. STANDARD TEST PROCEDURE  
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USE OF THE STANDARD TEST PROCEDURE ENSURES PROPER TM8-E CHECKOUT. ANY ERROR OCCURRENCE RESULTS IN AN ERROR REPORT ON THE TTY AND IN A RETURN TO THE PROGRAM MONITOR. ALL OPERATIONAL TEST PROCEDURES AFFORDED BY PROGRAM MONITOR AND SWITCH REGISTER CONTROL ARE DESCRIBED IN PARAGRAPH 7. ERROR RECOVERY PROCEDURES AND RELATED INFORMATION ARE GIVEN IN PARAGRAPH 8.

ACCOMPLISH THE FOLLOWING STEPS.

- A. START THE PROGRAM AS DESCRIBED IN PARAGRAPH 5.
- B. IF A TU10 TRANSPORT SYSTEM IS CONNECTED, ENSURE THAT ALL DRIVES ARE OFF LINE. ALL LEVELS INPUT FROM THE TU10 TO THE TM8-E SHOULD BE AT A "HIGH" LEVEL.
- C. SET THE SR=0001.
- D. TYPE "TA" WHICH RESULTS IN THE EXECUTION OF TEST 01 THROUGH TEST 14. SEVERAL PASSES WILL BE MADE OF EACH TEST.
- E. AFTER ALL TESTS HAVE BEEN EXECUTED, THE PROGRAM WILL PRINT "PASS 0001". THEN ANOTHER PASS IS AUTOMATICALLY STARTED.
- F. ALLOW THE PROGRAM TO RUN FOR AT LEAST 2 PASSES. EACH PASS TAKES APPROXIMATELY 15 MINUTES.

7. PROGRAM CONTROLS

THE FOLLOWING SUBPARAGRAPHS DESCRIBE THOSE CONTROLS WHICH THE USER HAS OVER THE PROGRAM.

THERE ARE TWO MAIN SOURCES OF PROGRAM CONTROL: A) PROGRAM MONITOR CONTROL VIA KEYBOARD COMMANDS; AND B) SWITCH REGISTER CONTROL.

7.1 PROGRAM MONITOR CONTROL VIA KEYBOARD COMMANDS

THE PROGRAM MONITOR KEYBOARD COMMANDS ARE DESCRIBED BELOW. IF A COMMAND ERROR IS DETECTED, MONITOR PRINTS "P" AND THE COMMAND MUST BE RETYPED. COMMANDS MAY BE INPUT AFTER MONITOR HAS PRINTED A LEFT BRACKET (C). THE TEST INTERRUPT COMMAND MAY BE TYPED AT ANY TIME.

THE PROGRAM MONITOR IS ENTERED UNDER ANY OF THE FOLLOWING CONDITIONS.

- A. AFTER PROGRAM STARTUP WITH SR 0=1, OR A RESTART AT 0201.
- B. AN ERROR OCCURS AND SR2=0.
- C. ALL SELECTED TESTS ARE DONE.
- D. THE TEST INTERRUPT COMMAND (REFERENCE PARAGRAPH 7.1.3) IS TYPED BY THE USER.

7.1.1 TEST SELECTION COMMANDS

THE FOLLOWING COMMANDS ARE USED TO SELECT FROM ONE TO TWELVE TESTS FOR EXECUTION, REGARDLESS OF THE ORDER IN WHICH A TEST SELECTION IS MADE; THOSE TESTS ARE EXECUTED IN NUMERICAL ORDER, IF A GIVEN TEST IS SPECIFIED TWICE IN THE SAME SELECTION, IT WILL BE DELETED FROM THAT SELECTION.

COMMAND	RESULT
TA	RUN ALL TESTS (TEST 01 THROUGH TEST 14)
T01110TNN	RUN THE TEST(S) INDICATED, T01 THROUGH T14 IN OCTAL ARE THE VALID SELECTIONS. IF THE USER SELECTS OTHER THAN T01 THROUGH T14 THE PROGRAM WILL EXECUTE ONE OF THE VALID TESTS (UNDETERMINED).

7.1.2 TEST CONTINUATION COMMANDS  
-----

THE FOLLOWING COMMANDS ARE USED TO CONTINUE IN THE TEST SEQUENCE IF THAT SEQUENCE HAS BEEN INTERRUPTED BY AN ERROR WITH SR2=0.

COMMAND	RESULT
-----	-----
C+	CONTINUE IN THE TEST SEQUENCE. IF NO TESTS ARE SELECTED, MONITOR WILL PRINT "?". IF THIS OCCURS A NEW TEST SELECTION MUST BE MADE.
EX+	CONTINUE WITH THE NEXT SUBTEST IF THE CURRENT SUBTEST IS FAILING AND SEVERAL SETS OF DATA REMAIN TO BE USED IN THAT SUBTEST. THIS COMMAND ENABLES THE USER TO EXIT A FAILING DATA SUBTEST AND CONTINUE WITH THE NEXT SUBTEST. AFTER THE CURRENT "EX+" COMMAND HAS BEEN UTILIZED FOR A FAILING DATA SUBTEST EXIT, IT IS NO LONGER EFFECTIVE. IF NO TESTS ARE SELECTED, MONITOR WILL PRINT "?". IN THIS CASE A NEW TEST SELECTION MUST BE MADE.

7.1.3 TEST INTERRUPT COMMAND

THE FOLLOWING COMMAND MAY BE USED TO INTERRUPT TESTING AND RETURN TO THE PROGRAM MONITOR.

COMMAND RESULT

ALTMODE KEY INTERRUPT TEST EXECUTION, RESPONDS TO "ALT" AND "ESC" KEYS. TEST SELECTION IS UNAFFECTED.

7.1.4 MISCELLANEOUS COMMANDS

COMMAND RESULT

TR TRACE THE PROGRAM FLOW USING THE ERROR REPORT FORMAT (MODIFIED). THIS COMMAND IS A PREFIX TO ALL "TEST SELECTION" AND "TEST CONTINUATION" COMMANDS, AND RESULTS IN A MODIFIED ERROR REPORT FOR EACH SUBTEST THAT PASSES. TRACE REPORTS ARE CONTROLLED VIA SR BITS 3 AND 4 IN THE SAME MANNER AS ERROR REPORTS. "TR" REMAINS IN EFFECT UNTIL THE NEXT ENTRY TO THE PROGRAM MONITOR.

DMINNN

DUMP THE CONTENTS OF MEMORY LOCATION NNNN IN FIELD M ON THE TTY. LINE-FEED DUMPS THE CONTENTS OF THE NEXT LOCATION; CARRIAGE RETURN RETURNS TO THE PROGRAM MONITOR, TEST SELECTION IS UNAFFECTED.

7.2 SWITCH REGISTER CONTROL OPTIONS

SR BIT	STATE	FUNCTION
0	0	RUN ALL TESTS NORMALLY.
	1	QUICK VERIFY (ONE PASS IS MADE OF EACH SELECTED TEST).
1	0	ENABLE TEST IN PROGRESS REPORTS, AS SOON AS A TEST IS STARTED THE TEST NUMBER (TESTNN) IS PRINTED ON THE TTY.
	1	DISABLE TEST IN PROGRESS REPORTS.
2	0	RETURN TO PROGRAM MONITOR ON ANY ERROR OCCURRENCE.
	1	RETURN TO PROGRAM MONITOR ONLY IF A FATAL ERROR HAS OCCURRED.
3	0	ENABLE ERROR AND TRACE REPORTS.
	1	DISABLE
4	0	ERROR AND TRACE REPORTS INCLUDE APPLICABLE DATA ONLY.
	1	FORCE ERROR AND TRACE REPORTS TO INCLUDE ALL POSSIBLE DATA.
5	0	DISABLE SUBTEST LOOP.
	1	LOOP ON CURRENT SUBTEST. ALL VARIABLE DATA REMAINS CONSTANT.
6-9	0	INHIBIT LOOPS 6-9 RESPECTIVELY.
	1	ENABLE LOOPS 6-9 RESPECTIVELY. LOOPS 6-9 ALLOW THE USER TO LOOP ON A SMALL GROUP OF SUBTESTS WITHIN THE CURRENT TEST THEREBY ENABLING INCREASING THE TESTING FREQUENCY OF A SMALL SET OF FUNCTIONS. THE LISTING MUST BE REFERENCED TO DETERMINE WHICH SUBTESTS ARE INCLUDED IN EACH LOOP. THE END OF EACH LOOP IS MARKED AS "*****LOOP N*****".
10	0	INHIBIT LOOP ON CURRENT TEST (TEST 01 = TEST 14).
	1	ENABLE LOOP ON CURRENT TEST.
11	0	TERMINATE TESTING UPON COMPLETION OF ALL SELECTED TESTS AND DELETE TEST SELECTION.
	1	RUN ALL SELECTED TESTS CONTINUOUSLY. AT THE COMPLETION OF EACH PASS THROUGH THE ENTIRE TEST SELECTION, THE NUMBER OF PASSES EXECUTED (IN OCTAL) IS PRINTED (PASS NNNN).

8. ERRORS

8.1 ERROR HALTS

THERE ARE NO ERROR HALTS IN THIS PROGRAM, IF AN ERROR SHOULD OCCUR AND SR2=0, THE PROGRAM WILL STOP TESTING AND RETURN TO THE PROGRAM MONITOR TO AWAIT A USER COMMAND. REFER TO PARAGRAPH 7.1 FOR AVAILABLE COMMANDS.



6.2 ERROR REPORTS

WHEN SR4=0, ERROR REPORTS INCLUDE ONLY THAT INFORMATION WHICH APPLIES DIRECTLY TO THE ERROR. IF SR4=1, ALL POSSIBLE INFORMATION IS PRINTED WHETHER APPLICABLE OR NOT. AN EXAMPLE OF A MAXIMUM INFORMATION ERROR REPORT IS SHOWN BELOW.

\*ER01D PC:0250 I1:6716 G0:0000 BD:7777 OD:7777  
WC:1234 CA:2345 CM:0000 FS:0000 MS:0000 DB:0000 AC:0000

THE SYMBOLS USED IN THE ERROR REPORTS ARE DEFINED BELOW.

SYMBOL DEFINITION

\*ERNXX ERROR OCCURRED IN TEST NN, SUBTEST X, (IF NN=00, THE ERROR OCCURRED OUTSIDE OF A FORMAL TEST.)

\*FENNX FATAL ERROR

TR\*NX TRACE REPORT INDICATOR, NO ERROR OCCURRED BUT TRACE IS ENABLED.

PC\*NNNN ADDRESS IN PROGRAM AT WHICH ERROR WAS DETECTED.

I1\*NNNN OCTAL CODE FOR IOT1 IN A VARIABLE SUBTEST.

I2\*NNNN OCTAL CODE FOR IOT2 IN A VARIABLE SUBTEST.

THE FOLLOWING THREE SYMBOLS ARE FURTHER DEFINED IN THE PROGRAM LISTING IN THE SUBTEST COMMENTS.

GD\*NNNN GOOD TEST VALUE

BD\*NNNN REAL TEST VALUE (BAD)

OD\*NNNN PREVIOUS GOOD TEST VALUE (OLD)

THE FOLLOWING SYMBOLS INDICATE THE CONTENTS OF THE SPECIFIED REGISTER AT THE TIME THE ERROR WAS DETECTED.

WC\*NNNN WORD COUNT REGISTER

CA\*NNNN CURRENT ADDRESS REGISTER

CM\*NNNN COMMAND REGISTER

FS\*NNNN FUNCTION/STATUS REGISTER

MS\*NNNN MAIN STATUS REGISTER

DB\*NNNN DATA BUFFER REGISTER

AC\*NNNN PROCESSOR ACCUMULATOR (VALID ONLY FOR ILLEGAL SKIP ERRORS.)

8.3 STANDARD ERROR RECOVERY PROCEDURE  
-----

THE STANDARD ERROR RECOVERY PROCEDURE ASSUMES THAT THE STANDARD TEST PROCEDURE IS BEING USED; THAT IS, THAT ALL ERROR OCCURRENCES RESULT IN AN ERROR REPORT AND IN A RETURN TO THE PROGRAM MONITOR.

WHEN AN ERROR OCCURS, USE THE FOLLOWING STEPS AS A GUIDE FOR RECOVERY REFERRING TO PARAGRAPH 8.2 FOR ERROR REPORT SYMBOL DEFINITIONS.

- A. REFERENCE THE POINT IN THE PROGRAM LISTING INDICATED BY THE "PC:" NUMBER.
- B. THE ERROR CODE IN THE LISTING SHOULD MATCH THE CODE IN THE ERROR REPORT (ERNX OR FENNX).
- C. COMMENTED IMMEDIATELY BELOW THE ERROR CODE IN THE LISTING IS AN EXPLANATION OF THE NUMBERS PRINTED AFTER THE GD, BD AND OD SYMBOLS.
- D. IN THE IMMEDIATE VICINITY OF THE ERROR CODE AND RELATED SUBTEST A DESCRIPTION OF THE SUBTEST WILL BE FOUND.
- E. IF THE ERROR IS FATAL (FENNX), THERE IS NO STANDARD RECOVERY. THE CAUSE OF THE FAILURE MUST BE DETERMINED THROUGH STATIC MEANS.
- F. IF THE ERROR IS NON-FATAL (ERNX), THE USER MAY ELECT TO CONTINUE IN THE TEST SEQUENCE (C+), OR TO ENTER A SUBTEST LOOP AS DESCRIBED BELOW.

SUBTEST LOOPS  
-----

TO ENTER A SUBTEST LOOP, CARRY OUT THE FOLLOWING STEPS:

- A. SET SR BITS 2, 3 AND 5#1.
- B. TYPE "C+".

TO EXIT A SUBTEST LOOP, RESTORE THE SWITCHES TO THEIR NORMAL POSITION.

9. RESTRICTIONS  
-----

NONE.

10. EXECUTION TIME  
-----

ONE NORMAL PASS OF ALL TESTS TAKES APPROXIMATELY 15 MINUTES.  
A QUICK VERIFY PASS (SRQ=1) TAKES ONLY 2 MINUTES.

11. TEST ABSTRACTS  
-----

TEST 01 VERIFIES THE EXISTENCE OF THE VARIOUS 670X AND 671X IOT'S EXCEPT RMSR (6714), AND TESTS THE BASIC OPERATION OF ALL TMS-E REGISTERS EXCEPT THE MAIN STATUS AND STATUS PORTION OF THE FUNCTION/STATUS REGISTERS.

TEST 02 IS A WORST CASE DATA TEST. DATA IS LOADED INTO AND READ BACK FROM ALL NON-STATUS REGISTER BITS AND VERIFIED.

TEST 03 VERIFIES THE EXISTENCE OF THE VARIOUS FUNCTIONS (AS FAR AS POSSIBLE WITHOUT A TRANSPORT) BY CHECKING EACH FUNCTION'S AFFECT ON CURRENT ADDRESS AND WORD COUNT INCREMENTING, AS WELL AS VERIFYING THAT DATA IS TRANSFERRED BETWEEN THE CONTROL AND THE PROCESSOR IN THE PROPER DIRECTION DURING DMA TRANSFERS.

TEST 04 TESTS THE ABILITY OF WORD COUNT, CURRENT ADDRESS AND COMMAND REGISTER BITS 6-8 TO INCREMENT PROPERLY.

TEST 05 VERIFIES THAT PROPER DATA TRANSFERS ARE MADE BETWEEN THE DATA BUFFER AND MEMORY IN BOTH DIRECTIONS.

TEST 06 VERIFIES THAT THE CURRENT ADDRESS REFERENCES THE CORRECT MEMORY LOCATION AND THAT COMMAND REGISTER BITS 6-8 REFERENCE THE CORRECT MEMORY FIELD. ALL EXISTING FIELDS ARE UTILIZED IN THIS TEST.

TEST 07 TESTS THE VARIOUS STATUS BITS TO CONTAIN THE PROPER STATUS AS GOVERNED BY THE "NO TRANSPORT" CONDITION.

TEST 10 CHECKS VARIOUS POSITIVE AND NEGATIVE CONDITIONS OF MAG TAPE FLAG, ERROR FLAG, GO BIT, ILLEGAL FUNCTION, READ-COMPARE ERROR AND EMA OVERFLOW ERROR AS A NO TRANSPORT CONDITION WILL ALLOW.

TEST 11 EXERCISES THE IOT'S SKEF AND SKTD UNDER INTERESTING CONDITIONS AS WELL AS OTHER SKIP AND NO SKIP IOT'S IN THE 672X IOT DECODER.

TEST 12 CHECKS THE UNIQUENESS OF INTERRUPTS AS CAUSED BY MAG TAPE FLAG AND THE ERROR FLAG.

TEST 13 VERIFIES THE UNIQUENESS OF ALL 672X IOT'S IN RELATION TO CLF AND SBRM.

TEST 14 COMPLETELY TESTS THE PROPER OPERATION OF THE READ-COMPARE LOGIC.

12. LISTING (ATTACHED)  
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/TM8E CONTROL TEST PART 1 MAINDEC=08-DHTMA-A=L

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\*\*\*\*\*  
/ WARNING: \*\*\*\*\*  
/ THIS PROGRAM SHOULD NOT BE STOPPED UNLESS \*\*\*\*\*  
/ MONITOR HAS TYPED "C", \*\*\*\*\*  
/ \*\*\*\*\*

/THIS DIAGNOSTIC PROGRAM TESTS AS MUCH OF THE TM8E CONTROL AS  
/IS POSSIBLE WITHOUT UTILIZING A TRANSPORT SYSTEM AND MAY  
/RESIDE IN ANY EXISTING MEMORY FIELD.

/BASIC STARTING PROCEDURE!  
/POWER DOWN THE TRANSPORT SYSTEM AS NECESSARY  
/TO ENSURE ALL SIGNALS RETURNING FROM THE TRANSPORT SYSTEM ARE HIGH,  
/LOAD THE PROGRAM INTO THE DESIRED MEMORY FIELD WITH THE BINARY LOADER;  
/LOAD THE INSTRUCTION AND DATA FIELDS WITH THE PROGRAM FIELD,  
/LOAD ADDRESS 200,  
/CLEAR ALL SWITCHES,  
/SET SR BIT 0 IF IT IS DESIRED TO SELECT TESTS WITH THE MONITOR,  
/IF SR0 IS CLEAR, ALL TESTS WILL BE RUN,  
/SET ANY OTHER SR OPTIONS DESIRED;  
/DEPRESS "CLEAR", THEN "CONTINUE".

/SWITCH REGISTER CONTROL OPTIONS.

/SR BIT STATE CONTROL  
/0! RUN ALL TESTS NORMALLY,  
/ 1 IF SET TO 1 ANYTIME PRIOR TO THE QUICK  
/ VERIFY MESSAGE, SELECT TESTS WITH MONITOR,  
/ IF SET TO 1 AFTER QUICK VERIFY MESSAGE, RUN QUICK VERIFY,  
/ (1 PASS OF EACH SELECTED TEST);  
/1! TEST PROGRESS REPORTS;  
/ 1 NO TEST PROGRESS REPORTS,  
/2! GO TO MONITOR ON ANY ERROR OCCURRENCE,  
/ 1 GO TO MONITOR ONLY UPON FATAL ERROR,  
/3! PRINT ERROR MESSAGES AND IF "TR" IS PENDING ENABLE TRACE MESSAGES,  
/ 1 PRINT ONLY FATAL ERROR MESSAGES,  
/4! ERROR MESSAGES DISPLAY NECESSARY DATA ONLY,  
/ 1 ERROR MESSAGES DISPLAY ALL DATA WHETHER APPLICABLE  
/ OR NOT, THIS SWITCH SHOULD BE USED ONLY

/ IF TEST 01 PASSES,  
/NOTE: IF SR5=1 WHEN EXITING MONITOR TO RUN A NEW TEST SELECTION, THE  
/PROGRAM MAY HANG UP IN THE ERZ08 TEST, RELEASING THIS SWITCH MOMENTARILY  
/WILL ALLOW THE PROGRAM TO CONTINUE WITH THE NEW SELECTED TESTS,  
/S1 0 NO SUBTEST LOOP,  
/ 1 LOOP ON SUBTEST, (SCOPE LOOP)  
/LOOPS 6-9 ARE SELECTED BY SR 6-9, THESE LOOPS ALLOW THE USER TO LOOP  
/ON A SMALL GROUP OF SUBTESTS WITHIN THE CURRENT TEST, THEREBY INCREASING  
/THE TESTING FREQUENCY OF A SELECTED FUNCTION, THE LISTING MUST BE REFERENCED  
/TO DETERMINE WHICH SUBTESTS ARE INCLUDED IN EACH LOOP,  
/IF LOOP N IS SELECTED, SETTING LOOP N=1 SWITCH WILL TRANSFER TO THAT  
/LOOP UPON COMPLETION OF THE PRESENT LOOP, BY THIS MEANS IT IS POSSIBLE

/TO BACKTRACK IN A GIVEN TEST,  
/6-9: 0 NO LOOP 6-9 RESPECTIVELY,  
/ 1 LOOP 6-9 RESPECTIVELY, (LOOP ON SMALL GROUP  
/ OF SUBTESTS),  
/10: 0 NO LOOP ON CURRENT TEST (T01-T14),  
/ 1 LOOP ON CURRENT TEST (T01-T14),  
/11: 0 NO LOOP ON COMPLETE TEST AS SELECTED,  
/ 1 LOOP ON COMPLETE TEST AS SELECTED  
/ (OCTAL PASS NUMBER TYPED AT END OF EACH PASS).

/TTY KEYBOARD COMMAND CONTROL.  
 /NOTES: 1. THE FOLLOWING COMMANDS MAY BE ENTERED WHEN "C" IS  
 /TYPED.  
 / (UPON ENTRY MONITOR TYPES THE CONTENTS OF LOCATION 2, AND THE TEST  
 / THAT IS CURRENTLY BEING RUN, THEN "C")  
 /2. "←" INDICATES "RETURN" KEY; "RUSOUT" ADRPTS A COMMAND STRING  
 / PRIOR TO "←"; "SPACE" MAY BE INPUT AT ANY POINT.  
 /3. "ALTMODE" INTERRUPTS THE MAIN PROGRAM AND RETURNS TO MONITOR  
 / ANYTIME THAT THE INTERRUPT INDICATOR IS ON.

/COMMAND RESULT  
 /"T01T02T05TNN←" RUN THE TESTS INDICATED. IF A TEST NUMBER IS INPUT 2  
 / TIMES, THAT TEST IS NOT RUN.  
 / RUN ALL TESTS.  
 /"TA←" CONTINUE FROM POINT OF INTERRUPTION, IF NO TESTS ARE  
 /"C←" PENDING OR A FATAL ERROR OCCURS, "C←" RESULTS IN  
 / SYNTAX ERROR.  
 /"EX←" IF A SUBTEST CONTAINS A LOOP TO CHECK OUT VARYING DATA  
 / PATTERNS, AND IF ERRORS KEEP OCCURRING PREVENTING CONTINUING  
 / ON QUICKLY WITH THE NEXT SUBTEST, DEPRESS "ALTMODE"  
 / (OR ALLOW RETURN TO MONITOR ON ERROR IF THE INTERRUPT SYSTEM  
 / IS OFF DURING THIS TEST.)  
 / THEN "EX←". THIS WILL EXIT THE CURRENT SUBTEST AFTER  
 / THE NEXT ERROR ALLOWING THE TEST TO CONTINUE AT THE NEXT  
 / SUBTEST. THE "EXITFL" WHICH IS SET BY "EX←"  
 / IS CLEARED AS SOON AS IT IS UTILIZED FOR AN  
 / ESCAPE. IF NO TESTS ARE PENDING OR A FATAL ERROR  
 / OCCURS, "EX←" RESULTS IN SYNTAX ERROR.  
 / TRACE USING ERROR MESSAGE FORMAT IF ERROR DOES NOT OCCUR.  
 /"TR" "TR" MAY PRECEED "TA", "TNN", "C", OR "EX" COMMANDS;  
 / TRACE REMAINS IN EFFECT UNTIL "C" IS DISPLAYED AGAIN.  
 / IT RESULTS IN A MODIFIED ERROR MESSAGE BEING DISPLAYED  
 / FOR EACH SUBTEST RUN IF THAT TEST PASSES; THESE  
 / PRINTOUTS ARE CONTROLLED BY THE SAME SR OPTIONS AS  
 / ERROR MESSAGES.  
 /"DN:MMMM" DISPLAY THE CONTENTS OF MEMORY LOCATION MMMM IN FIELD N.  
 / IF A LINE FEED IS TYPED THE NEXT SEQUENTIAL MEMORY  
 / LOCATION IS TYPED, WHEN THE ADDRESS OVERFLOWS THE  
 / NEXT MEMORY FIELD WILL BE REFERENCED AND THE  
 / CONTENTS DISPLAYED. A CARRIAGE RETURN ALLOWS THE INPUT  
 / OF A NEW COMMAND.



/ERRORS:

/IF AN ERROR OCCURS, REFER TO THE ERROR PRINTOUT AND THE PROGRAM LISTING FOR THE SUBTEST FAILURE DESCRIPTION.

/ERROR PRINTOUTS:

/HEADER  
/\*\*\*\*\*  
DEFINITION  
\*\*\*\*\*

```

/ERNX      ERROR OCCURRED IN TEST NN SUBTEST X(TNNX);
/FENX      SAME AS ABOVE EXCEPT FATAL ERROR;
/TR*NX     TRACE PRINTOUT (FROM "TR" MONITOR COMMAND);
/PC:NNNN  ADDRESS IN PROGRAM AT WHICH ERROR OCCURRED;
/11:NNNN  OCTAL CODE FOR I011;
/12:NNNN  OCTAL CODE FOR I012;
/60:NNNN  GOOD TEST VALUE; DESCRIBED IN SUBTEST LISTING;
/80:NNNN  BAD OR ACTUAL TEST VALUE; DESCRIBED IN SUBTEST LISTING;
/00:NNNN  USUALLY PREVIOUS GOOD; DESCRIBED IN SUBTEST LISTING;
/THE FOLLOWING DISPLAY THE CONTENTS OF THE INDICATED REGISTER:
/HC:NNNN  WORD COUNT;
/CA:NNNN  CURRENT ADDRESS;
/CM:NNNN  COMMAND;
/FS:NNNN  FUNCTION * STATUS;
/MS:NNNN  MAIN STATUS;
/DB:NNNN  DATA BUFFER;
/AC:NNNN  ACCUMULATOR (USED FOR SKIP ERRORS ONLY).

```

/PROGRAM DIRECTORY:

NO PUNCH  
#7600

7600

/REFER TO ASSEMBLY COLUMN JUST TO THE LEFT OF THE MNEMONIC FOR THE  
/ABSOLUTE STARTING ADDRESS OF THAT PARTICULAR PROGRAM SEGMENT,  
/(THIS IS FOR REFERENCE ONLY. DO NOT USE THESE STARTING ADDRESSES.)

/MNEMONIC - PROGRAM SEGMENT.

7600	0203	TEST01	/TEST SECTION:
7601	0600	TEST02	/670X,671X IOT EXISTENCE AND BASIC WC,CA,CM,DB,FR REGISTER TEST.
7602	1000	TEST03	/WC,CA,CM,DB,FR REGISTER DATA TEST.
7603	1235	TEST04	/BASIC FUNCTIONS EXISTENCE TEST.
7604	1400	TEST05	/WC,CA REGISTERS INCREMENT TEST (INCLUDES CM6=8).
7605	1600	TEST06	/MEMORY-DB DATA TEST.
7606	2000	TEST07	/CA REGISTER MEMORY REFERENCE TEST (USES ALL EXISTING MEMORY FIELDS).
7607	2200	TEST08	/STATUS REGISTERS STAND ALONE TEST.
7610	3000	TEST11	/BASIC TESTS FOR MTF, ILLEGAL FUNCTION, READ COMPARE, AND EF.
7611	3200	TEST12	/MTF AND EF SKIP AND SKIP TEST.
7612	3400	TEST13	/MTF-EF INTERRUPT TEST.
7613	3600	TEST14	/672X IOT UNIQUENESS TEST.
			/COMPLETE READ COMPARE TEST.
7614	4200	EXEC	/PROGRAM CONTROL SECTION:
7615	4400	MONIT	/EXECUTIVE.
7616	5400	INTSEV	/MONITOR.
			/INTERRUPT SERVICE.
7617	5000	ERRORS	/ERROR HANDLERS:
7620	5600	COMP	/ERROR HANDLING ROUTINE.
7621	6677	FE00A	/ERROR DETECTOR.
			/ERROR TABLE.
7622	4000	S6700	/UTILITIES:
7623	1624	MARK3	/TM8E IOT SUBROUTINES,
			/(IF THE DEVICE CODES ARE
			/NOT 70-72 IN YOUR SYSTEM
			/CHANGE ONLY THE IOT CODES IN
			/EACH OF THE 23 SUBROUTINES AND IN THE LOCATION MARK3.)
7624	6105	LAS5	/LOOP SENSORS,
7625	6200	AMG8E	/PACKED TEXT MESSAGE GENERATOR,
7626	6244	TYPE	/TTY ROUTINES.
7627	6301	EDIT	/SOCIAL NUMBER OUTPUT EDITOR,
7630	6400	CLR1	/"INITIALIZE" GENERATORS.
7631	6432	OFFLNE	/FUNCTION ROUTINES,
			/DATA SECTION:
7632	6613	T1LS1	/TEST01 IOT LISTS,
7633	6647	T3LS1	/TEST03 FUNCTION LIST,
7634	3146	T11LS1	/TEST11 IOT LIST
7635	3337	T12LS1	/TEST12 INTERRUPT ENABLE LIST,
7636	3146	T11LS1	/TEST13 IOT LIST.
7637	0000	0000	/CONSTANTS, POINTERS, ETC.
7640	7163	MSG1	/MESSAGE TEXTS.
			ENPUNCH

```

0000 *0000
0000 0000
0001 5001
0002 3002
0003 0003
0004 1200
0005 2003

      0
      JMP      1
      2
      3
T3LODP, T3LOAD
K3,      3

```

/LOCATIONS 1-3 ARE ASSEMBLED SET  
/UP FOR HIGH SPEED DUMP. THEY  
/ARE CHANGED FOR INTERRUPT HANDLING  
/BY THE PROGRAM,

```

0020 *0020
      1
      2
      7
      10
      20
      40
      70
      100
      200
      215
      400
      2000
      4000
      4040
      7400

```

/WIDE USAGE NUMERIC CONSTANTS, (POSITIVE)

```

0037 7776
0040 7774
0041 7773
0042 7764
0043 7760
0044 7520
0045 7510

0046 6201

```

/WIDE USAGE NUMERIC CONSTANTS, (NEGATIVE)

```

0047 0050
0050 0000
0051 0000
0052 0000
0053 0000
0054 0000

```

/WIDE USAGE INSTRUCTIONAL CONSTANTS:  
K0DF, CDF

/TRIAL BUFFER AREA.

XBUFF, XBUFF  
XBUFF, 0

/WIDE USAGE POINTERS

```

0055 4200
0056 6511
0057 4144
0060 5463

```

EXEC, EXEC  
FUNC, FUNC  
SCDF, SCDF  
INSRSP, INSRSP

0061 6200 AMG8EP, AMG8E  
 0062 4400 MONITP, MONIT  
 0063 6301 EDITP, EDIT  
 0064 6326 EDTEMP, EDTEM  
 0065 6262 BELLP, BELL  
 0066 6270 CRLF, CRLF  
 0067 7313 ERMSGP, ERMSG  
 0070 6244 TYPEP, TYPE  
 0071 5507 EXECFP, EXECFX

/TM8E IOT SUBROUTINE POINTERS,

0072 4000 S6700P, S6700 /LOAD IOT,  
 0073 4004 SLWCRP, SLWCR /LWCR  
 0074 4010 SCWCRP, SCWCR /CWCR  
 0075 4014 SLCARP, SLCAR /LCAR  
 0076 4020 SCCARP, SCCAR /CCAR  
 0077 4024 SLCMRP, SLCMR /LCMR  
 0100 4030 SLFGRP, SLFGR /LFGR  
 0101 4034 SLDBRP, SLDBR /LDBR  
 0102 4040 S6710P, S6710 /READ IOT,  
 0103 4044 SRWCRP, SRWCR /RWCR  
 0104 4050 SRCARP, SRCAR /RCAR  
 0105 4054 SRMSRP, SRMSR /RMSR  
 0106 4060 SRCMRP, SRCMR /RCMR  
 0107 4064 SRFSRP, SRFSR /RFSR  
 0110 4070 SRDBRP, SRDBR /RDBR  
 0111 4074 S6720P, S6720 /CONTROL IOT,  
 0112 4100 SSKEFP, SSKEF /SKEF  
 0113 4105 SSKCBP, SSKCB /SKCB  
 0114 4112 SSKTDP, SSKTD /SKTD  
 0115 4117 SSKTRP, SSKTR /SKTR  
 0116 4124 SCLFP, SCLF /CLF  
 0117 4130 SCLTP, SCLT /CLT  
 0120 4134 SSDLEP, SSDLE /SDLE  
 0121 4140 SSBMP, SSBRM /SBRM

/PSEUDO MNEMONIC SUBROUTINE POINTERS,

0122 6547 SRBSWP, SRBSW /BSW  
 0123 6400 CLR1P, CLR1 /CLEAR1  
 0124 6406 CLR2P, CLR2 /CLEAR2  
 0125 6414 CLR3P, CLR3 /CLEAR3  
 0126 6105 LAS5P, LAS5 /LOOPS  
 0127 6113 LAS6P, LAS6 /LOOP6  
 0130 6121 LAS7P, LAS7 /LOOP7  
 0131 6127 LAS8P, LAS8 /LOOP8  
 0132 6135 LAS9P, LAS9 /LOOP9  
 0133 5600 COMPP, COMP /COMPAR  
 0134 5000 ERRORP, ERRORS /ERROR  
 0135 6534 DATBRP, DATBRK /BREAK  
 0136 6525 CMDP, CMD /COMMAND  
 0137 6432 OFFLNP, OFFLINE /OFFLIN  
 0140 6436 REWNP, REWIND /REWIND  
 0141 6442 READDP, READD /READ  
 0142 6446 RDCMP, RDCMP /RDCOMP  
 0143 6452 WRITP, WRIT /WRITE

```

0144 6496 EOFF, EOFF
0145 6462 SPFORP, SPFORP
0146 6466 SPREV, SPREV
0147 6472 XGAPP, XGAPP
0150 6500 GOEP, GOE
0151 0565 SIOT1P, SIOT1
0152 0570 SIOT2P, SIOT2
0153 6541 WCOVER, WCOVER

/WIDE USAGE TEMPORARIES;
GOOD, 0
BAD, 0
OLD, 0
TXXTM1, 0
TXXTM2, 0
TXXTM3, 0
SAVEAC, 0
SAVEL, 0
INTEF, 0
PRGFLD, 0
INTMTF, 0
MEMFLD, 0
ACLOC, 0

/WIDE USAGE SOFTWARE FLAGS AND INDICATORS;
TRACE, 0
INTFLG, 0

TTOFLG, 0
EXITFL, 0
ACTFLG, 0
TSTAT, 0

TSTNUM, 0

/WIDE USAGE TEMPORARIES;
/GOOD DATA;
/BAD DATA;
/OLD DATA;
/THREE TEMPORARIES FOR
/FORMAL TEST USAGE ONLY;

/AC SAVED;
/LINK SAVED;
/IF INTERRUPT SERVICE ADDRESS OCCUPIES;
/MEMORY FIELD WHICH PROGRAM OCCUPIES;
/MTTF INTERRUPT SERVICE ADDRESS;
/NUMBER OF EXTENDED MEM FIELDS PRESENT;
/CONTENTS OF AC FOR ILLEGAL SKIP;

```

```

0171 0000
0172 0000

0173 0000
0174 0000
0175 0000
0176 0000

0177 0000

/THIS IS WHERE THE PROGRAM STARTS;
/*****
*200
START, JMP I EXECR /START HERE, GO TO EXEC,
0201 STL
0202 JMP I EXECR

```

/TEST SECTION,

/THE TEST SECTION IS COMPOSED OF 12 TESTS (T01-T14) EACH  
/OF WHICH IS MADE UP OF SUBTESTS DESIGNATED BY THE LETTERS A-Z, THEN 1-9.

/THE SUBTEST FORMAT IS DESCRIBED BELOW,

/SUBTEST FORMAT FOR NON-DATA TESTS:

```

/T01Z, INST /HOUSEKEEP TO SET UP LOOPS AND SPECIAL LOCATIONS
/ INST /SUCH AS "GOOD" AND "OLD",
/ RUN TEST
/ AT THE END OF THE TEST, AC CONTAINS SOME PREDETERMINED VALUE,
/ COMPARE /COMPARE THE VALUE IN AC WITH THE VALUE IN "GOOD",
/ IF AC="GOOD" CHECK FOR SUBTEST LOOP,
/ IF AC NOT EQUAL TO "GOOD" GO TO "ERRORS", TAKE
/ THE REQUIRED ACTION, THEN CHECK FOR SUBTEST LOOP,
/ ADDRESS OF ERROR STATUS WORDS,
ERR01Z /SUBTEST LOOP JUMP-REENTER HERE IF
JMP T01Z /SUBTEST LOOP REQUESTED,
/ ETC. /POINT OF CONTINUATION,

```

/THE SUBTEST FORMAT FOR DATA TESTS HAS ONE ADDITIONAL  
/INSTRUCTION FOLLOWING THE SUBTEST LOOP JUMP:

```

/ JMP T01Z /SUBTEST LOOP JUMP,
/ TAG /THIS INSTRUCTION RETURNS TO USE NEW DATA,
/ IF AN "EX-" COMMAND IS PENDING AND AN ERROR OCCURS, THE ERROR
/ ROUTINE ("ERRORS") WILL SKIP THIS INSTRUCTION THEREBY INHIBITING ANY
/ MORE DATA FROM BEING USED IN THIS SUBTEST, THIS ENABLES THE USER
/ TO ESCAPE FROM A FAILING DATA TEST AND PROCEED TO THE NEXT SUBTEST,

```

/TEST 01, 670X, 671X IOT EXISTENCE AND BASIC NON-STATUS REGISTERS /TEST.

/1, VERIFIES THAT 670X AND 671X IOT'S EXIST EXCEPT FOR /RMSR AND THAT WC, CA, CM, CB AND PR0=4 CAN BE /LOADED AND READ WITH ALL 1'S AND ALL 0'S, THE FACT /THAT EACH OF THESE IOT'S FUNCTION ONLY WITH THEIR /RELATED REGISTERS AND NOT WITH ANOTHER REGISTER IS /ALSO VERIFIED.

/2, TESTS T010-T01P ARE GENERALIZED AND ARE CONTROLLED BY /A LOCAL EXECUTIVE WHICH SELECTS THE IOT'S TO USE.

/3, LOOPS 6-9:  
/3.1 LOOP 6 CYCLES ON T01A-T01C (AC CLEARING)  
/3.2 LOOP 7 CYCLES ON T010-T01N (IOT EXISTENCE & BASIC REGISTER)  
/3.3 LOOP 8 CYCLES ON T010-T01P (IOT UNIQUENESS)

0203 0000 TEST01, 0 IAC  
0204 7001 DCA  
0205 3177 TSTNUM  
0206 6002 IOF INTFLG  
0207 3172 DCA GOOD  
0210 3154 DCA

/RUN WITH INTERRUPT SYSTEM OFF.

0211 7240 /VERIFY 6700 CLEARS AC,  
0212 4472 T01A, CLA CMA  
0213 4533 I6700  
0214 6705 COMPAR  
0215 5211 ER01A  
/IOT 6700 DID NOT CLEAR AC, GD=GOOD AC/ BD=REAL AC,  
T01A JMP

0216 7240 /VERIFY 6710 CLEARS AC,  
0217 4502 T01B, CLA CMA  
0220 4533 I6710  
0221 6707 COMPAR  
ER01B  
/IOT 6710 DID NOT CLEAR AC, GD=GOOD AC/ BD=REAL AC  
T01B JMP

0222 5216 /VERIFY 6720 DOES NOT CLEAR AC  
T01C, CLA CMA  
0223 7240 DCA GOOD  
0224 3154 CLA CMA  
0225 7240 I6720  
0226 4511 COMPAR  
0227 4533 ER01C  
0230 6711 JMP  
/IOT 6720 CLEARED AC, GD=GOOD AC, BD=REAL AC,  
T01C+2

0231 5225 LOOP6  
0232 4527 JMP  
0233 5210 T01A-1 /\*\*\*\*\*LOOP 6\*\*\*\*\*

```

0234 4775      JMS I      T1EX1P      /TO EXECUTIVE.
/VERIFY LOAD IOT LXXR CLEARS AC.
T01D,      DCA      GOOD
0235 3154      TAD      TXXTM1
0236 1157      LXXR
0237 4551      COMPAR
0240 4533      ER01D
0241 6713      /LOAD IOT LXXR DID NOT CLEAR AC.  GD=GOOD AC; BD=REAL AC;
/11=LXXR,      JMP      T01D+1      /SUBTEST LOOP.
0242 5236
/VERIFY THAT LXXR WITH AC=7777, THEN RXXR RESULTS IN A
/NON-ZERO AC.      TXXTM1
T01E,      LXXR
0243 1157      CLA
0244 4551      RXXR
0245 7200      AND
0246 4552      SNA CLA
0247 0157      CMA
0250 7650      COMPAR
0251 7040      ER01E
0252 4533      /LXXR WITH AC=7777, THEN RXXR DID NOT RESULT IN A NON-ZERO
0253 6715      /AC.  I1=LXXR; I2=RXXR
0254 5243      JMP      T01E      /SUBTEST LOOP
/VERIFY XX REGISTER TO BE LOADED AND READ ALL 4'S BY LXXR
/T01F,      CLA CMA      GOOD
0255 7240      DCA      TXXTM1
0256 3154      TAD
0257 1157      LXXR
0260 4551      CLA
0261 7200      RXXR
0262 4552      AND
0263 0157      TXXTM1
0264 1160      TXXTM2
0265 4533      COMPAR
0266 6717      ER01F
/XX REGISTER NOT LOADED AND/OR READ WITH ALL 1'S USING
/LXXR AND RXXR,  GD=GOOD AC; BD=REAL AC; I1=LXXR;
/I2=RXXR,      JMP      T01F+2      /SUBTEST LOOP
0267 5237
/VERIFY THAT LXXR WITH AC=0000, THEN RXXR RESULTS IN
/AT LEAST ONE BIT CLEAR IN AC.
T01G,      DCA      GOOD
0270 3154      LXXR
0271 4551      CLA
0272 7200      RXXR
0273 4552      AND
0274 0157      TXXTM1
0275 1160      TXXTM2
0276 7001      IAC CLA
0277 7650      /MASK BITS 0-4 FOR FS ONLY,
/ADD IN COMPLEMENT MASK,
/ADD 1 TO CHECK FLIP TO 0,

```



0300 7040  
0301 4533  
0302 6721  
  
0303 5271

CMA  
COMPAR  
ER01C  
/LXXR WITH AC=0000, THEN RXXR DID NOT RESULT IN AT  
/LEAST ONE BIT CLEAR IN AC; I1=LXXR; I2=RXXR;  
T01G+1  
/SUBTEST LOOP  
JMP

0304 4551  
0305 7200  
0306 4552  
0307 0157  
0310 4533  
0311 6723

/VERIFY XX REGISTER CAN BE LOADED AND READ ALL 0'S;  
T01H,  
LXXR  
CLA  
RXXR  
AND  
TXXTM1 /MASK BITS 2-4 FOR FS ONLY  
COMPAR  
ER01H  
/LXXR WITH AC=0000, THEN RXXR DID NOT RESULT IN A ZERO AC;  
/GD=GOOD AC; BD=REAL AC; I1=LXXR; I2=RXXR;  
T01H  
/SUBTEST LOOP  
JMP

0312 5304  
  
0313 1157  
0314 4551  
0315 4525  
0316 4552  
0317 0157  
0320 4533  
0321 6725

/VERIFY CLT CLEARS THE XX REGISTER WHICH IS PRELOADED TO 1'S  
/BY LXXR; LOBR WITH AC=0000 USED FOR DB,  
T01I,  
TAD  
LXXR  
CLEAR3  
RXXR  
AND  
TXXTM1 /MASK BITS 0-4 FOR FS ONLY  
COMPAR  
ER01I  
/XX REGISTER NOT CLEARED BY CLT (OR LOBR IF DB); GD=GOOD AC; BD=REAL AC;  
/I1=LXXR; I2=RXXR;  
T01I  
/SUBTEST LOOP  
JMP

0322 5313  
  
0323 4525  
0324 7240  
0325 4552  
0326 0157  
0327 4533  
0330 6727

/VERIFY RXXR CLEARS AC. REGISTER PRELOADED TO 0000;  
T01J,  
CLEAR3  
CLA CMA  
RXXR  
AND  
TXXTM1 /MASK BITS 0-4 FOR FS ONLY  
COMPAR  
ER01J  
/RXXR DOES NOT CLEAR AC. GD=GOOD AC; BD=REAL AC; I2=RXXR;  
T01J  
/SUBTEST LOOP  
JMP

0331 5323  
  
0332 1157  
0333 4551  
0334 7200  
0335 4776

TAD  
LXXR  
CLA  
JMS I T1EX2P  
/PRELOAD REGISTER TO ALL 1'S  
/FOR CXXR CHECKS  
/ONLY TEST REGISTERS FOR WHICH  
/CXXR APPLIES;

0336 7200  
0337 4551  
0340 4552  
0341 7001  
0342 7650

/VERIFY CXXR CLEARS AT LEAST ONE BIT IN THE REGISTER WHICH IS  
/PRELOADED TO ALL 1'S,  
T01K,  
CLA  
CXXR  
RXXR  
IAC  
SNA CLA

```

0343 7040      CMA
0344 4533      COMPAR
0345 6731      ER01K
/CXXR DID NOT CLEAR AT LEAST ONE BIT IN XX REGISTER
/I1=CXXR; I2=RXR      JMP      T01K+1      /SUBTEST LOOP

0346 5337

0347 7200      /VERIFY THAT CXXR CLEARS THE XX REGISTER
0350 4551      T01L,   CLA
0351 4552      CXXR
0352 4533      RXXR
0353 6733      COMPAR
ER01L
/CXXR DID NOT CLEAR XX REGISTER, GD=GOOD AC; BD=REAL AC;
/I1=CXXR; I2=RXR,    JMP      T01L+1      /SUBTEST LOOP

0354 5350

0355 7240      /VERIFY CXXR CLEARS AC.
0356 4551      T01M,   CLA CMA
0357 4533      CXXR
0360 6735      COMPAR
ER01M
/CXXR DOES NOT CLEAR AC. GD=GOOD AC; BD=REAL AC;
/I1=CXXR,    JMP      T01M      /SUBTEST LOOP

0361 5355

0362 7240      /VERIFY CXXR WITH AC=7777 DOES NOT SET ANY BIT IN XX
0363 4551      /REGISTER.
0364 4552      T01N,   CLA CMA
0365 4533      CXXR
0366 6737      RXXR
COMPAR
ER01N
/CXXR SET AT LEAST ONE BIT IN XX REGISTER, GD=GOOD AC;
/BD=REAL AC; I1=CXXR; I2=RXR,    JMP      T01N      /SUBTEST LOOP

0367 5362

0370 4777      JMS I   T1EX3P      /BACK TO EXECUTIVE;

0371 4530      T1LP7, LOOP7
0372 5232      JMP      T01D-3      /*****LOOP 7****

0373 5774      JMP I   ,*1      /GO TO T010 WHEN D=0N DONE
0374 0400      T010L
0375 0450      T1EX1P, T1EX1
0376 0473      T1EX2P, T1EX2
0377 0501      T1EX3P, T1EX3

0400      PAGE

0400 4305      T010L,   JMS      T1EX4      /TO EXECUTIVE TO INSERT
0401 4525      CLEAR3  /IOT'S.
0402 3154      DCA      GOOD

```

/VERIFY SPECIFIC LOAD, CLEAR, AND READ IOT'S AFFECT ONLY THEIR  
 /RELATED REGISTERS, AC SET TO 7777 PRIOR TO LOAD OR CLEAR  
 /WITH ALL OTHER REGISTERS CLEAR, IOT'S INSERTED BY EXECUTIVE,  
 T010, CLA CMA  
 LXXR  
 RXXR  
 AND TXXTM1  
 COMPAR  
 ER010

0403 7240  
 0404 4551  
 0405 4552  
 0406 0157  
 0407 4533  
 0410 6741

/IOT LXXR AFFECTS REGISTER READ BY RXXR, OR RXXR READS  
 /REGISTER LOADED BY LXXR, GD=500 AC; BD=REAL AC;  
 /I1=LXXR; I2=RXXR,  
 JMP T010 /SUBTEST LOOP

0411 5203

/VERIFY THAT SPECIFIC LOAD, CLEAR AND READ IOT'S AFFECT  
 /ONLY THEIR RELATED REGISTERS, AC SET TO 0000 PRIOR TO  
 /LOAD OR CLEAR WITH ALL OTHER REGISTERS SET TO 1/S,  
 /SAME METHOD AS T010,  
 CLEAR3  
 CLA CMA GOOD  
 DCA CMA  
 CLA CMA  
 LMC R  
 CLA CMA  
 LCA R  
 CLA CMA  
 LCM R  
 CLA CMA  
 LDB R  
 TAD K7600  
 LFC R  
 CLA  
 LXXR  
 RXXR  
 AND  
 TAD  
 COMPAR  
 ER01P

0412 4525  
 0413 7240  
 0414 3154  
 0415 7240  
 0416 4473  
 0417 7240  
 0420 4475  
 0421 7240  
 0422 4477  
 0423 7240  
 0424 4501  
 0425 1374  
 0426 4500  
 0427 7200  
 0430 4551  
 0431 4552  
 0432 0157  
 0433 1160  
 0434 4533  
 0435 6743

T01P,  
 /LOAD ONE REGISTER  
 /READ ANOTHER  
 /MASK BITS 0=4 FOR FS ONLY,  
 /ADD COMPLEMENT MASK,  
 TXXTM1  
 TXXTM2

/IOT LXXR AFFECTS REGISTER READ BY RXXR, OR RXXR READS  
 /REGISTER LOADED BY LXXR, GD=500 AC; BD=REAL AC;  
 /I1=LXXR; I2=RXXR,  
 JMP T01P+1 /SUBTEST LOOP

0436 5230

JMS T1EX5 /TO EXECUTIVE

0437 4340

LOOP8  
 JMP I T1LP7P /\*\*\*\*\*LOOP 8\*\*\*\*

0440 4531  
 0441 5773

CLEAR1  
 TAD I  
 DCA  
 JMP I  
 0  
 TEST01  
 /CLEAR REGISTERS = ION;  
 /EXIT,

0442 4523  
 0443 1647  
 0444 3246  
 0445 5646  
 0446 0000  
 0447 0203

/TEST 01 LOCAL EXECUTIVE TO SERVICE TESTS T01D=T01N,

T1EX1, 0 TAD T1LS1P  
 DCA 10  
 TAD T1LS2P  
 DCA 11  
 TAD T1LS3P  
 DCA 12  
 TAD M5  
 DCA T1EXT1  
 TAD I 10  
 DCA IOT1  
 TAD I 12  
 DCA IOT2  
 TAD I 12  
 DCA TXXTM1  
 TAD TXXTM1  
 CMA  
 DCA TXXTM2  
 JMP I T1EX1  
 TAD I 11  
 SNA  
 JMP .4  
 DCA IOT1  
 JMP I T1EX2  
 0  
 ISE T1EXT1  
 JMP T1EX1A  
 JMP I T1EX3

0450 0000  
 0451 1356  
 0452 3010  
 0453 1357  
 0454 3011  
 0455 1360  
 0456 3012  
 0457 1041  
 0460 3361  
 0461 1410  
 0462 3366  
 0463 1412  
 0464 3371  
 0465 1412  
 0466 3157  
 0467 1157  
 0470 7040  
 0471 3160  
 0472 3650  
 0473 0000  
 0474 1411  
 0475 7450  
 0476 5302  
 0477 3366  
 0500 5673  
 0501 0000  
 0502 2361  
 0503 5261  
 0504 5701

/ENTER PRIOR TO T01D;  
 /SET UP LIST POINTERS

/SET IOT1 FOR LXXR;

/SET IOT2 FOR RXXR;

/SET MASK AND COMPLEMENT  
 /MASK;

/GO BACK AND RUN T01D=T01J,  
 /ENTER AFTER T01J,  
 /SEE IF CXXR APPLIES;

/CXXR APPLIES, PUT IN IOT1,  
 /GO BACK AND RUN T01K=T01N,  
 /ENTER AFTER T01N,  
 /ALL IOT'S CHECKED?  
 /NO, RESET IOT'S  
 /YES, EXIT TO END OF T01N;

/TEST 01 LOCAL EXECUTIVE TO SERVICE TESTS T010=T01P,

T1EX4, 0 TAD T1LS1P  
 DCA 10  
 TAD T1LS2P  
 DCA 11  
 TAD T1LS3P  
 DCA T1EXT1  
 TAD M5  
 DCA T1EXT2

0505 0000  
 0506 1356  
 0507 3010  
 0510 1357  
 0511 3011  
 0512 1360  
 0513 3361  
 0514 1041  
 0515 3362

/SET LIST POINTERS;

/SET FOR 5 LOAD, CLEAR COMBINATIONS,

/GET LOAD,  
 /PUT IN IOT1,  
 /SET FOR PAIR COUNTER,

/UPDATE READ POINTER;

/GET HEAD POINTER AND  
 /STORE IN AUTO-INDEX,  
 /SET FOR 4 READS,

T1EX7, TAD I 10  
 DCA IOT1  
 TAD M2  
 DCA T1EXT4  
 ISE T1EXT1  
 ISE T1EXT1  
 TAD T1EXT1  
 DCA 12  
 TAD M4

0516 1410  
 0517 3366  
 0520 1037  
 0521 3364  
 0522 2361  
 0523 2361  
 0524 1361  
 0525 3012  
 26 1040

```

0527 3363          DCA      T1EXT3
0530 1412          TAD I    12
0531 3371          DCA      IOT2
0532 1412          TAD I    12
0533 3157          DCA      TXXTM1
0534 1157          TAD      TXXTM1
0535 7040          CMA
0536 3160          DCA      TXXTM2
0537 5705          JMP I    T1EX4
0540 0000          T1EX5, 0
0541 2363          ISZ     T1EXT3
0542 5330          JMP     T1EX6
0543 2364          ISZ     T1EXT4
0544 7410          SKP     T1EXT4
0545 5353          JMP     .+6
0546 1411          TAD I    11
0547 7450          SNA
0550 5353          JMP     .+3
0551 3366          DCA      IOT1
0552 5324          JMP     T1EX6
0553 2362          ISZ     T1EXT2
0554 5316          JMP     T1EX7
0555 5740          JMP I    T1EX5
0556 6612          T1LS1P, T1LS1-1
0557 6617          T1LS2P, T1LS2-1
0560 6624          T1LS3P, T1LS3-1
0561 0000          T1EXT1, 0
0562 0000          T1EXT2, 0
0563 0000          T1EXT3, 0
0564 0000          T1EXT4, 0
0565 0000          SIOT1, 0
0566 0000          IOT1, 0
0567 5765          JMP I    SIOT1
0570 0000          SIOT2, 0
0571 0000          IOT2, 0
0572 5770          T1LP7P, T1LP7
0573 0371          K7600, 7600
0574 7600

```

```

/GET READ,
/PUT IN IOT2,
/GET MASK, SAVE MASK,
/AND COMPLEMENT MASK,

```

```

/RUN TEST,

```

```

/ENTER AFTER TEST, 4 READS?
/NO, DO NEXT READ,
/YES, DONE WITH THIS LOAD, CLEAR PAIR?

```

```

/YES, GET CLEAR,
/NO, GET CLEAR,

```

```

/CLEAR APPLIES, RUN TEST AFTER
/SETTING UP READS,
/ALL DONE?
/NO, RUN NEXT PAIR,
/YES, EXIT,

```

```

/IOT1 SUBROUTINE,

```

```

/IOT2 SUBROUTINE,

```

/TEST 02. NON-STATUS REGISTER DATA TEST.

/1. VERIFIES THAT WC, CA, CM, DB AND FR0-4 CAN BE LOADED  
/AND READ USING INCREMENTED COMPLEMENT DATA.

/2. ALL SUBTEST LOOPS CYCLE ON CONSTANT DATA.

/3. LOOPS 6-9:

/3.1 LOOP 6: T02A(WC)  
/3.2 LOOP 7: T02B(CA)  
/3.3 LOOP 8: T02C(CM)  
/3.4 LOOP 9: T02D=T02E(DB,FR0-4)

0600	PAGE	
0600	TEST02, 0	
0601	CLEAR1	/CLEAR ALL-ION;
0602		/VERIFY WC CAN BE LOADED AND READ USING INCREMENTAL
0603	4307	/COMPLEMENTED PATTERN;
0604	7777	T02A, JMS T02LDS
0605	5215	JMP 7777
0606	1157	JMP T02B-2
0607	4473	TAD TXXTM1
0610	7200	LWCR
0611	4503	CLA
0612	4533	RWCR
0613	6745	COMPAR
0614		ER02A
0615		/WC NOT LOADED OR READ CORRECTLY, GD=GOOD WC; OD=PREVIOUS
0616	5205	/GOOD WC, SUBTEST LOOP CYCLES ON PRESENT DATA;
	5331	JMP 06
		JMP T02LDR
		LOOP6
	4527	JMP T02A
	5202	*****LOOP 6*****
0617	4307	
0620	7777	/VERIFY CA CAN BE LOADED AND READ USING INCREMENTAL
0621	5232	/COMPLEMENTED DATA;
0622	1157	T02B, JMS T02LDS
0623	4475	JMP 7777
0624	7200	JMP T02C-2
0625	4504	TAD TXXTM1
0626	4533	LVAR
0627	6747	CLA
		RVAR
		COMPAR
		ER02B
		/CA NOT LOADED OR READ CORRECTLY, GD=GOOD CA; OD=PREVIOUS
0630	5222	/GOOD CA, SUBTEST LOOP CYCLES ON PRESENT DATA;
0631	5331	JMP 06
		JMP T02LDR
		/CLEAR AC TO ENSURE GOOD TEST,

```

0632 4530 LOOP7
0633 5215 JMP T02B-2 /*****LOOP 7*****/

0634 3172 /VERIFY CM CAN BE LOADED AND READ USING INCREMENTAL
0635 6202 /COMPLEMENTED DATA.
0636 4307 T02C, DCA INTFLG /ALLOW NO INTERRUPTS.
0637 7777 IOF T02LDS
0640 5251 JMS 7777
0641 1157 JMP T02D-3
0642 4477 TAD TXXTM1
0643 7200 LCMR
0644 4506 CLA
0645 4533 RCMR
0646 6751 COMPAR
ER02C /CLEAR AC TO INSURE GOOD TEST,

0647 5241 /CM NOT LOADED OR READ CORRECTLY, GD=GOOD CH; OD=PREVIOUS
0650 5331 /GOOD CM, SUBTEST LOOP CYCLES ON PRESENT DATA,
JMP :=6 /SUBTEST LOOP
JMP T02LDR

0651 4523 CLEAR1 /CLEAR ALL = 10N.
0652 4531 LOOP8
0653 5232 JMP T02C-2 /*****LOOP 8*****/

0654 4524 /VERIFY DB CAN BE LOADED AND READ USING INCREMENTAL
0655 4307 /COMPLEMENTED DATA.
0656 7777 T02D, CLEAR2 T02LDS
0657 5270 JMS 7777
0660 1157 JMP T02E
0661 4501 TAD TXXTM1
0662 7200 LDBR
0663 4510 CLA
0664 4533 RDBR
0665 6753 COMPAR
ER02D /CLEAR AC TO ENSURE GOOD TEST,

0666 5260 /DB NOT LOADED OR READ CORRECTLY, GD=GOOD DB; OD=PREVIOUS
0667 5331 /GOOD DB, SUBTEST LOOP CYCLES ON PRESENT DATA,
JMP :=6 /SUBTEST LOOP
JMP T02LDR

0670 4307 /VERIFY FS 0=4 CAN BE LOADED AND READ USING INCREMENTAL
0671 7600 /COMPLEMENTED DATA.
0672 5304 T02E, T02LDS
0673 1157 JMS 7600 /MASK FOR BITS 0=4
0674 4500 JMP T02LP9
0675 7200 TAD TXXTM1
0676 4507 LFGR
0677 0271 CLA
0700 4533 RFSR
0701 6755 AND T02E+1 /MASK
COMPAR
ER02E /FS 0=4 NOT LOADED OR READ CORRECTLY, GD=GOOD FS 0=4;

```

```

0702 5273 /BD=REAL FS 0=4 (MASKED); OD=PREVIOUS GOOD FS 0=4,
0703 5331 JMP .7
      JMP T02LDR

```

```

0704 4532 T02LP9, LOOP9
0705 5252 JMP T02D-2 /*****LOOP 9****
0706 5600 JMP I TEST02 /EXIT,

```

/DATA SUPPLIER FOR WC, CA, CM, DB, FR.  
 /STARTING FROM 0 OUTPUTS THE FOLLOWING INCREMENTAL COMPLEMENTED  
 /DATA PATTERN.

/0: 7777,7777,0,0,7777,7777,0,0  
 /1: 7776,7776,1,1,7776,7776,1,1, ETC.

/THE CALLING SEQUENCE IS:  
 /JMS T02LDS  
 /DATA MASK CONSTANT  
 /EXIT  
 /TEST INSTRUCTIONS  
 /ERROR CHECK  
 /JMP T02LDR TO GET NEW DATA.

```

0707 0000 T02LDS, 0
0710 3157 DCA TXXTM1
0711 1021 TAD K2
0712 1307 T02LDS
0713 1360 DCA TXXTM2
0714 1040 TAD M4
0715 3161 DCA TXXTM3
0716 1037 TAD M2
0717 3345 DCA T02LDM
0720 1154 TAD OLD
0721 3156 DCA TXXTM1
0722 1157 TAD CMA
0723 7040 DCA AND I
0724 3157 DCA DCA
0725 1157 TAD AND I
0726 0707 DCA DCA
0727 3154 DCA DCA
0730 5560 JMP I
0731 2345 T02LDR, ISZ
0732 7410 SKP
0733 5337 JMP
0734 1154 TAD
0735 3156 DCA
0736 5560 JMP I
0737 2161 ISZ
0740 5316 JMP
0741 2157 ISZ
0742 5314 JMP
0743 2307 ISZ
0744 5707 JMP I
0745 0000 T02LDM, 0

```

/ENTER,  
 /0 FOR A STARTER,  
 /COMPUTE RETURN EXIT  
 /AND SAVE,  
 /SET FOR FOUR COMPLEMENTS,  
 /SET FOR TWO SENDS OF ONE PATTERN,  
 /PUT PREVIOUS DATA  
 /IN OLD,  
 /SET LAST DATA,  
 /COMPLEMENT,  
 /SAVE,  
 /GET LAST DATA,  
 /MASK,  
 /THIS IS WHAT WE SHOULD READ,  
 /RUN THE TEST,  
 /THIS PATTERN SENT 2 TIMES,  
 /YES, PUT OLD DATA IN OLD,  
 /RUN PATTERN AGAIN;  
 /COMPLEMENTING DONE 4 TIMES?  
 /NO, SET UP NEXT DATA;  
 /YES, INCREMENT DATA;  
 /SEND OUT NEXT DATA TRAIN;  
 /ALL DONE, UPDATE RETURN;  
 /EXIT,



```

/TEST 03. BASIC FUNCTIONS EXISTENCE TEST,
/LOCAL EXECUTIVE,
/1. REFER TO SUBTESTS FOR SPECIFIC TEST INFORMATION,
/2. TESTS T03D-T03I ARE GENERALIZED AND ARE CONTROLLED BY A
/3. LOOPS 6-9:
/3.1 LOOP 6 CYCLES ON T03A-T03C,
/3.2 LOOP 7 CYCLES ON T03D-T03E,
/3.3 LOOP 8 CYCLES ON T03F-T03G,
/3.4 LOOP 9 CYCLES ON T03H-T03I,
/4. EACH SUBTEST LOOP CYCLES ON THE FUNCTION IN CURRENT USE,

```

```

1000 PAGE
1001 TEST03, 0 CLEAR1 /CLEAR ALL REGISTERS=ION
1002 TAD XBUFFP
1003 DCA OLD

```

```

1004 DCA GOOD
1005 CLEAR2 /CLEAR REGISTERS
1006 LWCR /0 TO WC
1007 WRITE
1008 GO
1009 RWCR
1010 COMPAR
1011 ER03A
1012
1013

```

```

1014 /VERIFY WRITE-GO DOES NOT CAUSE A DATA BREAK WITH NO
/TRANSPORT READY, WC STARTS AT 0000 AND SHOULD
/REMAIN 0000.
T03A,
T03A+1 /SUBTEST LOOP,
5205

```

```

1015 /VERIFY WRITE CAUSES TRANSFER OUT OF MEMORY, CA REFERENCES
1016 /ADDRESSES XBUFF OR XBUFF+1, CM6=8 REFERENCE THE PROGRAM
1017 /FIELD, AND THAT DB CAN RECEIVE ALL 1'S. (XBUFF AND
1018 /XBUFF+1 CONTAIN 7777,)
1019 T03B, CLA CMA
1020 DCA GOOD
1021 JMS I T3LOOP /LOAD UP REGISTERS,
1022 LDBR /0 TO DB
1023 WRITE /1 DATA BREAK
1024 BREAK
1025 R0BR
1026 COMPAR
1027 ER03B

```

```

1028 /GD=GOOD DBI 0D=ADDRESS XBUFF (CA SHOULD EQUAL THIS
1029 /QUANTITY OR ONE MORE=CA INCREMENT IS NOT BEING
1030 /TESTED,)
1031 JMP T03B+2 /SUBTEST LOOP,
1032
1033 /VERIFY READ CAUSES TRANSFER INTO MEMORY, CA REFERENCES
1034 /ADDRESSES XBUFF OR XBUFF+1, AND THAT DB CAN SEND ALL
1035 /0'S. (XBUFF AND XBUFF+1 CONTAIN 7777 PRIOR TO

```

```

1027 4404 /BREAK,.)
1030 4501 T03C, JMS I T3LOOP /LOAD UP REGISTERS
1031 4541 LDBR /0 TO DB
1032 4535 READ /1 DATA BREAK
1033 1050 BREAK /SUM
1034 1051 TAD XBUFF XBUFF+1
1035 4533 TAD XBUFF+1
1036 6763 COMPAR
ER03C
/0D=GOOD SUM OF THE CONTENTS OF XBUFF AND XBUFF+1 (ONE OF
/1 THE VALUES SHOULD BE 7777, THE OTHER 0000 YIELDING A
/SUM OF 7777); 0D=ACTUAL SUM; 0D=ADDRESS XBUFF
/1 CA SHOULD EQUAL THIS QUANTITY OR 1 MORE = CA INCREMENT
/NOI BEING TESTED.)
1037 5227 JMP T03C /BSUBTEST LOOP

1040 4527 LOOP6
1041 5204 JMP T03A /*****LOOP6****

1042 3154 /TO EXECUTIVE TO INSERT FUNCTION,
1043 4776 JMS I T3EX1P /FUNCTION LIST POINTER
1044 6647 T3LS1 /NUMBER OF FUNCTIONS TO USE,
1045 7771 -7 /WHERE TO PUT FUNCTIONS;
1046 1051 +3 /LOAD ALL REGISTERS,
1047 4404 JMS I T3LOOP /0 TO DB,
1050 4501 LDBR /VARIABLE FUNCTION HERE,
1051 0000 0 /1 DATA BREAK,
1052 4535 BREAK /XBUFF AND XBUFF+1 SHOULD
1053 1050 TAD /BE EQUAL AND UNCHANGED,
1054 7041 CIA
1055 1051 TAD XBUFF+1
1056 4533 COMPAR
1057 6765 ER03D

1060 5777 /0D=GOOD DIFFERENCE OF THE CONTENTS OF XBUFF+1=XBUFF;
1061 4775 JMS I T3EX2P /BD=ACTUAL DIFFERENCE; 0D=ADDRESS XBUFF; FS=FUNCTION
/WHICH FAILED, SUBTEST LOOP CYCLES ON CURRENT FUNCTION;
/0 TO DB,
/BACK FOR ANOTHER FUNCTION,

1062 4776 /VERIFY THAT OFFLINE, REWIND, WEOF, SPCFWD, SPCREV AND READ
1063 6651 /DO NOT CAUSE TRANSFER FROM PROCESSOR MEMORY TO DB;
1064 7772 /XBUFF AND XBUFF+1 CONTAIN 7777 PRIOR TO BREAK, DB CONTAINS 0000,
1065 1070 T03E, JMS I T3EX1P /TO EXECUTIVE TO GET FUNCTION,
1066 4404 T3LS1+2 /FUNCTION LIST POINTER,
-6 /# OF FUNCTIONS
+3 /WHERE TO INSERT FUNCTION
JMS I T3LOOP /LOAD REGISTERS

1067 4501 LDBR /0 TO DB
1070 0000 0 /VARIABLE FUNCTION
4535 BREAK /1 DATA

```

1072 4510 RBR /DB SHOULD STILL BE 0000,  
 1073 4533 COMPAR  
 1074 6767 ER03E

1075 5777 /GD=GOOD DB; OD=ADDRESS XBUFF; FS=FUNCTION WHICH FAILED  
 1076 4775 JMP I T3EX32 /SUBTEST LOOP  
 JMS I T3EX2P /GET ANOTHER FUNCTION

1077 4530 LOOP7  
 1100 5240 JMP T030-2 /\*\*\*\*\*LOOP7\*\*\*\*\*

1101 7201 /VERIFY SPCFWD, SPCREV, READ, RDCOMP AND WRITE CAUSE  
 1102 3154 /WC INCREMENT FROM 0000 TO 0001.  
 1103 4776 T03F, CLA IAC

1104 6654 DCA GOOD /SET VARIABLE FUNCTION;  
 1105 7773 JMS I T3EX1P  
 T3LS1+5

1106 1111 -5  
 1107 4404 +3  
 1110 4473 JMS I T3LOOP /LOAD REGISTERS  
 1111 0000 LWCR /0 TO WC  
 1112 4535 BREAK /FUNCTION INSERTED  
 1113 4503 RWCR / 1 DATA BREAK  
 1114 4533 COMPAR  
 1115 6771 ER03F

1116 5777 /GD=GOOD WC; FS=FUNCTION WHICH FAILED,  
 1117 4775 JMP I T3EX32 /SUBTEST LOOP  
 JMS I T3EX2P /GET ANOTHER FUNCTION

1120 7201 /VERIFY READ, RDCOMP, WRITE CAUSE CA INCREMENT FROM  
 1121 1047 /XBUFF TO XBUFF+1  
 1122 3154 T03G, CLA IAC

1123 4776 TAD XBUFFP  
 1124 6646 DCA GOOD /GET A FUNCTION.  
 1125 7775 JMS I T3EX1P  
 T3LS1-1

1126 1130 -3  
 1127 4404 +2  
 1130 0000 JMS I T3LOOP /LOAD REGISTERS  
 1131 4535 BREAK /FUNCTION INSERTED HERE,  
 1132 4504 RCAR / 1 DATA BREAK,  
 1133 4533 COMPAR  
 1134 6773 ER03G

1135 5777 /GD=GOOD CA; FS=FAILING FUNCTION,  
 1136 4775 JMP I T3EX32 /SUBTEST LOOP  
 JMS I T3EX2P /GET ANOTHER FUNCTION

1137 4531 LOOP8  
 1140 5277 JMP T03F-2 /\*\*\*\*\*LOOP 8\*\*\*\*

1141 3154 /VERIFY OFFLINE, REWIND, WEOF DO NOT CAUSE WC INCREMENT,  
 1142 4776 T03H, DCA GOOD /WC PRESET TO 0000.  
 JMS I T3EX1P /GET FUNCTION

```

1143 6651 T3LS1+2
1144 7775 -3
1145 1147 .#2
1146 4473 LWCR
1147 0000 0
1150 4535 BREAK
1151 4503 RWCR
1152 4533 COMPAR
1153 6775 ER03H
/GO=GOOD WC; FS=FAILING FUNCTION /SUBTEST LOOP;
JMP I T3EX32 /GET ANOTHER FUNCTION;
JMS I T3EX2P

1156 4776 /VERIFY OFFLINE, REWIND, WE0F; SPCFWD; SPCREV DO NOT
1157 6651 /CAUSE CA INCREMENT, CA PRESET TO 0000,
1160 7773 T031,
1161 1163 JMS I T3EX1P
1162 4475 T3LS1+2
1163 0000 -5
1164 4535 .#2
1165 4504 LCAR
1166 4533 0
1167 6777 BREAK
/GO=GOOD CA; FS=FAILING FUNCTION /SUBTEST LOOP
JMP I T3EX32 /GET ANOTHER FUNCTION
JMS I T3EX2P

1172 4532 LOOP9
1173 5337 JMP T03H-2
1174 5600 JMP I TEST03

1175 1231 T3EX2P, T3EX2
1176 1213 T3EX1P, T3EX1
1177 1227 T3EX32, T3EX3+2
1200 1200 PAGE

1200 0000 T3LOAD, 0
1201 4524 CLEAR2
1202 7240 CLA CMA
1203 3050 DCA CMA
1204 7240 CLA CMA
1205 3051 DCA
1206 1047 TAD
1207 4475 LCAR
1210 1165 TAD
1211 4477 LCMR
1212 5600 JMP I T3LOAD

1213 0000 T3EX1,
1214 1613 TAD I
1215 3010 DCA
1216 2213 IS#

```

/LOAD XBUFF AND XBUFF+1  
/WITH 7777, AND LCAR WITH  
/XBUFF ADDRESS;  
/TEST 03 FUNCTION SELECTOR EXECUTIVE,  
/GET CALL #1 \* THIS IS STARTING  
/FUNCTION IN T3LS1;  
/GET CALL #2 \* THIS IS THE NUMBER

1217	1613	TAD I	T3EX1	/OF FUNCTIONS TO BE USED,
1220	3157	DCA	TXXTM1	
1221	2213	ISZ	T3EX1	/UPDATE CALL POINTER TO LOCATION IN
1222	1613	TAD I	T3EX1	
1223	3160	DCA	TXXTM2	
1224	2213	ISZ	T3EX1	
1225	1410	TAD I	10	/WHICH FUNCTION TO BE INSERTED AND
1226	3560	DCA I	TXXTM2	/POINT OF EXIT,
1227	4524	CLEAR2		
1230	5613	JMP I	T3EX1	/RUN TEST WITH THIS FUNCTION,
1231	0000	Ø		/ENTER AT END OF SUBTEST,
1232	2157	ISZ	TXXTM1	/ALL FUNCTIONS USED?
1233	5225	JMP	T3EX3	/NO, GET NEXT FUNCTION,
1234	5931	JMP I	T3EX2	/YES, EXIT TO END OF THIS SUBTEST
				/PLUS 1 INSTRUCTION,

/TEST 04, FULL WORD COUNT AND CURRENT ADDRESS INCREMENT TEST  
/INCLUDING CM6-8,

/1. REFER TO SUBTEST FOR SPECIFIC TEST INFORMATION,  
/2. LOOPS 6-7:  
/2.1 LOOP 6 CYCLES ON T04A, (WC INCREMENT)  
/2.2 LOOP 7 CYCLES ON T04B, CD, AND E (CA AND EMA TESTS),  
/3. THE SUBTEST LOOPS CYCLE ON THE PRESENT OLD VALUE  
/BEING INCREMENTED TO THE GOOD VALUE.

1235 0000 TEST04, 0  
1236 4523 CLEAR1

/VERIFY COMPLETE WC INCREMENT USING SPCFWD FUNCTION,  
/START WITH WC=0 AND INCREMENT A FULL COUNT FOUR  
/TIMES DISREGARDING WC OVERFLOW, /SPACE FORWARD;  
T04A, SPCFWD M4

1237 4545 TAD DCA TXXTM1  
1240 1040 DCA GOOD  
1241 3157 DCA GOOD  
1242 3154 DCA GOOD  
1243 1154 TAD OLD  
1244 3156 DCA OLD  
1245 2154 ISZ GOOD  
1246 5252 JMP T04ASL  
1247 2157 ISZ TXXTM1  
1250 7410 SKP  
1251 5262 JMP T4LP6  
1252 1156 TAD OLD  
1253 4473 LWCR  
1254 4535 BREAK  
1255 4503 RWCR  
1256 4533 COMPAR  
1257 7001 ER04A

T04AL, DCA OLD /GOOD TO OLD,  
DCA OLD /+1 TO GOOD,  
T04ASL, TAD OLD /YES,  
LWCR /LOAD WC WITH OLD,  
BREAK /1 BREAK,  
RWCR /CHECK  
COMPAR

/WC INCREMENT FAILURE, GD=GOOD WCJ OD=PREVIOUS GOOD WCJ  
/WC=REAL WC, THE SUBTEST LOOP CYCLES ON THE CURRENT PREVIOUS  
/GOOD WC (OD) BEING INCREMENTED TO THE GOOD WC VALUE,  
JMP T04ASL  
JMP T04AL

1260 5252  
1261 5243  
1262 4527 T4LP6, LOOP6  
1263 5237 JMP T04A /\*\*\*\*\*LOOP 6\*\*\*\*

/VERIFY THAT "GO" BIT CAN BE SET WITH MITF CLEAR;  
T04B, TAD K100  
DCA GOOD

1264 1027 CLEAR2  
1265 3154 WRITE  
1266 4524 GO  
1267 4543 RFSR K100  
1270 4550 AND  
1271 4507 COMPAR  
1272 0027 ER04B  
1273 4533  
1274 7003

/GO BIT NOT SET, GD=GOOD GO BIT; BD=REAL BIT FROM FS,

1275 5264

JMP

T04B

/SUBTEST LOOP,

/VERIFY COMPLETE CA AND EMA INCREMENT, T04C PERTAINS  
/TO CA TEST, T04D TO EMA TEST. START WITH  
/CA=0, EMA=0. CA WILL OVERFLOW, EMA WILL COUNT TO  
/7 THEN FREEZE. CA SHOULD CONTINUE TO WRAP AROUND,  
T04CD, TAD M20 /20 PASSES.

1276 1043  
1277 3157  
1300 3160  
1301 3376  
1302 1160  
1303 3161  
1304 1376  
1305 3377  
1306 2160  
1307 5324  
1310 2157  
1311 7410  
1312 5373  
1313 1376  
1314 1023  
1315 0026  
1316 3376  
1317 1376  
1320 7640  
1321 5324  
1322 1026  
1323 3376  
1324 1160  
1325 3154  
1326 4517  
1327 1161  
1330 3156  
1331 1161  
1332 4475  
1333 1377  
1334 4477  
1335 4456  
1336 4140  
1337 4535  
1340 4504  
1341 4533  
1342 7005

T04CL,

DCA TXXTM1  
DCA TXXTM2  
DCA T04T1  
TAD TXXTM2  
DCA TXXTM3  
TAD T04T1  
DCA T04T2  
ISZ TXXTM2  
JMP T04CSL  
ISZ TXXTM1

T4LP7  
T04T1  
K10  
K70  
DCA T04T1  
TAD T04T1

SZA CLA

JMP I+3  
TAD K70  
DCA T04T1  
TAD TXXTM2  
DCA GOOD  
CLT TXXTM3  
TAD OLD  
TAD TXXTM3

LQMR

JMS I  
4140  
BREAK  
RCAR  
COMPAR  
ER04C

T04CSL,

DCA TXXTM2  
DCA GOOD  
CLT TXXTM3  
TAD OLD  
TAD TXXTM3  
LCAR T04T2  
TAD FUNC

/CLEAR ALL;  
/SET UP OLD.  
/LOAD CA WITH OLD VALUE.  
/LOAD EMA WITH OLD VALUE.  
/WRITE-GO WITH EMA INC ENABLE,  
/A BREAK  
/CHECK CA INCREMENT

/CA INCREMENT FAILURE, CA DID NOT INCREMENT FROM  
/OLD TO GOOD VALUE. GD=GOOD CAJ OD=OLD CAJ CA=REAL CA;  
/SUBTEST LOOP CYCLES ON OLD BEING INCREMENTED TO GOOD;

1343 7000  
1344 7410  
1345 5357  
1346 1376  
1347 3154  
1350 1377  
1351 3156  
1352 4506

NOB  
SKP  
JMP  
TAD  
DCA  
TAD  
DCA  
RCMR

/PUT GOOD EMA IN GOOD,  
/OLD EMA IN OLD,  
/CHECK EMA (CM6-8)

```

1353 4533 COMPAR
1354 7007 ER04D
/EMA INCREMENT OR FREEZE FAILURE, EMA (CM6=0) DID
/NOT INCREMENT FROM OLD TO GOOD VALUE OR DID
/INCREMENT OR CHANGE WHEN IT SHOULD NOT HAVE,
/GD=GOOD EMA; OD=OLD EMA; CM=REAL EMA IN
/BITS 6-8, THE SUBTEST LOOP CYCLES ON CURRENT DATA,
JMP T04CSL /SUBTEST LOOP,
JMP T04CL
1355 5324
1356 5302

```

```

1357 4524 /VERIFY THAT EMA DOES NOT INCREMENT WHEN EMA INC
1360 3154 /DISABLED; CA SET TO 7777, EMA TO 0, THEN CAUSE CA
1361 7240 /INCREMENT AND EMA SHOULD REMAIN 0,
T04E, CLEAR2 /CLEAR ALL,
DCA CMA GOOD /7777 TO CA,
LCAR /0 TO EMA
LCMR /WRITE=GO, EMA INC
JMS I FUNCPC /DISABLED,
4100 /1 BREAK,
BREAK /CHECK EMA=0,
RCMR
COMPAR
ER04E
1370 4533
1371 7011

```

```

/EMA CHANGED WHEN EMA INC DISABLED, GO=GOOD EMA;
/CM=REAL EMA IN BITS 6-8,
JMP T04E /SUBTEST LOOP,
1372 5357
1373 4530 T4LP7, LOOP7
1374 5262 JMP T4LP6
1375 5635 JMP I TEST04
1376 0000 T04T1, 0
1377 0000 T04T2, 0
/*****LOOP 7****
/EXIT

```



```

/TEST 05. MEMORY=DATA BUFFER DATA TEST.
/1. VERIFIES THAT DB CAN RECEIVE DATA FROM MEMORY AND
/CAN TRANSFER DATA TO MEMORY.
/2. ALL SUBTEST LOOPS CYCLE ON CONSTANT DATA,
/3. LOOPS 6-7:
/3.1 LOOP 6 CYCLES ON T05A (MEM TO DB=WRITE)
/3.2 LOOP 7 CYCLES ON T05B (DB TO MEM=READ)
/4. LOCATION XBUFF IS LOADED INTO CA
/WHICH CAUSES XBUFF+1 TO BE USED FOR TRANSFERS;
    
```

```

PAGE
TEST05, 0 CLEAR1
1400
0000
1401 4523

1402 4543
1403 4650
1404 7777
1405 5222
1406 1047
1407 4475
1410 1157
1411 3051
1412 1165
1413 4477
1414 4535
1415 4510
1416 4533
1417 7013

1420 5206
1421 5651

1422 4527
1423 5202

1424 4650
1425 7777
1426 5245
1427 4524
1430 1047
1431 4475

/VERIFY PROPER DATA TRANSFERS FROM XBUFF+1 IN MEMORY TO
/DB USING WRITE FUNCTION, CA IS SET TO XBUFF AND
/SHOULD INCREMENT TO XBUFF+1 BEFORE TRANSFER, COMPLEMENT
/INCREMENTING DATA IS USED,
T05A, WRITE /SET WRITE FUNCTION,
JMS I T05LDS /GET DATA,
7777
JMP T5LP6 /DONE EXIT,
T05ASL, TAD XBUFFP /LOAD CA=XBUFF,
LCAR /LOAD XBUFF+1 WITH DATA WORD,
TAD /LOAD CM WITH PROPER MEMORY
DCA XBUFF+1 /FIELD,
TAD /1 DATA BREAK,
LCMR /READ AND CHECK DB;
BREAK
RBR
COMPAR
ER05A
/GD GOOD DB; 00=PREVIOUS GOOD DB; DB=REAL DB;
JMP T05ASL /SUBTEST LOOP,
JMP I T05LDR

T5LP6, LOOP6 /*****LOOP 6****
JMP T05A

/VERIFY PROPER DATA TRANSFERS FROM DB TO XBUFF+1 (IN MEMORY)
/USING THE READ FUNCTION, CA SET TO XBUFF AND SHOULD
/INCREMENT TO XBUFF+1 BEFORE TRANSFER, COMPLEMENT INCREMENTING
/DATA USED,
T05B, JMS I T05LDS /GET DATA,
7777
JMP T5LP7 /DONE EXIT,
T05BSL, CLEAR2 /CLEAR REGISTERS,
TAD XBUFFP /LOAD CA WITH XBUFF,
LCAR
    
```

1432 1157 TXXTM1 /DATA TO DB,  
 1433 4501 LDBR /FIELD TO CM,  
 1434 1165 TAD /SET HEAD FUNCTION  
 1435 4477 LCMR /1 DATA BREAK,  
 1436 4541 READ /COMPARE MEMORY  
 1437 4535 BREAK  
 1440 1051 TAD XBUFF+1

1441 4533 COMPAR  
 1442 7015 ER058  
 /GD= GOOD CONTENTS OF XBUFF+1) BDIREAL CONTENTS OF XRUFF+1)  
 /OD= PREVIOUS GOOD CONTENTS OF XBUFF+1,  
 JMP T05BSL /SUBTEST LOOP,  
 JMP I T05LDR

1445 4530 T5LP7, LOOP7 /\*\*\*\*\*LOOP 7\*\*\*\*  
 1446 5222 JMP I T5LP6  
 1447 5600 JMP I TEST05

1450 0707 T05LDS, T02LDS  
 1451 0731 T05LDR, T02LDR

```

/TEST 06, CURRENT ADDRESS MEMORY REFERENCE TEST.
/1, VERIFIES THAT CA AND CM6-8 CAN REFERENCE MEMORY
CORRECTLY IN ALL EXISTING MEMORY FIELDS.
/2, THE METHOD USED INSERTS THE CURRENT ADDRESS AND THEN THE COMPLEMENT
/OF THE ADDRESS IN THE CURRENT MEMORY LOCATION FOR DATA. THE CA AND EMA ARE SET TO THE
/CURRENT LOCATION - 1; THEN A 1 WORD SIMULATED WRITE IS
/EXECUTED; MEMORY IS RESTORED TO ITS ORIGINAL CONTENTS AND THEN
/DB AND DATA ARE COMPARED. DATA IS NOT INSERTED IN LOCATIONS
/MARK1 THROUGH MARK2. THE ACTUAL MEMORY CONTENTS ARE USED FOR DATA.
/3, THE SUBTEST LOOP CYCLES ON THE SAME MEMORY LOCATION
/USING THE SAME DATA.

```

```

PAGE
TEST06, 0 CLEAR1
/VERIFY THAT CA AND EMA (CM6-8) REFERENCE THE CORRECT MEMORY LOCATION
/AND THAT DB RECEIVES CORRECT DATA, WRITE FUNCTION USED, DATA IS ADDRESS
/AND COMPLEMENT ADDRESS.
T06A, JMS T6EX1
      CLT OLD /SET UP CA AND CM.
      TAD LCAR TXXTM2
      LCMR JMS I FUNCN /WRITE=GO=EMA INCREMENT ENABLED
      JMS 4140 /INTERRUPT SYSTEM TURNED
      IOF /OFF.
      TAD DCA /CHANGE TO CURRENT DATA FIELD.
      DCA /N /GET ORIGINAL MEM AND SAVE,
      TAD DCA T06AMP /GET DATA AND PUT IN MEM,
      DCA I T06AMP /DATA BREAK,
      SKP /RESTORE MEMORY.
      6727
      NOP
      TAD T6SAVE
      DCA I T06AMP /CHANGE TO THIS PROGRAMS DATA FIELD.
      JMS I SCDFP /INTERRUPT SYSTEM ON.
      ION /DATA SHOULD BE IN DB.
      RDBR
      COMPAR
      ER06A
/OD=GOOD DB; OD=CURRENT MEMORY LOCATION BEING USED MINUS ONE; DB=REAL DB
      JMP T06A+1 /SUBTEST LOOP,
      JMS T6EX2
      LOOP6 T06A-1
      JMP /*****LOOP 6****

```

```

1600 4242
1600 4517
1601 1156
1605 4475
1606 1160
1607 4477
1610 4456
1611 4140
1612 6002
1613 1161
1614 3154
1615 6201
1616 1623
1617 3361
1620 1161
1621 3623
1622 7410
1623 0000
1624 6727
1625 7000
1626 1361
1627 3623
1630 4457
1631 6001
1632 4510
1633 4533
1634 7017
1635 5203
1636 4264
1637 4527
1640 5201

```

```

1641 5600 JMP I TEST06
1642 0000 MEMFLD
1643 1167 TAD
1644 3157 DCA
1645 1046 TAD
1646 3215 DCA
1647 3223 T06ADF
1650 1362 DCA
1651 7041 TAD
1652 3364 CIA
1653 1364 DCA
1654 1363 TAD
1655 7041 CIA
1656 3365 DCA
1657 7040 CMA
1660 3366 DCA
1661 1356 TAD
1662 3313 DCA
1663 7410 SKP
1664 0000 0
1665 2366 ISZ
1666 7610 SKP
1667 5273 CLA
1670 7240 CLA
1671 3367 DCA
1672 5313 JMP
1673 3367 DCA
1674 1037 TAD
1675 3366 DCA
1676 1223 TAD
1677 3156 DCA
1700 1215 TAD
1701 0026 AND
1702 3160 DCA
1703 2223 ISZ
1704 5313 JMP
1705 2157 ISZ
1706 7410 SKP
1707 5664 JMP
1710 1215 TAD
1711 1023 TAD
1712 3215 DCA
1713 0000 0

T6EX1,
T6EX2,
T6EX3,
T6EX4,
T6FLAG

1714 1367 SPA
1715 7710 JMP
1716 5347 TAD
1717 1160 CIA
1720 7041 TAD
1721 1165 SNA
1722 7650 CLA

MEMFLD
TXXTM1
KCDF
T06ADF
T06AMP
MARK1P
T6CNT1
T6CNT1
MARK2P
T6CNT2
T6CNT3
KNOP
T6I1
T6CNT3
CLA
CMA
T6FLAG
T6I1
M2
T6CNT3
T06AMP
OLD
K70
TXXTM2
T06AMP
T6I1
TXXTM1
T6EX2
T06ADF
K10
T06ADF
T6EX3
T6EX4
T6FLAG
SPA
T6EX4
TXXTM2
PRGFLO
SNA
CLA

/TEST 06 LOCAL EXECUTIVE;
/GET EXTENDED MEM FIELDS
/AND PUT IN TEMP.
/INITIALIZE CDF INSTRUCTION,
/SET STARTING ADDRESS TO 0,
/MAKE THE ADDRESS MARK1 INTO
/A COUNTER,
/MAKE (MARK2-MARK1)+2 INTO
/A COUNTER,
/FAKE THE COMPLEMENT COUNTER SO
/IT RESETS ON FIRST ENTRY,
/SET T6I1 TO NOP,
/ENTER HERE AT END OF EACH PASS,
/TIME FOR COMPLEMENT DATA?
/YES, SET COMPLEMENT DATA
/FLAG, THEN GO TO T6I1
/FOR ROUTING,
/NO, CLEAR COMPLEMENT FLAG,
/RESET COMPLEMENT COUNTER,
/PUT PREVIOUS ADDRESS IN
/OLD,
/PUT PREVIOUS DATA FIELD
/IN TEMP FOR LOADING
/QM6=0,
/UPDATE TO NEW CURRENT ADDRESS,
/NO OVERFLOW = GO TO T6I1 FOR ROUTING,
/ADDRESS OVERFLOW, CHECK FOR
/ANOTHER EXISTING MEM FIELD,
/NO MORE LEFT, EXIT TO END OF TEST,
/SOME FIELDS LEFT, UPDATE
/CDF INSTRUCTION FOR NEW
/FIELD,
/THIS INSTRUCTION CAN BE ONE OF THREE INSTRUCTIONS!
/PRIOR TO REACHING MARK1!
/AFTER REACHING MARK1 BUT NOT MARK2!
/AFTER PASSING MARK2,
/NOT UP TO MARK1, TIME FOR DATA COMPLEMENT?
/YES, BYPASS MARK1 UPDATES,
/HAS THE CURRENT ADDRESS REACHED
/THE PROGRAM FIELD?
/NO, BYPASS MARK1 UPDATES,

```

```

1723 2364      ISZ      T6CNT1
1724 5347      JMP      T6EX4
1725 1357      TAD      KJMP61
1726 3313      DCA      T6I1
1727 1367      TAD      T6FLAG
T6EX3:
1730 7710      SPA      CLA
1731 5337      JMP      T6EX5
1732 2365      ISZ      T6CNT2
1733 5337      JMP      T6EX5
1734 1360      TAD      KJMP62
1735 3313      DCA      T6I1
1736 5347      JMP      T6EX4
T6EX5:
1737 1215      TAD      T06ADF
1740 3342      DCA      .+2
1741 6002      IOF
1742 6201      CDF
1743 1623      TAD      I
1744 4457      JMS      I
1745 6001      ION
1746 5353      JMP
T6EX4:
1747 1223      TAD
1750 2367      ISZ
1751 7040      CMA
1752 7040      CMA
T6EX6:
1753 3161      DCA
1754 4474      CHCR
1755 5642      JMP      I

KNOP,      NOP
KJMP61,   JMP
KJMP62,   JMP
T6SAVE,   0
MARK1P,   MARK1
MARK2P,   MARK2+2

T6CNT1,   0
T6CNT2,   0
T6CNT3,   0
T6FLAG,   0

/NO, UPDATE MARK1 COUNTER,
/NOT THERE YET, USE DATA,
/AT MARK1, CHANGE ROUTING TO T6EX3;
/PASSED OR AT MARK1, COMPLEMENT
/DATA PASS;
/YES, BYPASS (MARK2=MARK1)+2 UPDATES,
/NO, ARE WE PASSED MARK2?
/NO, USE ACTUAL MEM CONTENTS,
/YES, CHANGE ROUTING TO EX4,
/USE MEM CONTENTS VICE DATA, GET CURRENT
/DOF INSTRUCTION AND STORE FOR USE;
/INTERRUPT SYSTEM OFF,
/CHANGE TO CURRENT OF;
/GET CONTENTS OF CURRENT ADDRESS,
/RESET TO OF OF PROGRAM,
/INTERRUPT SYSTEM ON,
/PUT MEM CONTENTS IN TEMP AND EXIT;
/USE DATA, GET CURRENT ADDRESS
/FOR DATA, TIME FOR COMPLEMENT?
/NO, COMPLEMENT DATA ONCE,
/YES, DITTO OR COMPLEMENT DATA AGAIN,
/STORE DATA OR MEM CONTENTS IN TEMP
/USED FOR STORING DATA IN CURRENT
/LOCATION, THEN RUN TEST,
/BIASED BY +2 TO YIELD CORRECT TALLY
/NUMBER FOR (MARK2=MARK1)+2,
/MARK1 COUNTER,
/(MARK2=MARK1)+2 COUNTER,
/COMPLEMENT DATA COUNTER,
/SET TO 7777 IF TIME FOR COMPLEMENT DATA.

```

/TEST 07: STATUS REGISTERS STAND ALONE TEST.

/1. THE PURPOSE OF THIS TEST IS TO VERIFY THAT MS AND FS INDICATE ONLY WHAT IS EXPECTED WITH THE TRANSPORT SYSTEM EFFECTIVELY DISCONNECTED FROM THE TM8E CONTROL. ALL INDICATORS WHICH MAY BE IN ONE STATE OR ANOTHER ARE DISREGARDED.

/2. LOOPS 6-7:

/2.1 LOOP 6 CYCLES ON MS CHECK (T07A=T07B)  
/2.2 LOOP 7 CYCLES ON S1 CHECK (T07C=T07D)

2000	PAGE	
2000	TEST07, 0	
2001	CLEAR1	
2002	1032	T07A, TAD K400 /SELECT REMOTE BIT TO GOOD;
2003	3154	DCA GOOD
2004	4505	RMSR /READ MS AND MASK OUT
2005	0251	AND K3774 /EF, R/C AND IF BITS.
2006	4533	COMPAR
2007	7021	ER07A
2010	5204	/GD=GOOD MS MASKED; BD=REAL MS MASKED; JMP T07A+2 /SUBTEST LOOP
2011	7240	
2012	4505	/VERIFY THAT RMSR CLEARS AC BEFORE READING; SELECT REMOTE
2013	0251	/SHOULD BE ONLY BIT SET.
2014	4533	T07B, CLA CMA /AC=7777,
2015	7023	RMSR AND K3774 /READ MS AND MASK OUT
2016	5211	COMPAR ER07B /EF, R/C AND IF BITS,
2017	4527	/GD=GOOD MS MASKED; BD=REAL MS MASKED;
2020	5202	JMP T07B /SUBTEST LOOP,
2021	4537	
2022	1025	LOOP6 T07A /*****LOOP6****
2023	3154	JMP OFFLIN /FUNCTION TO 00.
2024	1005	TAD K40
2025	4477	DCA GOOD
2026	4507	TAD K3
2027	4533	LCMR /LOAD 9 TRK 800BPI
2030	7025	RFSR
		COMPAR
		ER07C
		/GD=GOOD FS; BD=REAL FS;
		JMP T07C+3 /SUBTEST LOOP.
2031	5224	
		/VERIFY THAT RFSR CLEARS AC PRIOR TO READING.

```

2032 7240 T070, CLA CMA /AC=7777,
2033 4507 RFSR /READ FS-9TRK BIT SHOULD
2034 4533 COMPAR /STILL BE SET,
2035 7027 ER07D
/GD=GOOD FS: BD=REAL FS. /SUBTEST LOOP
2036 5232 JMP T07D

2037 3154 /VERIFY THAT CM BIT 11 = 0 FORCES 7 CHANNEL. (FS BIT 6 = 0)
2040 7126 T07E, DCA GOOD /DENSITY BITS=10
2041 4477 SIL RTL
LCMR
RFSR
2042 4507 COMPAR
2043 4533 ER07E
2044 7031 /GD=GOOD FS: BD=REAL FS,
2045 5240 JMP T07E+1 /SUBTEST LOOP,

2046 4530 LOOP7 /*****LOOP 7****
2047 5217 JMP T07C-2

2050 5600 JMP I TEST07
2051 3774 K3774, 3774

```

/TEST 10, BASIC TESTS FOR MTF, GO BIT, IF, R/C ERROR, EMA OVERFLOW  
 /AND EF, /VERIFIES EXISTENCE AND PROPER FUNCTIONING OF THE ABOVE ON A  
 /PARTIAL BASIS,

/LOOPS 6-9;  
 /LOOP 6 CYCLES ON MTF AND GO BIT TESTS (T10A-T10J)  
 /LOOP 7 CYCLES ON IF TESTS (T10K-T10R)  
 /LOOP 8 CYCLES ON R/C AND EMA CHECKS (T10S-T10W)  
 /LOOP 9 CYCLES ON EF CHECKS (T10X-T10Z)

2200	PAGE		
0000	TEST10, 0		
2201	CLEAR1		
2202	JMS	T10A	/EXECUTE T10A
2203	JMP	T10B	/THEN GO TO T10B
2204	0		/VERIFY CLI CLEARS MTF, CHECK VIA SKTD NOT SKIPPING,
0000	DCA	GOOD	/JMS'D HERE
2205	CLEAR2		/CLI
2206	SKTD		/SHOULD NOT SKIP.
2207	SKP		
2210	CMA		
2211	COMPAR		
2212	ER10A	T10A+2	/SUBTEST LOOP.
2213	JMP	T10A	
2214	JMP I		
2215			
2216			
4545			
2217	WCOV		/SPACE FORWARD;
4553	LOBR		/FORCE WCOV
2220	SKTD		/SET MTF
4501	CMA		/MTF SHOULD BE SET CAUSING
2221	COMPAR		/SKTD TO SKIP,
7040	ER10B	T10B	/SUBTEST LOOP,
2223	JMP		
4533			
2224	JMS	T10A	/T10A AGAIN
7035	CLA	CMA	/SET UP FOR NEXT.
5216	DCA	GOOD	
2225			
4204			
2226			
7240			
3154			
2231			
4524			
2232			
4473			
33			
4545			
34			
4550			

/VERIFY WCOV OCCURS WITH WC=7777, SPACE FORWARD THEN  
 /1 DATA BREAK FOLLOWED BY LOBR, LOBR,WCOV SHOULD SET MTF AND  
 /SKTD SHOULD SKIP,  
 T10B,  
 SPCFWD  
 WCOV  
 LOBR  
 SKTD  
 CMA  
 COMPAR  
 ER10B  
 JMP  
 T10B  
 JMS  
 CLA  
 DCA  
 /VERIFY LOBR WITH NO WCOV DOES NOT SET MTF,  
 /CHECK VIA SKTD,  
 T10C,  
 CLEAR2  
 LWCR  
 SPCFWD  
 GO  
 /CLEAR ALL,  
 /ENSURE WC=0000  
 /SPACE FORWARD FUNCTION,  
 /GO.



2235 4535 BREAK  
 2236 4501 LDBR  
 2237 4514 SKTD  
 2240 7040 CMA  
 2241 4533 COMPAR  
 2242 7037 ER10C  
 2243 5231 JMP T10C  
 /1 DATA BREAK.  
 /SKTD SHOULD NOT SKIP  
 /SUBTEST LOOP.

2244 4524  
 2245 4761  
 2246 7477  
 2247 7753  
 2250 2253  
 2251 4545  
 2252 4553  
 2253 0000  
 /VERIFY WCOV AND AN IOT OTHER THAN LDBR  
 /DOES NOT SET MTF. VARIABLE IOT (I1) INSERTED BY EXECUTIVE,  
 T10E,  
 CLEAR2  
 JMS I T10X1P /GET IOT.  
 T10LS1-1  
 -25  
 .43  
 SPCFWD  
 WCOV  
 0  
 /PUT IT HERE.

2254 7000 NOP  
 2255 7200 CLA  
 2256 4514 SKTD  
 2257 7040 CMA  
 2260 4533 COMPAR  
 2261 7041 ER10D  
 /I1=VARIABLE IOT  
 JMP I T10X2P /SUBTEST LOOP,  
 JMS I T10X3P /GET MORE IOT'S.  
 /SKTD SHOULD NOT SKIP.

2264 4524  
 2265 4540  
 2266 4514  
 2267 7040  
 2270 4533  
 2271 7043  
 2272 5264  
 /VERIFY THAT REWIND FUNCTION WITHOUT RW STATUS (SINCE  
 /NO DRIVE ON LINE) DOES NOT SET MTF,  
 T10E,  
 CLEAR2  
 REWIND  
 SKTD  
 CMA  
 COMPAR  
 ER10E  
 JMP T10E  
 /SHOULD NOT SKIP.  
 /SUBTEST LOOP.

2273 4524  
 2274 4546  
 2275 4514  
 2276 7040  
 2277 4533  
 2300 7045  
 2301 5273  
 /VERIFY SPACE REVERSE WITHOUT BUT DOES NOT SET MTF,  
 T10F,  
 CLEAR2  
 SPCREV  
 SKTD  
 CMA  
 COMPAR  
 ER10F  
 JMP T10F  
 /SUBTEST LOOP.

2302 4524  
 2303 4541  
 2304 4542  
 /VERIFY READ, READ COMPARE OR WRITE WITHOUT LPCS DOES NOT SET  
 /MTF,  
 T10G,  
 CLEAR2  
 READ  
 RDCOMP

```

2305 4543 WRITE
2306 4514 SKTD
2307 7040 CMA /SHOULD NOT SKIP.
2310 4533 COMPAR
2311 7047 ER10G
2312 5302 JMP T10G /SUBTEST LOOP,

```

```

2313 1027 /VERIFY "GO" BIT SETS WHEN LOADED AND MTF CLEAR,
2314 3154 T10H, TAD K100
2315 4524 DCA GOOD
2316 4550 CLEAR2
2317 4507 GO
2320 0027 RFSR K100
2321 4533 COMPAR
2322 7051 ER10H
2323 5315 JMP /GD= GOOD "GO" BIT; BD= REAL "GO" BIT, /SUBTEST LOOP,

```

```

2324 3154 /VERIFY "GO" BIT CLEARS AFTER LOADED WITH MTF SET;
2325 4545 T10I, DCA GOOD
2326 4553 SPCFWD /SET MTF,
2327 4501 WCOV /SET MTF;
2330 4550 LDBR /SET "GO"/ SHOULD CLEAR
2331 4507 GO /IMMEDIATELY,
2332 0027 AND K100
2333 4533 COMPAR
2334 7053 ER10I
2335 5325 /GD= GOOD "GO" BIT; BD= REAL GO BIT, /SUBTEST LOOP,
JMP T10I+1

```

```

2336 4524 /VERIFY CLT CLEARS "GO" BIT.
2337 4550 T10J, CLEAR2
2340 4524 GO /SET "GO"
2341 4507 CLEAR2 /CLT
2342 0027 RFSR /CHECK CLEAR;
2343 4533 AND K100
2344 7055 COMPAR
2345 5337 ER10J
/VERIFY CLT CLEARS "GO" BIT; BD= REAL "GO" BIT, /SUBTEST LOOP,
JMP T10J+1

```

```

2346 4527 LOOP6
2347 5201 JMP TEST10+1 /*****LOOP 6****

```

```

2350 3154 /VERIFY CLT CLEARS ILLEGAL FUNCTION (IF), ACTUALLY IT SHOULD
2351 4524 /NEVER BE SET IN THIS TEST - SEE T100.
2352 4505 T10K, DCA GOOD
2353 0020 RMSR
2354 4533 AND K1
2355 0020 COMPAR

```

```

2355 7057 ER10K
/GD= GOOD "IF"; BD= REAL "IF"
JMP T10K+1 /SUBTEST LOOP

2356 5351

2357 5760 JMP I ,*1
2360 2400 T100
2361 1213 T10X1P; T3EX1
2362 1227 T10X2P; T3EX3+2
2363 1231 T10X3P; T3EX2

```

PAGE

```

2400
/VERIFY WRITE OR WEOF WITHOUT FILE PROTECT DOES NOT SET IF.
T100, CLEAR2
2401 4543 WRITE
2402 4544 WEOF
2403 4505 RMSR
2404 0020 AND K1
2405 4533 COMPAR
2406 7061 ER100
/GO= GOOD IF; BD= REAL IF (BIT 11)
JMP T100 /SUBTEST LOOP

```

```

2407 5200
/VERIFY LCMR, LFGR OR LOBR WITHOUT CONTROL BUSY DO NOT
/SET IF.
T10P, CLEAR2
2410 4524 LCMR
2411 4477 LFGR
2412 4500 LFGR
2413 4501 LOBR
2414 4505 RMSR
2415 0020 AND K1
2416 4533 COMPAR
2417 7063 ER10P
/GO= GOOD IF; BD= REAL IF (BIT 11)
JMP T10P /SUBTEST LOOP

```

```

2420 5210
/VERIFY 9 CHANNEL AND CM10=0 AND "GO" DO NOT SET IF.
/("GO" SHOULD NOT GENERATE "PRESEI" NOW.)

```

```

2421 4524 T100, CLEAR2
2422 4550 GO /CM10=0 FROM CLEAR;
2423 4505 RMSR
2424 0020 AND K1
2425 4533 COMPAR
2426 7065 ER100
/GO= GOOD IF; BD= REAL IF (BIT 11)
JMP T100 /SUBTEST LOOP.

```

```

2427 5221
/VERIFY SPCREV WITHOUT BOT DOES NOT SET IF.
T10R, CLEAR2
2430 4524 SPCREV
2431 4546 SPCREV
2432 4505 RMSR

```

```

2433 0020      AND      K1
2434 4533      COMPAR
2435 7067      ER10R
          /GD= GOOD IF; BD= REAL IF (BIT 11)
2436 5230      JMP      T10R      /SUBTEST LOOP,

2437 4530      LOOP7
2440 5752      JMP I   T10KP      /*****LOOP 7*****

2441 4243      JMS     T10S      /EXECUTE T10S
2442 5254      JMP     T10T      /THEN ONTO T10T,

2443 0000      /VERIFY "CLT" CLEARS READ/COMPARE ERROR,
2444 3154      DCA     GOOD
2445 4524      CLEAR2
2446 4505      RMSR
2447 0021      AND     K2
2448 4533      COMPAR
2449 7071      ER10S

2452 5245      /GD= GOOD R/C ERROR BIT; BD= REAL R/C ERROR (BIT 10)
2453 5643      JMP     T10S+2
          JMP I   T10S

2454 1021      /VERIFY THAT DB=7777 AND MEM=0000 YIELDS AN R/C ERROR,
2455 3154      TAD     K2
2456 3051      DCA     GOOD
          DCA     XBUFFER+1      /MEM IO 0000
2457 4524      CLEAR2
          TAD     XBUFFER      /MEM+1 TO CA
2460 1047      LQAR
2461 4475      TAD     PRGFLO      /FIELD TO CM,
2462 1165      TAD     CMA      /7777 IO DB
2463 4477      LDBR
2464 7040      RDCOMP
2465 4501      BREAK
2466 4542      RMSR
2467 4535      AND     K2
2470 4505      COMPAR
2471 0021      ER10T
2472 4533      /GD= GOOD R/C ERROR; BD= REAL R/C ERROR (BIT 10)
2473 7073      JMP     T10T+3      /SUBTEST LOOP

2474 5257      JMS     T10S
2475 4243      JMS     T10U
2476 4300      JMP     T10V

2500 0000      /VERIFY CLT CLEARS EMA OVERFLOW BIT,
2501 3154      T10U,
2502 4524      DCA     GOOD
          CLEAR2
          RFSR
          3 4507
    
```

```

2504 0023 AND K10
2505 4533 COMPAR
2506 7075 ER10U
/GD= GOOD EMA OVERFLOW; BD= REAL EMA OVERFLOW (BIT 8)
      T10U+2
      JMP I T10U
2507 5302
2510 5700

2511 1023
2512 3154
2513 4524
2514 7040
2515 4475
2516 4536
2517 0070
2520 4456
2521 0140
2522 4476
2523 4507
2524 0023
2525 4533
2526 7077

2527 5313
2530 4300

2531 4524
2532 4476
2533 4536
2534 0070
2535 4456
2536 0140
2537 7040
2540 4475
2541 4507
2542 0023
2543 4533
2544 7101
2545 5331
2546 4531
2547 5237

2550 5751
2551 2600
2552 2346

2600 2600
2602 4202

/VERIFY EMA OVERFLOW BIT SETS WHEN EMA SET TO 7
/AND CA 0 CHANGES FROM 1 TO 0,
T10V,
DCA K10
DCA GOOD
CLEAR2
CMA
LCAR
COMAND
70
JMS I FUNC
0140
CCAR
RFSR
AND K10
AND COMPAR
ER10V
/GD= GOOD EMA OVERFLOW; BD= REAL EMA OVERFLOW (BIT 8)
      T10V+2
      JMP
      JMS T10U

/VERIFY EMA SET TO 7 AND CA 0 CHANGING FROM 0 TO 1
/DOES NOT SET EMA OVERFLOW BIT,
T10W,
CLEAR2
CCAR
COMAND
70
JMS I FUNC
0140
CMA
LCAR
RFSR
AND K10
AND COMPAR
ER10W
/GD= GOOD EMA OVERFLOW (BIT 8)
      BD= REAL EMA OVERFLOW (BIT 8)
      T10W
      JMP
      T10LP8, LOOP8
      JMP T10S-4
      /*****LOOP 8****

JMP I 1
T10X-2
T10KP, T10K-2
PAGE
JMS T10X

```

2601 5213

JMP T10Z

/VERIFY CLI CLEARS EF,  
T10X,

2602 0000  
2603 3154  
2604 4524  
2605 4505  
2606 0034  
2607 4533  
2610 7103

DCA GOOD  
CLEAR2  
RMSR  
AND K4000  
COMPAR  
ER10X

/GD= GOOD EF; BD= REAL EF (BIT 0)  
JMP T10X+2 /SUBTEST LOOP,  
JMP I T10X

2611 5204  
2612 5602

/VERIFY R/C ERROR WITH MTF SET SETS EF,  
T10Z,

2613 1034  
2614 3154  
2615 4524  
2616 1047  
2617 4475  
2620 1165  
2621 4477  
2622 7040  
2623 4501  
2624 4542  
2625 4535  
2626 4545  
2627 4553  
2630 4501  
2631 4505  
2632 0034  
2633 4533  
2634 7105

TAD K4000  
DCA GOOD  
CLEAR2  
TAD XBUFFP /GENERATE R/C ERROR,  
LCAR PRGFLO  
LCMR  
CMA  
LDBR  
RDCOMP  
BREAK  
SPCFWD  
WCOV  
LDBR  
RMSR  
AND K4000  
COMPAR  
ER10Z

/GD=GOOD EF; BD=REAL EF(BIT 0)  
JMP T10Z+2 /SUBTEST LOOP  
JMS T10X

2635 5215  
2636 4202

/VERIFY EMA OVERFLOW AND MTF SET SETS EF,  
T101,

2637 1034  
2640 3154  
2641 4524  
2642 7240  
2643 4475  
2644 4536  
2645 0070  
2646 4456  
2647 0140  
2650 4476  
2651 4545  
2652 4553  
2653 4505  
2654 0034  
5 4533

TAD K4000  
DCA GOOD  
CLEAR2  
CLA CMA  
LCAR  
COMAND  
70  
JMS I FUNC  
0140  
CCAR  
SPCFWD  
WCOV  
RMSR  
AND K4000  
COMPAR  
/FORCE MTF  
/CHECK  
/GENERATE EMA OVERFLOW

/FORCE MTF  
/CHECK

```

2656 7107 ER101
2657 5241 /GD=GOOD EF; BD=REAL EF(BIT 0)
          JMP T101+2 /SUBTEST LOOP

2660 3154 /VERIFY EMA OVERFLOW WITHOUT MTF SET DOES NOT SET EF,
2661 4524 T102, DCA GOOD
2662 7240 CLEAR2
2663 4475 CLA CMA
2664 4536 LCAR
2665 0070 COMMAND
2666 4456 70 JMS I FUNCP

2667 0140 0140
2670 4476 CCAR
2671 4505 RMSR
2672 0034 AND K4000
2673 4533 COMPAR
2674 7111 ER102
          /GD=GOOD EF; BD=REAL EF (BIT 0)
          JMP T102+1 /SUBTEST LOOP,

2675 5261

2676 4532 LOOP9
2677 5705 JMP I T10LPP /*****LOOP9*****/

2700 1704 TAD I TST10P
2701 3303 DCA .+2
2702 5703 JMP I .+1
2703 0000 0
2704 2200 TST10P, TEST10
2705 2546 T10LPP, T10LP8

```

/TESI11, MTF AND EF SKIP=NO SKIP TEST,  
 /1, VERIFIES PROPER SKIP OR NO SKIP OF ALL 672X IOT'S  
 /WITH MTF AND EF BOTH CLEAR, THEN EACH SET WITH THE OTHER CLEAR.

/2, ALL IOT'S USED (672X) ARE SUPPLIED BY A LOCAL  
 /EXECUTIVE AND IF AN ERROR OCCURS THE IOT IN USE IS DISPLAYED  
 /UNDER"!!" IN THE ERROR PRINTOUT,

/3, ALL SUBTEST LOOPS CYCLE ON IOT IN PRESENT USE;

/4, LOOPS 6-8:  
 /4.1 LOOP 6 CYCLES ON T11A-T11B (MTF AND EF CLEAR)  
 /4.2 LOOP 7 CYCLES ON T11C-T11D (MTF SET, EF CLEAR)  
 /4.3 LOOP 8 CYCLES ON T11E-T11F (MTF SET, EF SET)

PAGE	TEST	DESCRIPTION
3000	TEST11, 0	GOOD
3000	0	CLEAR1
3001	4523	DCA
3002	3154	GOOD
3003	4524	
3004	4513	
3005	7040	
3006	4533	
3007	7113	
3010	5203	
		/VERIFY SKCB SKIPS WHEN MTF AND EF CLEAR, T11A, CLEAR2 /CLEAR ALL, SKCB /SHOULD SKIP. CMA COMPAR ER11A JMP T11A /SUBTEST LOOP
3011	4322	
3012	3147	
3013	7772	
3014	3016	
3015	4524	
3016	0000	
3017	7410	
3020	7040	
3021	4533	
3022	7115	
		/VERIFY ALL 672X IOT'S WITH THE EXCEPTION OF SKCB DO NOT SKIP /WHEN MTF AND EF CLEAR, IOT IS UNDER I11, T11B, JMS T11EX1 /GET AN IOT, T11LS1+1 -6 T11B1 CLEAR2 T11B1, SKP /INSERTED HERE AND SHOULD NOT /SKIP, CMA COMPAR ER11B WHICH FAILED, /I1=I0I JMP T11B+4 /SUBTEST LOOP JMS T11EX2
3023	5215	
3024	4341	
3025	4527	
3026	5203	
		LOOP6 JMP T11A /*****LOOP6*****
3027	4322	
3030	3146	
3031	7776	
3032	3037	
		/VERIFY SKCB AND SKTD SKIP WHEN MTF SET AND EF CLEAR, T11C, JMS T11EX1 /GET IOT, T11LS1 -2 T11C1



```

3033 4524 CLEAR2 /FORCE MTF
3034 4545 SPCFWD
3035 4553 WCOV
3036 4501 LDBR /SET MTF,
3037 0000 /IOT SHOULD SKIP,
3040 7040 CMA
3041 4533 COMPAR
3042 7117 ER11C
/11=FAILING IOT, T11C+4
JMP T11EX2
JMS
3043 5233
3044 4341

```

```

3045 4322 /VERIFY SKTR,SDLE,SBRM AND CLF DO NOT SKIP WHEN
3046 3151 /MTTF SET AND EF SET,
3047 7774 T11D, T11LS1+3 /GET AND IOT
3050 3055 -4
T11D1 /FORCE MTF,
3051 4524 CLEAR2
3052 4545 SPCFWD
3053 4553 WCOV
3054 4501 LDBR
3055 0000 /SET MTF (ALSO SETS IF AND EF)
3056 7410 /IOT SHOULD NOT SKIP,
3057 7040 SKP
3060 4533 CMA
3061 7121 ER11D
/11=FAILING IOT
JMP T11D+4 /SUBTEST LOOP,
JMS T11EX2
LOOP7 /*****LOOP7****
JMP T11C-2

```

```

3066 4322 /VERIFY SKEF,SKTD AND SKCB SKIP WHEN MTF AND EF SET,
3067 3145 T11E, JMS T11EX1
3070 7775 T11LS1=1
3071 3074 -3
T11E1
3072 4524 CLEAR2 SETEF
3073 4357 JMS
3074 0000 T11E1, /SET MTF AND EF,
3075 7040 CMA /IOT SHOULD SKIP,
3076 4533 COMPAR
3077 7123 ER11E
/11=FAILING IOT, T11E+4
JMP T11EX2
JMS
3100 5272
3101 4341

```

```

3102 4322 /VERIFY SKTR,SDLE,SBRM AND CLF DO NOT SKIP WHEN MTF
3103 3151 /AND EF SET,
T11F, JMS T11EX1
T11LS1+3 /GET AN IOT,

```



3165 1165  
 3166 4477  
 3167 4542  
 3170 4535  
 3171 4545  
 3172 4553  
 3173 4501  
 3174 5757

TAD  
 LCMR  
 RDCOMP  
 BREAK  
 SPCFWD  
 WCOV  
 LDBR  
 JMP I  
 PRGFLD  
 SETEF

/TEST 12, MTTF=EF INTERRUPT TEST,

/1, VERIFIES CORRECT INTERRUPT NON-OCCURRENCES AND OCCURRENCES  
/WITH ALL INTERESTING COMBINATIONS OF MTTF, EF AND THEIR ENABLES,

/2, IF AN ERROR OCCURS, THE CM AND MS PRINTOUTS SHOW  
/THE CURRENT COMBINATION,

/3, ALL SUBTEST LOOPS CYCLE ON CURRENT COMBINATIONS,

/4, LOOPS 6-8:

/4.1 LOOP 6 CYCLES ON T12A (MTTF AND EF CLEAR)  
/4.2 LOOP 7 CYCLES ON T12B-T12C (MTTF SET, EF CLEAR)  
/4.3 LOOP 8 CYCLES ON T12D-T12E (MTTF SET, EF SET,)

3200	PAGE	
3200	TEST12, 0	
3201	CLEAR1	
3202	IOF	
3203	JMS I INSRSP	
3204	T12INT	
3205	T12INT	
3206	DCA	GOOD

/VERIFY NO INTERRUPT WHEN EF AND MTTF CLEAR REGARDLESS OF STATE OF

/INTERRUPT ENABLE BITS,

3207	T12A,	JMS	/GET ENABLES;
3210		T12EX1	
3211		T12LS1=1	
3212		-3	
3213	T12A1		
3214	CLEAR3		/LOAD CM WITH ENABLES,
3215	COMAND		
3216	0	T12EX4	/CHECK FOR NO INTERRUPT,
3217	JMS		
3220	COMPAR		
	ER12A		

/CM= ENABLE BITS,

3221	JMP	T12A+4	/SUBTEST LOOP,
3222	JMS	T12EX2	
3223	LOOP6		
3224	JMP	T12A-1	/*****LOOP 6****

/VERIFY NO INTERRUPT WHEN MTTF AND EF SET, AND BOTH INTERRUPT ENABLES CLEAR,

T12B,

3225	DCA	GOOD	
3226	CLEAR3		/CLEAR ALL;
3227	SPCFWD		/FORCE MTTF
3230	WCOV		
3231	LDBR		/CLEAR ENABLES
3232	COMAND		
3233	0		/CHECK
3234	JMS	T12EX4	TERRUPT

```

3235 4533 COMPAR
3236 7131 ER12B
3237 5226 JMP T12B+1 /SUBTEST LOOP

/VERIFY INTERRUPT WITH MTF SET AND MITF INT ENABLE SET.
T12C,
3240 7240 CLA CMA
3241 3154 DCA GOOD
3242 4525 CLEAR3
3243 4545 SPCFWD
3244 4553 WCOV
3245 4501 LDBR
3246 4536 COMAND
3247 0100 100
3250 4326 JMS
3251 4533 COMPAR
3252 7133 ER12C
3253 5242 JMP T12C+2 /SUBTEST LOOP

LOOP7
3254 4530 JMP T12B-2 /*****LOOP 7****
3255 5223

```

```

/VERIFY NO INTERRUPT WITH EF AND MTF SET; AND ENABLES CLEAR.
T12D,
3256 3154 DCA
3257 4525 CLEAR3
3260 4745 JMS I SETEFP
3261 4536 COMAND
3262 0000 0
3263 4326 JMS
3264 4533 COMPAR
3265 7135 ER12D
3266 5257 JMP T12D+1 /SUBTEST LOOP

/VERIFY NO INTERRUPT WITH EF AND MTF SET; AND ENABLES CLEAR.
T12E,
3267 7240 CLA CMA
3270 3154 DCA GOOD
3271 4525 CLEAR3
3272 4745 JMS I SETEFP
3273 4536 COMAND
3274 0200 200
3275 4326 JMS
3276 4533 COMPAR
3277 7137 ER12E
3300 5271 JMP T12E+2 /SUBTEST LOOP

LOOP8
3301 4531 JMP T12D-2 /*****LOOP 8****
3302 5254

```

```

/VERIFY INTERRUPT WHEN EF AND EF INT ENABLE SET.
T12E,
3303 4523 CLEAR1
3304 5600 JMP I TEST12

/EXIT
3305 0000 T12EX1, 0 /TEST 12 COMMAND REGISTER CONSTANT
3306 1705 TAD I T12EX1 /SELECTOR, SAME BASIC METHOD
3307 3010 DCA 10

```

3310	2305	ISZ	T12EX1	
3311	1705	TAD I	T12EX1	
3312	3157	DCA	TXXTM1	
3313	2305	ISZ	T12EX1	
3314	1705	TAD I	T12EX1	
3315	3160	DCA	TXXTM2	
3316	2305	ISZ	T12EX1	
3317	1410	TAD I	10	
3320	3560	DCA I	TXXTM2	
3321	5705	JMP I	T12EX1	
3322	0000	0		
3323	2157	ISZ	TXXTM1	
3324	5317	JMP	T12EX3	
3325	5722	JMP I	T12EX2	
3326	0000	0		
3327	7200	CLA		
3330	6001	ION		
3331	7000	NOP		
3332	7000	NOP		
3333	6002	IOF		
3334	5726	JMP I	T12EX4	
3335	7240	CLA CMA		
3336	5726	JMP I	T12EX4	
3337	0000	0		
3340	0100	100		
3341	0200	200		
3342	0000	0		
3343	0200	200		
3344	0100	100		
3345	3157	SETEFP, SETEF		

```

/PUT 0'S IN INTERRUPT INDICATOR.
/(THE AC, )
/INTERRUPT ON.
/WAIT.
/INTERRUPT OFF, AND
/GET INDICATOR AND
/EXIT.
/MTTF, EF INT, HANDLER FOR TEST 12.
/JUST SET AC=7777.
/NO INT ENABLE.
/MTTF INT ENABLE.
/EF INT ENABLE.
    
```

```

/TEST 13, 672X IOT UNIQUENESS FOR CLF AND SBRM.
/1, VERIFIES THAT CLF CLEARS ONLY STATUS BITS AND NOT NON-
/STATUS REGISTERS (SINCE TUR HIGH) AND THAT NO OTHER 672X IOT
/GENERATES CLF OR SBRM.
/2, PORTIONS OF THIS TEST ARE RUN BY TEST 11 LOCAL EXECUTIVE
/IN WHICH CASE THE IOT IN USE IS DISPLAYED UNDER "I1:" IF
/AN ERROR OCCURS.
/3, LOOPS 6-7:
/3.1 LOOP 6 CYCLES ON T13A-T13C (CLF CHECKS)
/3.2 LOOP 7 CYCLES ON T13D-T13E (SBRM UNIQUENESS)

```

```

3400 PAGE
3400 TEST13, 0 CLEAR1
3401 4523

3402 3154
3403 4545
3404 4553
3405 4501
3406 4516
3407 4514
3410 7410
3411 7040
3412 4533
3413 7141
3414 5203

/USING MTF, VERIFY CLF CLEARS STATUS, MTF AND EF; THIS
/IS ENOUGH TO VERIFY THAT CLF IS WORKING.
T13A, DCA GOOD /FORCE MTF
      SPCFWD
      WCOV
      LOBR
      CLF /SHOULD CLEAR MTF
      SKTD /CHECK
      SKP /SUBTEST LOOP.
      CMA
      COMPAR
      ER13A
      JMP T13A+1

/VERIFY IOT 672X EXCEPT CLF DOES NOT CLEAR MTF.
T13B, JMS I T13EX1
      T11LS1
      *6
      T13B1
      SPCFWD /FORCE MTF
      WCOV
      LOBR
      NOP /EXECUTE IOT; SHOULD NOT CLEAR
      SKTD /MTF;
      CMA /CHECK;
      COMPAR
      ER13B
/ I1= FAILING IOT
      JMP T13B+4
      JMS I T13EX2

/VERIFY THAT CLF DOES NOT CLEAR CA (OR ANY OTHER NON-STATUS
/REGISTER) SINCE "TUR" IS HIGH,
T13C, CLA CMA
      DCA GOOD
3434 7240
3435 3154

```

```

3436 7240      CLA CMA
3437 4475      LCAR
3440 4516      CLF
3441 4504      RCRAR
3442 4533      COMPAR
3443 7145      ER13C
          /GD= GOOD CA
3444 5236      JMP
          T13C+2
          /SUBTEST LOOP,

3445 4527      LOOP6
3446 5202      JMP
          T13A
          /*****LOOP 6*****

3447 7240      CLA CMA GOOD
3450 3154      DCA
3451 7240      CLA CMA
3452 4475      LCAR
3453 4543      WRITE
3454 4516      CLF
3455 4504      RCRAR
3456 4533      COMPAR
3457 7147      ER13D
          /GD= GOOD CA
          JMP
          T13D+2
          /SUBTEST LOOP,

3460 5251      /VERIFY SKCB, SKTD, SKEF, SKTR OR SOLE DO NOT GENERATE
          /BREAK REQUEST, CHECK VIA WC INCREMENT,
          T13E, JMS I T13EX1
          T11S1
          -5
          T13E1
          CLEAR2
          CLA CMA
          LWCR
          SPDFWD
          T13E1, NOP
          3470 4545      ROCR
          3471 0000      COMPAR
          3472 7000      ER13E
          3473 4503      /I1= FAILING IOT, GD= GOOD WC,
          3474 4533      JMP
          3475 7151      JMS I
          T13E+4
          T13EX2
          /SUBTEST LOOP

3500 4530      LOOP7
3501 5245      JMP
          T13D-2
          /*****LOOP 7*****

3502 5600      JMP I
3503 3122      T13EX1, TEST13
3504 3141      T13EX2, T11EX2
    
```





```

/TEST 14, FULL READ-COMPARE TEST,
/1, VERIFIES!
/1.1 NO READ-COMPARE ERROR WHEN MEM AND DB CONTAIN SAME
/1.2 READ-COMPARE ERROR IS GENERATED WHEN MEM AND DB
/1.3 CA DOES NOT INCREMENT WHEN R=C ERROR EXISTS,
/2, TESTS T14A AND T14B ARE RUN BY A LOCAL EXECUTIVE
WHICH GENERATES DATA FOR MEM AND DB, AND ALSO RUNS THE PORTION
OF THE TEST WHICH SETS UP CA, DB, RDCOMP FUNCTION, THE BREAK
REQUEST, THEN READS MS AND MASKS TO THE R/C BIT,
/3, ALL SUBTEST LOOPS CYCLE ON THE CURRENT DATA,
/4, LOOPS 6-8:
/4.1 LOOP 6 CYCLES ON T14A (NO R=C ERROR)
/4.2 LOOP 7 CYCLES ON T14B (R=C ERROR)
/4.3 LOOP 8 CYCLES ON T14C (CA NO INCREMENT)

```

3600	PAGE	
3600	TEST14, 0	
3601	CLEAR1	
3602	/VERIFY NO READ-COMPARE ERROR WHEN MEM AND DB CONTAIN SAME DATA,	
3603	/COUNT PATTERN USED,	
3604	T14A, DCA GOOD	
3605	JMS T14EX1	
3606	COMPAR	
3607	ER14A	
3610	/GD=GOOD MS MASKED; BD=REAL MS MASKED; OD=DATA IN MEM,	
3611	JMP T14EX2	
4527	JMS T14EX3	
5202	LOOP6	
1021	JMP T14A	/*****LOOP6*****
3154	/VERIFY READ-COMPARE ERROR IS GENERATED WHEN ONE BIT DIFFERS IN DB	
4310	/AND MEM, TAD K2	
4533	DCA GOOD	/VERIFY READ-COMPARE ERROR
7155	JMS T14EX4	/WHEN ONE BIT DIFFERS IN DB
5325	COMPAR	/AND MEM,
4327	ER14B	/DATA IN DB UNDER "DB!",
4530	/GD=GOOD MS MASKED; BD=REAL MS MASKED; OD=DATA IN MEM; DB=REAL DB	
5210	JMP T14EX5	
1047	JMS T14EX6	/SUBTEST LOOP,
4530	LOOP7	
5210	JMP T14B-2	/*****LOOP7*****
1047	/VERIFY CA DOES NOT INCREMENT WHEN READ-COMPARE ERROR EXISTS,	
	T14C, TAD XBUFFP	

```

3624 3154 DCA GOOD
3625 7240 CLA CMA
3626 3051 DCA XBUFF+1
3627 4524 CLEAR2
3630 4501 LDBR
3631 1047 TAD
3632 4475 LCAR
3633 1165 TAD PRGFLD
3634 4477 LCMR
3635 4542 RDCOMP
3636 4535 BREAK
3637 1047 TAD XBUFFP
3640 4475 LCAR
3641 7240 CLA CMA
3642 4501 LDBR
3643 4535 BREAK
3644 4504 RCAR
3645 4533 COMPAR
3646 7157 ER14C
/SET UP FOR NEXT CALLING
/WITH R/C ERROR EXISTING,

T14C1, XBUFFP
/GENERATE R/C ERROR,

/CD=6000 CA, T14C1 /SUBTEST LOOP,
JMP

```

```

3650 1036 TAD K7400
3651 3051 DCA XBUFF+1
3652 3154 DCA GOOD
3653 1262 TAD +7
3654 3360 DCA T14EX8
3655 4524 CLEAR2
3656 1047 TAD XBUFFP
3657 4475 LCAR
3660 1005 TAD K3
3661 5366 JMP T14EX8+6
3662 3663 +1
3663 4533 COMPAR
3664 7161 ER14D
/VERIFY THAT CM10=11 INHIBIT READ=COMPARE CHECKS ON BITS
/0=3 OF DB AND MEM. 7400 IN MEM, 0000 IN DB, THERE SHOULD BE NO
/READ=COMPARE ERROR,
T14D, TAD K7400
DCA XBUFF+1
DCA GOOD
TAD +7
DCA T14EX8
CLEAR2
TAD XBUFFP
LCAR
TAD K3
JMP T14EX8+6
+1
COMPAR
ER14D
/CD=6000 R/C ERROR BIT: BD=REAL R/C ERROR BIT
JMP T14D+5
SUBTEST LOOP,

```

```

3665 5255 JMP
3666 4531 LOOP8
3667 5221 JMP
3670 5600 JMP I TEST14
3671 0000 T14EX1, 0
3672 4347 JMS T14EX7
3673 1156 TAD OLD
3674 3051 DCA XBUFF+1
3675 1156 TAD OLD
3676 3160 DCA TXXTM2
3677 4360 JMS T14EX8
3700 5671 JMP I T14EX1
/ENTER FROM T14A,
/INITIALIZE DATA AND LOOP COUNTERS,
/PUT OLD IN MEM,
/PUT OLD IN LOC USED TO LOAD DB,
/SET UP CA, DB, RDCOMP, BREAK AND
/LOOK AT R/C BIT IN MS THEN GO TO

```

3701	0000	T14EX3, 0	ISZ	OLD	/TEST 14A COMPAR SECTION;
3702	2156		JMP	*4	/ENTER FROM END OF T14A,
3703	5307		ISZ	T14TM1	/UPDATE OLD DATE;
3704	2375		SKP		/IF OLD NOT=0 RUN NEW OLD;
3705	7410		JMP		/IF OLD=0, HAS IT BEEN 0 TWICE?
3706	5701		JMP		/NO,
3707	5273	T14EX4, 0	JMP		/YES, EXIT T14A,
3710	0000		JMS		/NO, RUN T14A AGAIN WITH NEW DATA;
3711	4347		TAD		/ENTER FROM T14B,
3712	1156		DCA		/INITIALIZE DATA AND LOOP COUNTERS;
3713	3051		TAD	XBUFF+1	/PUT OLD IN MEM.
3714	1156		TAD	OLD	
3715	1377		TAD	T14BIT	/COMPLEMENT THE BIT IN OLD
3716	3160		DCA	TXXTM2	/INDICATED BY CURRENT BIT IN
3717	1156		TAD	OLD	/T14BIT AND PUT MODIFIED OLD
3720	0377		AND	T14BIT	/IN LOC USED TO LOAD DB,
3721	7104		CLL	RAL	/COMPLEMENTING THE BIT IS
3722	7041		CIA		/ACTUALLY AN EXCLUSIVE OR FUNCTION.)
3723	1160		TAD	TXXTM2	
3724	3160		DCA	TXXTM2	
3725	4360	T14EX5, 0	JMS		/SET UP CA, DB, RCOMP, BREAK, AND LOOK
3726	5710		JMP		/AT R/C BIT IN MS; THEN GO TO T14B COMPAR,
3727	0000		0		/ENTER FROM END OF T14B,
3730	2376	T14EX6, 0	ISZ		/ALL 12 BITS BEEN COMPLEMENTED
3731	7410		SKP		/ONE AT A TIME?
3732	5337		JMP		
3733	1377		TAD	T14BIT	/NO, MOVE BIT ONE LEFT,
3734	7104		CLL	RAL	
3735	3377		DCA		/GO BACK AND COMPUTE NEW DB DATA,
3736	5314		JMP		/YES, PUT BIT IN POSITION 12,
3737	7001		IAC		
3740	3377		DCA		/OLD + 1 TO OLD, OLD = 0?
3741	2156		ISZ		/YES, HAS IT BEEN 0 TWICE?
3742	5346		JMP		
3743	2375		ISZ		/YES, EXIT T14B,
3744	7410		SKP		/NO, RUN COMPLEMENTING BITS ON OLD;
3745	5727		JMP		/INITIALIZE LOOP COUNTERS AND
3746	5355		JMP		/DATA, SET UP FOR 2 RUNS
3747	0000	T14EX7, 0	0		/FOR OLD (0192 DATA SETS),
3750	1037		TAD		/SET OLD TO 0 FOR A STARTER,
3751	3375		DCA		/SET BIT TO POSITION 12 FOR
3752	3156		DCA		/STARTER
3753	7001		IAC		/SET UP FOR 12 BIT ROTATES
3754	3377		DCA		/FOR USE IN T14B ONLY,
3755	1042		TAD		/EXIT
3756	3376		DCA		/T14A AND T14B TEST POSITION
3757	5747	T14EX8, 0	JMP		/GENERATE "INITIALIZE,"
3760	0000		0		/GET DB DATA AND LOAD DB,
3761	4524		CLEAR2		
3762	1160		TAD		/PUT XBUFF IN CA,
3763	4501		LDBR		
3764	1047		TAD		
3765	4475		L CAR		
	55				

3766 1165  
3767 4477  
3770 4542  
3771 4535  
3772 4505  
3773 0021  
3774 5760  
3775 0000  
3776 0000  
3777 0000

TAD  
LCMR  
RDCOMP  
BREAK  
RMSR  
AND  
JMP I  
K2  
T14EX8  
T14TM1: 0  
T14TM2: 0  
T14BIT: 0

PRGFLO  
/SET RDCOMP FUNCTION,  
/ONE BREAK REQUEST  
/HEAD MS AND MASK TO SEE  
/R/C ERROR BIT ONLY  
/GO TO COMPARE PORTION OF TEST



4055	6714	6714	SRCMR,	6714	/RMSR
4056	5654	JMP I		JMP I	
4057	4347	JMS		JMS	
4060	0000	0		0	
4061	6715	6715		6715	
4062	5660	JMP I		JMP I	
4063	4347	JMS		JMS	
4064	0000	0		0	
4065	6716	6716		6716	
4066	5664	JMP I		JMP I	
4067	4347	JMS		JMS	
4070	0000	0		0	
4071	6717	6717		6717	
4072	5670	JMP I		JMP I	
4073	4347	JMS		JMS	
4074	0000	0		0	
4075	6720	6720		6720	
4076	5674	JMP I		JMP I	
4077	4347	JMS		JMS	
4100	0000	0		0	
4101	6721	6721		6721	
4102	5700	JMP I		JMP I	
4103	2300	ISZ		ISZ	
4104	5700	JMP I		JMP I	
4105	0000	0		0	
4106	6722	6722		6722	
4107	5705	JMP I		JMP I	
4110	2305	ISZ		ISZ	
4111	5705	JMP I		JMP I	
4112	0000	0		0	
4113	6723	6723		6723	
4114	5712	JMP I		JMP I	
4115	2312	ISZ		ISZ	
4116	5712	JMP I		JMP I	
4117	0000	0		0	
4120	6724	6724		6724	
4121	5717	JMP I		JMP I	
4122	2317	ISZ		ISZ	
4123	5717	JMP I		JMP I	
4124	0000	0		0	
4125	6725	6725		6725	
4126	5724	JMP I		JMP I	
4127	4347	JMS		JMS	
4130	0000	0		0	
4131	6712	6712		6712	
4132	5730	JMP I		JMP I	
4133	4347	JMS		JMS	
4134	0000	0		0	
4135	6726	6726		6726	
4136	5734	JMP I		JMP I	
4137	4347	JMS		JMS	
4140	0000	0		0	
4141	6727	6727		6727	
4142	5740	JMP I		JMP I	
4143	4347	JMS		JMS	

/CONTROL JOT;

/SKEF

/SKCB

/SKTD

/SKTR

/CLF

/CLT

/SDLE

/SBRM

SRMSR  
SKIPER

SRCHR  
SKIPER

SRFSR  
SKIPER

SRDBR  
SKIPER

S6720  
SKIPER

SSKEF  
SKIPER

SSKCB  
SKIPER

SSKTD  
SKIPER

SSKTR  
SKIPER

SCLF  
SKIPER

SCLT  
SKIPER

SSDLE  
SKIPER

SSBRM  
SKIPER

```

4144 0000 SODF, 0
4145 6201 SCDFI, /N
4146 5744 JMP I SCDF

SKIPER, 0
4147 0000 DCA ACLOC
4150 3170 TAD M4
4151 1040 TAD SKIPER
4152 1347 TAD SKIPER
4153 3347 DCA SKIPER
4154 7001 IAC
4155 1347 TAD SKIPER
4156 3155 DCA BAD
4157 1555 TAD I BAD
4160 3155 DCA BAD
4161 1747 TAD I SKIPER
4162 3347 DCA SKIPER
4163 1155 TAD BAD
4164 3375 DCA ,+11
4165 4534 ERROR
4166 6703 ER00C

/AN IOT SKIPPED WHICH SHOULD NEVER SKIP,
/BD=FAILING IOT CODE; AC=CONTENTS OF AC AT TIME OF FAILURE;
JMP ,+5
LOOPS ,+3
JMP ACLOC
TAD SKIPER
JMP I ACLOC
TAD 0
JMP ,+6
JMP ,+12

/AN IOT SKIPPED WHICH SHOULD NEVER SKIP,
/SAVE AC AT TIME OF FAILURE,
/GET POINTER TO IOT SUPROUTINE,
/ENTRY AND SAVE,
/GET POINTER TO IOT CODE,
/GET IOT CODE AND SAVE,
/GET RETURN TO MAIN PROGRAM,
/SAVE FOR EXIT,
/PUT IOT IN EXTERNAL SCOPE LOOP,
/GO TO ERRORS,

/AN IOT SKIPPED WHICH SHOULD NEVER SKIP,
/EXECUTE IF SR5=1,
/IF NOT CHECK SR5,
/EXECUTE IF SR5=1,
/NO SCOPE LOOP, EXIT TO CORRECT
/POINT IN MAIN PROGRAM,
/SCOPE LOOP, SET UP AC, THIS
/MAY OR MAY NOT BE CORRECT CONDITION,
/IOT CODE, NO FAILURE, CHECK SR5,
/FAILURE, CALL ERRORS,

```



/EXECUTIVE

/USING A TEST STATUS WORD, "TSTAT", EXEC DETERMINES WHICH TESTS ARE TO BE RUN. EACH BIT IN "TSTAT" REPRESENTS ONE TEST. (BIT N REPRESENTS TEST N+1 IN OCTAL.)  
 /"TSTAT" IS AUTOMATICALLY SET TO 7777 WHEN THE PROGRAM IS /STARTED AT 200, WITH SR0=0. IF SR0=1 AT TEST START TIME, /USER MUST SELECT TESTS WITH MONITOR. MONITOR THEN SETS THE /SELECTED TEST BITS AND ENTERS EXEC AT REX. ALL TESTS /WILL BE RUN THE NUMBER OF TIMES SPECIFIED BY THEIR /RESPECTIVE TALLY CONSTANTS UNLESS QUICK VERIFY IS SET, /WHEN ALL TESTS ARE COMPLETE, EXEC GOES TO MONITOR.

4200	EXEC,	RIF	PAGE	CODE
4200		DCA		/GET INSTRUCTION FIELD;
4201		TAD		/MODIFY HOMING CDF ROUTINE;
4202		DCA I		
4203		CDF		/SET UP INTERRUPT LINKAGE;
4204		TAD		/PUT RMF IN 1;
4205		DCA I		/JMP 1 3 IN 2;
4206		TAD		/INTSEV IN 3;
4207		DCA I		
4210		DCA I		/SET BACK TO CURRENT FIELD;
4211		TAD		/CLEAR ERROR EXIT FLAG;
4212		DCA I		/SET THE TTD SOFT FLAG;
4213		JMS I		
4214		DCA CMA		
4215		DCA		/PRINT TITLE MESSAGE
4216		S&L		/SET TEST IN PROGRESS FLAG
4217		JMP		/FIND OUT HOW MANY MEM FIELDS
4220		JMS I		/GET SR0 AND
4221		MSG1		/SAVE,
4222		CLA CMA		/TELL USER THAT SR0 IS NOW A
4223		DCA		/QUICK VERIFY SWITCH
4224		DCA		/GET OLD SR0
4225		JMS I		/GO DIRECTLY TO MONITOR?
4226		DCA		/YES.
4227		LAS		/NO. SET UP TO RUN ALL TESTS,
4230		DCA		/PUT AC IN TEST STATUS;
4231		DCA		/CLEAR ERROR EXIT FLAG
4232		JMS I		/SET TEST IN PROGRESS
4233		TAD		
4234		SPA		/CLEAR PASS COUNTER
4235		JMP		/SET TO CHECK FOR 12 TESTS
4236		CLA CMA		
4237		DCA		
4240	REX,	DCA		
4241		DCA		
4242		CLA CMA		
4243		DCA		
4244		DCA		
4245		DCA		
4246	EXECCL2,	DCA		

4247	1365	TAD	TSTP	/GET TEST STARTING ADDS, TABLE POINTER
4250	3355	DCA	TSTPP	/GET TEST TALLY TABLE POINTER
4251	1367	TAD	TALP	/SET TEST NUMBER TO '0
4252	3356	DCA	TALPP	/GET TEST STATUS AND PUT IN
4253	3177	DCA	TSTNUM	/TEMP,
4254	1176	TAD	TSTAT	/CHECK FOR A TEST BIT SET
4255	3357	DCA	TSTAEX	/SAVE TEST STATUS ROTATED;
4256	1357	TAD	TSTAEX	/UPDATE POINTERS
4257	7004	RAL	TSTAEX	/UPDATE TEST NUMBER
4260	3357	DCA	TSTPP	/RUN THIS TEST?
4261	2355	ISZ	TSTPP	/NO
4262	2356	ISZ	TALPP	/YES TEST PROGRESS REPORT?
4263	2177	ISZ	TSTNUM	
4264	7420	SNL	TSTNUM	
4265	5315	JMP	NOTSRN	
4266	7604	LAS	K2000	
4267	0033	AND		
4270	7640	SZA	CLA	
4271	5277	JMP	NOTSRP	/NO
4272	1177	TAD	TSTNUM	/YES, GET TEST NUMBER
4273	4463	JMS	I	/INSERT IN MSG AND PRINT
4274	3760	DCA	I	
4275	4461	JMS	I	
4276	7213	MSG2	AMG8EP	
4277	1756	NOTSPR,	TAD	/GET THIS TESTS TALLY AND SAVE
4300	3361	DCA	TALPP	/GET TEST STARTING ADDRESS
4301	1755	TAD	TSTAL	
4302	3362	DCA	TSTPP	
4303	4762	JMS	I	
4304	7604	LAS	EXTEMP	/RUN TEST
4305	0021	AND	EXTEMP	/LOOP ON CURRENT TEST
4306	7640	SZA	CLA	
4307	5303	JMP	EXOMT	
4310	7604	LAS	EXOMT	/YES RUN TEST AGAIN
4311	7710	SPA	CLA	/SR0 SET? (QUICK VERIFY)
4312	5315	JMP	NOTSRN	/NO, GO CHECK TEST TALLY
4313	2361	ISZ	TSTAL	/YES SEE IF ALL TESTS RUN
4314	5303	JMP	EXOMT	/NO DECREASE TALLY, TEST DONE?
4315	2354	ISZ	EXOMT	/NO RUN AGAIN
4316	5256	JMP	EXOMT	/YES 12 TESTS CHECKED?
4317	3177	DCA	EXECL1	/NO CHECK FOR NEXT TEST
4320	7604	LAS	TSTNUM	
4321	0020	AND		/YES, LOOP ON ALL SELECTED TESTS
4322	7650	SNA	CLA	
4323	5336	JMP	NOEXLP	/NO
4324	2346	ISZ	PASCNT	/YES UPDATE PASS COUNTER
4325	1346	TAD	PASCNT	/INSERT IN MSG AND
4326	4463	JMS	I	/PRINT
4327	3763	DCA	I	
4330	1464	TAD	M4P2P	
4331	3764	DCA	I	
4332	4461	JMS	I	
4333	7233	MSG4	AMG8EP	
4334	4465	JMS	I	/BEL

4335	5245	JMP	EXECCL2	
4336	4461	JMS I	AMGBEP	/RUN ALL OVER AGAIN
4337	7220	MS03		/PRINT DONE MESSAGE,
4340	3175	DCA	ACTFLG	/CLEAR TEST IN PROGRFSS,
4341	7240	CLA CMA		
4342	3173	DCA	TYOFLG	
4343	5462	JMP I	MONITP	/GO TO MONITOR
4344	5403	KJMPIS, 5403		
4345	5400	INTSEP, INTSEV		
4346	0000	PASCNT, 0		
4347	6244	KRMF, RMF		
4350	4145	SCDFIP, SCDFI		
4351	0001	P1, 1		
4352	0002	P2, 2		
4353	0003	P3, 3		
4354	0000	EXTAL, 0		
4355	0000	TSTPP, 0		
4356	0000	TALPP, 0		
4357	0000	TSTAEX, 0		
4360	7216	M2P1P, M2P1		
4361	0000	TSTTAL, 0		
4362	0000	EXTEMP, 0		
4363	7237	M4P2P, M4P2		
4364	7236	M4P1P, M4P1		
4365	6562	TSTP, TST=1		
4366	0203	TET1P, TEST01		
4367	6576	TALP, TAL=1		

/MONITOR

/1. ENTERED UNDER ONE OF THE FOLLOWING CONDITIONS:  
 /1.1 PROGRAM STARTED AT 202 WITH SR0=1,  
 /1.2 ERROR OCCURS WITH SR2=0,  
 /1.3 ANY FATAL ERROR OCCURS,  
 /1.4 "ALTMODE" IS STRUCK ON THE KEYBOARD AFTER TEST START,  
 /1.5 SELECTED TESTS ARE RUN TO COMPLETION.

4400	PAGE		
4401	MONIT,	JMS I	/INTERRUPT SYSTEM OFF;
4402		DCA	/SAVE SUBROUTINE ONFO;
4403		JMS I	
4404		MSG8	/PRINT "C";
4405		DCA	/CLEAR TEST STATUS TEMP;
4406		JMS I	/GET KEYBOARD;
4407		-303	
4410		CS	/C STRING;
4411		-324	
4412		TS	/T STRING;
4413		-305	
4414		ES	/E STRING;
4415		-304	
4416		D5	/"D" LOOK AT MEMORY;
4417		0	
4420	CS,	DCA	/"C" OR "EX"; PUT AC IN TEMP;
4421		TAD	/IF NO TEST IN PROGRESS; SYNTAX
4422		SMA CLA	/ERROR;
4423		JMP I	/GET KEYBOARD;
4424		JMS I	/"E";
4425		-215	
4426		+2	
4427		0	
4430		JMS I	/"C" OR "EX";
4431		JMS I	/RESTORE SUBROUTINES;
4432		TAD	/GET TEMP AND PUT IN EXIT
4433		DCA	/GLAG;
4434		JMP I	/EXIT TO INTERRUPT SERVICE;
4435	TS,	JMS I	/T STRING; GET KEYBOARD;
4436		-322	
4437		TR	/"TR"
4440		-301	
4441		TA	/"TA"
4442		1	
4443		+2	
4444		0	
4445		CLL RAL	/GET FIRST NUMBER; SCALE
4446		RTL	/AND SAVE;
4447		DCA	
4450		JMS I	/GET NEXT NUMBER
4451		1	
4452		+2	
4453		2	

4454	1335	TAD	TSTEM1	/ADD IWO NUMBERS AND CONVERT
4455	7041	CIA	TSTEM1	/SUM TO ISZ SENSITIVE COUNTER,
4456	3335	DCA		/DETERMINE TEST BIT,
4457	7120	CLL CML		
4460	7010	RAH		
4461	2335	ISZ	TSTEM1	/SAVE TEST BIT,
4462	5260	JMP	.#2	/EXCLUSIVE OR NEW TEST BIT
4463	3335	DCA	TSTEM1	/WITH OLD TEST STATUS,
4464	1335	TAD	TSTEM1	
4465	0337	AND	TSTATM	
4466	7104	CLL RAL		
4467	7041	CIA		
4470	1335	TAD	TSTEM1	/SAVE NEW TEST STATUS,
4471	1337	TAD	TSTATM	
4472	3337	DCA	TSTATM	/GET INPUT,
4473	4743	JMS I	LISNP	/"TNN"
4474	7563	-215		/"TNNI"
4475	4513	TCR		/GO BACK TO T STRING,
4476	7454	-324		
4477	4435	TS		
4500	0000	0		
4501	7240	CLA CMA	TRACE	/"TR" TRACE REQUESTED,
4502	3171	DCA	LISNP	/SET TRACE FLAG,
4503	4743	JMS I		/GET KEYBOARD,
4504	7475	-303		
4505	4420	CS		/"TRC"
4506	7454	-324		
4507	4435	TS		/"TRT"
4510	7473	-305		/"TRE"
4511	4527	ES		
4512	0000	0		
4513	4523	CLEAR1		/NULL MAGTAPE INTERRUPTS, ION,
4514	4466	JMS I	CRLF	
4515	1337	TAD	TSTATM	/PUT TEST STATUS IN AC,
4516	5736	JMP I	REXP	/GO TO "EXEC."
4517	7240	CLA CMA	TSTATM	/SET ALL TESTS,
4520	3337	DCA	LISNP	/GET KEYBOARD,
4521	4743	JMS I		
4522	7454	-324		/"TAT"
4523	4435	TS		/"TA"
4524	7563	-215		
4525	4513	TCR		/E STRING,
4526	0000	0		/"EX"
4527	4743	JMS I	LISNP	
4530	7450	-330		
4531	4533	.#2		
4532	0000	0		
4533	7240	CLA CMA	CS	/SET ERROR EXIT FLAG,
4534	5220	JMP		/EXIT VIA C STRING,
4535	0000	0		
4536	4240	TSTEM1,		
4537	0000	REXP,		
4540	6000	TSTATM,		
4541	6021	SAVEPP,		
		RESTPP,		
		RESTP,		

4542 4673 GUESP, QUES  
 4543 4677 LISNP, LISN  
 4544 5444 INTOKP, INIOK

4602	PAGE		
4600	TAD	M4	/SET FOR 4 NUMBERS,
4601	DCA	DS2	
4602	JMS	LISN	/LOOK FOR FIELD NUMBER;
4603	1		
4604	.*2		
4605	0		
4606	CLL	RAL	/SCALE AND SAVE.
4607	RTL		
4610	DCA	DS1	
4611	JMS	LISN	/LOOK FOR I
4612	.*2		
4613	0		
4614	TAD	DS1	/COMPUTE GDF INSTRUCTION,
4615	TAD	KCDF	
4616	TAD	DLF	
4617	DCA	DS1	/CLEAR TEMP FOR NEXT 4 INPUTS,
4620	DCA	DS1	/GET KEYBOARD,
4621	JMS	LISN	
4622	1		
4623	.*2		/OCTAL NUMBER,
4624	0		
4625	TAD	DS1	/ADD TEMP,
4626	ISZ	DS2	/4TH NUMBER?
4627	SKP		
4630	JMP	.*5	/NO, MOVE 3 PLACES LEFT,
4631	CLL	RAL	/STORE.
4632	RTL		
4633	DCA	DS1	/GO TO NEXT NUMBER,
4634	JMP	DLF2	/YES, 4TH NUMBER, SAVE ADDRESS,
4635	DCA	DS1	/CHANGE TO REQUESTED DATA FIELD,
4636	DCA	/N	/GET CONTENTS,
4637	TAD	DS1	/CHANGE BACK TO THIS PROGRAM FIELD,
4640	JMS	SCDFP	/EDIT AND INSERT IN
4641	JMS	EDITP	/MESSAGE.
4642	DCA	M11P2P	
4643	TAD	EDTEMP	
4644	DCA	M11P1P	
4645	JMS	AMGBEP	
4646	MSG11		
4647	JMS	LISN	/PRINT MESSAGE,
4650	.*2		
4651	DCR		/D*
4652	.*2		
4653	0		/DLF
4654	TAD	K215	/DLF, DUMP NEXT LOCATION,
4655	JMS	I	
4656	ISZ	TYPEP	
4657	JMP	DS1	
4660	JMP	DLF	
4661	TAD	DLF	

4662	1023	TAD	K10	
4663	3236	DCA	DLF	
4664	5236	JMP	DLF	
4665	4466	JMS I	CRLEP	/D*. LOOK FOR NEW COMMAND.
4666	5676	JMP I	MONP	
4667	0000	0		
4670	2000	0		
4671	7310	M11P1,	M11P1	/TYPE "P*", RING BELL.
4672	7311	M11P2,	M11P2	/RESTART MONITOR.
4673	4461	JMS I	AMG8P	/GET KEYBOARD AND CHECK SYNTAX.
4674	7241	MSG6		
4675	5676	JMP I	MONP	
4676	4403	MONIT+3		
4677	0000	0		
4700	6031	KSF		
4701	5300	JMP	"=1	
4702	6036	KR8		
4703	3346	DCA	LISNT1	/SAVE INPUT
4704	1346	TAD	LISNT1	/ECHO.
4705	4470	JMS I	TYPEP	/DO NOT CHECK "SPACE" IN SYNTAX.
4706	1346	TAD	LISNT1	
4707	1347	TAD	M240	
4710	7650	SNA	CLA	
4711	5300	JMP	LISN+1	
4712	1677	TAD I	LISN	/GET COMPARTOR.
4713	7450	SNA		
4714	5273	JMP	QUES	/IF 0, SYNTAX ERROR.
4715	7500	SMA		
4716	5327	JMP	LISNUM	/IF >0, LOOK FOR OCTAL NUMBER.
4717	1346	TAD	LISNT1	/MATCH?
4720	7640	SZA	CLA	
4721	5324	JMP	LISN2	/NO.
4722	3346	DCA	LISNT1	/YES, FAKE TEMP CLEAR AND
4723	5340	JMP	LISN3	/EXIT.
4724	2277	ISZ	LISN	/UPDATE CALL POINTERS.
4725	2277	ISZ	LISN	
4726	5312	JMP	LISN1	/GET NEXT COMPARTOR.
4727	7200	CLA	LISNT1	/OCTAL NUMBER REQUIRED.
4730	1346	TAD	M260	
4731	1044	TAD		
4732	7710	SPA	CLA	
4733	5324	JMP	LISN2	/NOT AN OCTAL NUMBER
4734	1346	TAD	LISNT1	/YES, UPDATE CALL.
4735	1045	TAD	M270	/GET RETURN ADDRESS.
4736	7700	SMA	CLA	
4737	5324	JMP	LISN2	
4740	2277	ISZ	LISN	
4741	1677	TAD I	LISN	
4742	3277	DCA	LISNT1	/GET 0 OR OCTAL NUMBER CODE
4743	1346	TAD	K7	/MASK TO LOW ORDER 3 BITS.
4744	2022	AND		
4745	5677	JMP I	LISN	/EXIT.
4746	2000	0		
4747	7540	M240,		"=240

/ERROR SERVICE ROUTINE.  
/DECODES ERROR STATUS WORDS AND CARRIES OUT THE INDICATED

/ACTION,  
/SAMPLE ERROR TABLE ENTRY;  
/ERR05C, STAT1 ERROR STATUS WORD;  
/ STAT2 PRINT STATUS WORD;

/STATUS WORD BIT ASSIGNMENTS:  
/STAT1 BIT STATE INDICATION  
/0: NON-FATAL ERROR  
/1: FATAL ERROR  
/2: NO EXIT ON "EX"  
/3: EXIT ON "EX"  
/4-5: TEST NUMBER (HEXADECIMAL)  
/6-11: ANSCII FOR SUBTEST LETTER (6 BIT);

/STAT2 BIT PRINT OUT THE FOLLOWING WHEN SET,  
/0: IOT1 (I1)  
/1: IOT2 (I2)  
/2: GOOD (GD)  
/3: BAD (BD)  
/4: OLD (OD)  
/5: WORD COUNT (WC)  
/6: CURRENT ADDRESS (CA)  
/7: COMMAND REGISTER (CM)  
/8: FUNCTION AND STATUS 1 (FS)  
/9: MAIN STATUS (MS)  
/10: DATA BUFFER (DB)  
/11: CONTENTS OF AC FOR IOT SKIP ERROR (AC)

/"ERRORS MAY BE ENTERED IN ONE OF TWO WAYS!  
/1, COMPAR GOES TO "ERRORS" IF "GOOD" NOT EQUAL TO "BAD",  
/ ADDR ADDRESS OF ERROR STATUS WORDS,  
/2, ERROR MAY BE USED BY FATAL ERRORS ONLY,  
/ ADDR

5000	ERRORS:	0	PAGE		/ENTER HERE WITH "ERROR"
5001		CLA			/ENTER HERE FROM "COMP" (PC PRESTORED)
5002		TAD			
5003		DCA I	K4352		/GET ERROR TABLE ADDRESS AND
5004		TAD I	ERMSGP		/SAVE,
5005		DCA	ERRPR		/NON-FATAL ERROR?
5006		TAD I	ERRPTR		/NO, FATAL ERROR, CHECK PRINTOUT,
5007		SPA CLA	FATERR		/NON-FATAL ERROR, CHECK PRINTOUT,
5010		JMP	OPRNT		/CHECK RETURN TO MONITOR,
5011		JMS	GHALTP		/MOVE POINTER TO SUBTEST LOOP,
5012		JMS I	ERRORS		/CHECK SUBTEST LOOP,
5013		ISE			/EXIT TO LOOP JUMP,
5014		LOOPS			/"EX" PENDING?
5015		JMP I	ERRORS		
5016		TAD	EXITFL		
5017		SMA CLA			



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5020 5227 JMP ,+7 ERRPTR
5021 1745 TAD I ERRPTR
5022 7004 RAL
5023 7700 SMA CLA
5024 5227 JMP ,+3 ERRORS
5025 2200 ISZ ERRORS
5026 3174 DCA EXITFL
5027 2200 ISZ ERRORS
5030 5600 JMP I ERRORS

5031 4461 FATEHR, JMS I AMGSEP
5032 7243 MSG7
5033 4465 JMS I BELL
5034 1372 TAD K605
5035 4253 JMS PRNT
5036 3175 DCA ACTFLG
5037 5462 JMP I MONITP
5040 0000 OPRNT, 0
5041 7604 LAS
5042 0032 AND
5043 7640 SZA CLA
5044 5640 JMP I OPRNT
5045 1371 TAD K522
5046 4253 JMS PRNT
5047 7240 CLA CMA
5050 1345 TAD
5051 3345 DCA I ERRPTR
5052 5640 JMP I OPRNT

5053 0000 PRNT, 0
5054 3746 DCA I ERMP1P
5055 1745 TAD I ERRPTR
5056 4522 BSW
5057 0365 AND
5060 4463 JMS I EDITP
5061 3747 DCA I ERMP2P
5062 1745 TAD I ERRPTR
5063 4522 BSW
5064 0373 AND
5065 1025 TAD K7700
5066 3750 TAD K40
5067 1200 DCA I ERMP3P
5070 4463 TAD I ERRORS
5071 3753 JMS I EDITP
5072 1464 DCA I ERMP5P
5073 3752 TAD I EDTEMP
5074 2345 DCA I ERMP4P
5075 1041 ISZ M5
5076 3354 DCA PRCNT1
5077 1042 TAD M14
5100 3355 DCA PRCNT2
5101 1362 TAD SYMPTP
5102 3357 DCA SYMPTP
5103 1356 TAD SYMPTP
5104 3360 DCA PRMPT

/NO,
/YES, DOES THIS ERROR REQUIRE
/"EX." SERVICE?

/NO, LEAVE EXITFL SET,
/YES, MOVE POINTER TO RECYCLE JUMP,
/CLEAR "EX." FLAG,
/MOVE POINTER ONE MORE POSITION,
/EXIT,

/FATAL ERROR, PRINT MSG.

/PUT "FE" IN AC,
/PRINT ERROR MESSAGE,
/CLEAR TEST IN PROGRESS FLAG,
/GO TO MONITOR,
/PRINTOUT?

/NO, EXIT,
/YES, PUT "ER" IN AC AND
/GENERATE ERROR PRINTOUT,
/BACK BIAS ERROR TABLE POINTER,
/TO POINT TO STAT1,
/EXIT.

/GENERATE ERROR MESSAGE,
/SAVE "FE" OR "ER",
/GET TEST NUMBER FROM
/STAT1 AND PUT IN ERROR
/MESSAGE,

/GET SUBTEST LETTER FROM STAT1
/AND PUT IN ERROR MESSAGE
/AFTER APPENDING "SPACE" CODE,

/EDIT RETURN JUMP FOR "PC" PRINTOUT,

/MOVE ERROR TABLE POINTER TO STAT2,
/SET COUNTER FOR 5 SYMBOLS
/BEFORE CR=LF,
/SET COUNTER FOR 12 SYMBOLS
/MAXIMUM,
/GET SYMBOL ROUTINE TABLE
/POINTER AND PUT IN TEMP,
/GET ERROR MESSAGE INSERT
/POINTER,

```

5105	STL RAR		
5106	DCA	PRNTK	/INITIALIZE TO SPACE
5107	LAS		/COMPLETE DUMP?
5110	AND	K230	
5111	SNA CLA		
5112	JMP	,+3	/YES, FAKE STAT2 FULL.
5113	CLA CMA		
5114	SKP		
5115	TAD I	ERRPTR	/GET STAT2 AND PUT IN TEMP.
5116	DCA	WHAT	/BIT BY BIT INSPECT STAT2 FOR
5117	TAD	WHAT	/SYMBOL PRINTOUTS, AS A
5120	RAL		/BIT IS SENSED SFT, GO TO
5121	DCA	WHAT	/SYMBOL ROUTINE AND INSERT
5122	SNL		/SYMBOL AND DATA IN
5123	JMP	PRNTCK	
5124	TAD I	SYMBOL	
5125	DCA	SYMADR	
5126	JMS I	SYMADR	
5127	ISZ	PRCNT1	
5130	JMP	,+3	/5 SYMBOLS PRINTED?
5131	TAD	K4300	/YES, INSERT 1 CR-LF IN SPACING.
5132	SKP		//NO, INSERT 1 SPACE.
5133	STL RAR		
5134	DCA	PRNTK	/UPDATE SYMBOL TABLE POINTER.
5135	ISZ	SYMBOL	/11 SYMBOLS CHECKED?
5136	ISZ	PRCNT2	/NO, CHECK AGAIN.
5137	JMP	PRNTLP	/YES, PUT CR-LF AND END
5140	TAD	K4300	/MSG CODE IN ERROR MSG;
5141	DCA I	PRMPT	/PRINT ERROR MESSAGE.
5142	JMS I	AMG8EP	
5143	ERMSG		
5144	JMP I	PRNT	/EXIT
5145	0		/TEMP FOR STATUS WORD POINTER;
5146	ERMP1		/ERROR MESSAGE POINTERS; ERROR TYPE,
5147	ERMP2		/I TEST NUMBER
5150	ERMP3		/I SUBTEST LETTER=SPACE.
5151	0		/HIGH ORDER PC.
5152	ERMP4		/I LOW ORDER PC.
5153	ERMP5		/SYMBOL POINTER.
5154	0		/5 SYMBOL COUNTER.
5155	0		/12 SYMBOL COUNTER.
5156	ERMP5+1		/SYMBOL TABLE POINTER, START POINTER,
5157	0		/ERROR MESSAGE INSERT TEMP.
5160	0		/SYMBOL TABLE POINTER TEMP.
5161	0		/ERROR MESSAGE INSERT POINTER,
5162	6663	SYMPT	/SPACING CONSTANT.
5163	0		/TEMP FOR STAT2 ROTATES.
5164	0		/POINTER TO RETURN TO MONITOR CHECK.
5165	0		
5166	0		
5167	0		
5170	0		



5260	4575	RMSR		/GET MS
5261	4330	JMS	SYMSEV	
5262	2315	2315		/"MS"
5263	5657	JMP I	MS	
5264	0000	0		
5265	4510	RDBR		/GET DB
5266	4330	JMS	SYMSEV	
5267	0204	0204		/"DB"
5270	5664	JMP I	DB	
5271	0000	0		
5272	1170	TAD	ACLOC	
5273	4330	JMS	SYMSEV	
5274	0301	0301		
5275	5671	JMP I	AC	
5276	0000	0		
5277	1703	TAD I	IOT1P	
5300	4312	JMS	I112	
5301	6111	6111		
5302	5676	JMP I	I1	
5303	0566	IOT1P,		
5304	0000	I2,		
5305	1711	TAD I	IOT2P	
5306	4312	JMS	I112	
5307	6211	6211		
5310	5704	JMP I	I2	
5311	0571	IOT2P,		
5312	0000	I112,		
5313	0370	AND	K377	
5314	1366	TAD	K1000	
5315	1316	DCA	.+1	
5316	0000	0		
5317	7001	IAC		
5320	3316	DCA	.+2	
5321	1712	TAD I	I112	
5322	3325	DCA	.+3	
5323	1716	TAD I	.+5	
5324	4330	JMS	SYMSEV	
5325	0000	0		
5326	2312	ISZ	I112	
5327	5712	JMP I	I112	
5330	0000	SYMSEV,		
5331	3365	DCA	SYMTEM	
5332	1763	TAD I	ERMPP	
5333	3364	DCA	ERMSYM	
5334	1730	TAD I	SYMSEV	
5335	2377	AND	(77	
5336	1776	TAD I	(PRNTK	
5337	3764	DCA I	ERMSYM	
5340	2364	ISZ	ERMSYM	
5341	1730	TAD I	SYMSEV	
5342	0375	AND	(7700	
5343	1357	TAD	K72	
5344	3764	DCA I	ERMSYM	

/ENTER FROM I1 OR I2,  
 /CONVERT INST TO TAD POINTER,  
 /INTO TAD POINTER,  
 /STORE TAD POINTER HERE,  
 /POINTER +1 WHICH IS ADDRESS  
 /OF IOT CODE,  
 /GET SYMBOL CODE,  
 /PUT IN CALL TO SYMSEV;  
 /GET IOT CODE,  
 /GO TO SYMBOL SERVICE,  
 /SYMBOL CODE GOES HERE,  
 /UPDATE RETURN TO I1 OR I2,  
 /RETURN,  
 /COMMON SYMBOL ROUTINE SERVICE,  
 /SAVE DATA,  
 /GET ERROR MESSAGE CURRENT POINTER,  
 /PUT ON THIS PAGE OF MEMORY,

5345	2364	ISZ	ERMSYM	/DATA,
5346	1365	TAD	SYNTEM	/GET DATA,
5347	4463	JMS I	EDITP	/EDIT,
5350	3365	DCA	SYNTEM	/SAVE LOW ORDER,
5351	1464	TAD I	ERTEMP	/GET HIGH ORDER AND INSERT,
5352	3764	DCA I	ERMSYM	/UPDATE POINTER TO LOW ORDER
5353	2364	ISZ	ERMSYM	/DATA, GET LOW ORDER AND INSERT,
5354	1365	TAD	SYNTEM	
5355	3764	DCA I	ERMSYM	/UPDAT POINTER TO SPACING,
5356	2364	ISZ	ERMSYM	/PUT CURRENT POINTER ON
5357	1364	TAD	ERMSYM	/"ERRORS" MEMORY PAGE,
5360	3763	DCA I	ERMPP	/UPDATE RETURN,
5361	2330	ISZ	SYNSEV	/EXIT,
5362	5730	JMP I	SYNSEV	
5363	5160	PRMPT		
5364	0000	ERMSYM, 0		
5365	0000	SYNTEM, 0		
5366	1000	K1000, 1000		
5367	0072	K72, 72		
5370	0377	K377, 377		

/GENERAL INTERRUPT HANDLER,

/CHECKS FOR TIO AND TTI INTERRUPTS AND SERVICES SAME,  
 /CHECKS APPLICABILITY OF EF AND MTF INTERRUPTS AND  
 /GOES TO SPECIFIED EF OR MTF INTERRUPT HANDLER IF  
 /SPECIFIED IN INTEF AND INTMTF,

ADDRESS	INSTRUCTION	OPERAND	COMMENT
5375			
7700			
5376			
5161			
5377			
2077			
5400			
5420	INTSEV,	OCA	SAVEAC
3162			/SAVE AC AND LINK,
5401		RAR	
7010			
5402		DCA	SAVEL
3163			
5403		CDF	00
6201		TAD I	P0
5404		DCA	0
1662		JMS I	SCDFP
5405			
3000		TSF	,+5
5406	INTAGN,	JMP	
4457		TCF	
5407		CLA	CMA
6041		DCA	
5410		JMP	
5215		KSF	
5411		JMP	
6042		KRB	
5412		TAD	M233
7240		SNA	
5413		JMP I	MONITP
3173		TAD	M142
5414		SNA	
5244		JMP I	MONITP
5415		TAD	M1
6031		SNA	CLA
5416		JMP I	MONITP
5232		TAD	
5417		SNA	CLA
6036		JMP	
5420		TAD	
1257		SNA	CLA
5421		JMP I	MONITP
7450		TAD	
5422		SNA	CLA
5462		JMP I	MONITP
5423		TAD	
1260		SNA	CLA
5424		JMP I	MONITP
7450		TAD	
5425		SNA	CLA
5462		JMP I	MONITP
5426		TAD	
1261		SNA	CLA
5427		JMP I	MONITP
7650		TAD	
5430		SNA	CLA
5462		JMP I	MONITP
5431		TAD	
5244		SNA	CLA
5432	INTNGA,	TAD	CLA
1164		JMP I	INTEF
5433		SNA	CLA
7640		JMP I	INTEF
5434		TAD	
5564		SNA	CLA
5435		JMP I	INTEF
1166		TAD	
5436		SNA	CLA
7640		JMP I	INTMTF
5437		TAD	
5566		DCA	
5440		ERROR	
1000		FE00A	
5441			
3155			
5442			
4534			
5443			
6677			
5444			
7300			

/ILLEGAL INTERRUPT. THE INTERRUPT CAUSE MAY BE THE TMP-E OR ANY  
 /DEVICE EXCEPT THE TTY. THIS ERROR IS FATAL AND KILLS ALL TEST  
 /SELECTIONS.  
 /BD=CONTENTS OF LOCATION 0; ALL OTHERS ARE CURRENT VALUES AND  
 /MAY NOT APPLY.  
 INTOK, CLA CLL

/NO STORE AC AND LINK

```

5445 1163 TAD SAVEI /AND EXIT;
5446 7004 RAL
5447 1172 TAD INTELG
5450 7640 SZA CLA
5451 5254 JMP +3
5452 1162 TAD SAVEAC
5453 5400 JMP I 0
5454 1162 TAD SAVEAC
5455 6001 ICON
5456 5400 JMP I 0
5457 7545 M233, -233
5460 7636 M142, -142
5461 7777 M1, +1
5462 0000 P0, 0

```

```

/EF AND MTF INTERRUPT SERVICE ROUTINES SPECIFIER,
/ENTER WITH
/JMS I INSRSP
/ADDRESS OF EF SERVICE PUT IN INTEF PUT IN INTMTF
/ADDRESS OF MTF SERVICE PUT IN INTMTF

```

```

INSRS, 0
CLA CLL CMA /SET ACTIVE FLAG;
DCA ACTFLG
TAD I INSRSP
DCA INTEF
ISZ INSRSP
TAD I INSRSP
DCA INTMTF
TAD INTEF
TAD INTMTF
SZA CLA INSRSP
JMP GOOD
DCA GOOD
LCMR
RCMR
COMPAR
ER00B
/CM SHOULD CONTAIN 0000 BUT DOES NOT,
/GD=GOOD CMT,
JMP +4
INSRSF, ISZ INSRSP
JMP I INSRSP

```

```

5463 0000
5464 7340
5465 3175
5466 1663
5467 3164
5470 2263
5471 1663
5472 3166
5473 1164
5474 1166
5475 7640
5476 5305
5477 3154
5500 4477
5501 4506
5502 4533
5503 6701

```

```

5504 5300
5505 2263
5506 5663

```

/EXECUTIVE ADDENDUM,  
EXECFX, 0 /ENQUIRE ABOUT EXISTING  
MSG10 /EXTENDED MEMORY FIELDS  
KSF /FOR THE CA MEMORY REFERENCE  
M260 /TEST,

5507	0000		
5510	4461	JMS I	AMGSEP
5511	7273	MSG10	
5512	6031	KSF	
5513	5312	JMP	M260
5514	6036	KRB	
5515	3157	DCA	TXXTM1
5516	1157	TAD	TXXTM1
5517	4470	JMS I	TYPEP
5520	1157	TAD	TXXTM1
5521	1044	TAD	M260
5522	7710	SPA	CLA
5523	5310	JMP	EXECFX+1
5524	1157	TAD	TXXTM1
5525	1045	TAD	M270
5526	7700	SMA	CLA
5527	5310	JMP	EXECFX+1
5530	1157	TAD	TXXTM1
5531	0022	AND	K7
5532	7001	IAC	
5533	7041	CIA	
5534	3167	DCA	MEMFLD
5535	4466	JMS I	ORLFP
5536	5707	JMP I	EXECFX



```

/COMPARE AND ERROR DETECTION ROUTINE,
/ENTER WITH "BAD" IN AC,
/USE THE FOLLOWING CALLING SEQUENCE,
/COMPAR CALL COMPARE,
/ERNX ADDRESS OF ERROR, (ERROR CODE),
/JMP LOOP JUMP
/.....CONTINUATION TEST CONTINUE,

/COMPARE DOES THE FOLLOWING:
/1, COMPARE "GOOD" AND "BAD",
/2, IF NOT EQUAL APPEARS TO GO TO ERRORS FROM POINT IN TEST
/ WHERE ERROR OCCURRED,
/3, IF EQUAL, CHECKS CURRENT SUBTEST LOOP,

```

5600	COMP,	PAGE	
0000		0	
5601		DCA	BAD
5602		TAD	GOOD
5603		CIA	
5604		TAD	BAD
5605		SNA CLA	
5606		JMP	+4
5607		TAD	COMP
5610		DCA I	ERRORP
5611		JMP I	ERR1P
5612		TAD	TRACE
5613		SMA CLA	COMP
5614		JMP	
5615		LAS	
5616		AND	K400
5617		SZA CLA	
5620		JMP	COMP
5621		TAD	COMP
5622		DCA I	ERRORP
5623		TAD I	COMP
5624		DCA I	TRPP
5625		TAD	K4324
5626		DCA I	ERMSSG
5627		TAD	K2252
5630		JMS I	PRNTP
5631	COMP,	ISZ	COMP
5632		LOOP5	
5633		SKP	
5634		ISZ	COMP
5635		JMP I	COMP
5636		ERRORS+1	
5637	ERR1P,	PRNT	
5640	TRPP,	FRAPTR	
5641	K2252,	2252	
5642	K4324,	4324	

```

/SAVE AC IN BAD,
/COMPARE GOOD AND BAD,

/ERROR, SET UP AND GO TO
/ERRORS AND DON'T
/RETURN HERE,
/TRACE REQUESTED?

/YES, TRACE INHIBITED?

/NO, GET ERROR POINTER
/AND PUT IN ERRORS,
/GET STAT1 ADDRESS AND
/PUT IN PRNT,

/FAKE AN ERROR PRINTOUT,
/UPDATE RETURN JUMP TO SUBTEST LOOP JUMP,
/SUBTEST LOOP?

/NO, UPDATE ONE MORE,
/EXIT,

```

/SAVE AND RESTORE COMMON SUBROUTINE INFORMATION ROUTINES,  
 /LOCATIONS TYPE, TYPTM, CRLF, AMGR, AMGR1, AMGR2,  
 /AMGR3, EDIT, EDTEM, EDTEM1, EDTEM2, EDTEM3, ETC. MUST BE SAVED AND RESTORED  
 /SINCE MONITOR MAY INTERRUPT THESE ROUTINES  
 /AND USE THEM ITSELF.

6000	SAVEP,	0200	CLA	
6001		7200	TAD	M16
6002		1242	DCA	SAVEP1
6003		3243	TAD	SRPTS
6004		1266	DCA	SAVEP2
6005		3244	TAD	SRHOLD
6006		1247	DCA	SAVEP3
6007		3245	TAD	SAVEP2
6010		2244	ISE	SAVEP3
6011		2245	TAD I	SAVEP2
6012		1644	DCA	SAVEP4
6013		3246	TAD I	SAVEP4
6014		1646	DCA I	SAVEP3
6015		3645	ISE	SAVEP1
6016		2243	JMP	107
6017		5210	JMP I	SAVEP
6020		5600		
6021	RESTP,	0000	CLA	
6022		7200	TAD	M16
6023		1242	DCA	SAVEP1
6024		3243	TAD	SRHOLD
6025		1247	DCA	SAVEP2
6026		3244	TAD	SRPTS
6027		1266	DCA	SAVEP3
6030		3245	ISE	SAVEP2
6031		2244	ISE	SAVEP3
6032		2245	TAD I	SAVEP3
6033		1645	DCA	SAVEP4
6034		3246	TAD I	SAVEP4
6035		1644	DCA I	SAVEP2
6036		3646	DCA I	SAVEP4
6037		2243	ISE	SAVEP1
6040		5231	JMP	107
6041		5621	JMP I	RESTP
6042		7762		
6043	M16,	0000	-16	
6044	SAVEP1,	0000		
6045	SAVEP2,	0000		
6046	SAVEP3,	0000		
6047	SAVEP4,	0000		
6047	SRHOLD,	0047		
6050		0000		
6051		0000		
6052		0000		
6053		0000		
6054		0000		
6055		0000		
6056		0000		
6057		0000		

6060	3000	0
6061	3000	0
6062	3000	0
6063	3000	0
6064	3000	0
6065	3000	0
6066	6066	0
6067	5270	0
6070	6244	0
6071	6261	0
6072	6200	0
6073	6237	0
6074	6214	0
6075	6240	0
6076	6301	0
6077	6326	0
6100	6327	0
6101	6330	0
6102	6331	0
6103	6547	0
6104	6561	0

SRPTS:

CRLE  
TYPE  
TYPIEM  
AMG8E  
AMG8E1  
AMG8E2  
AMG8E3  
EDIT  
EDTEM  
EDTEM1  
EDTEM2  
EDTEM3  
SRBSW  
SRBSWT

/LOOP SWITCH SENSORS.  
LAS5,    0    LAS  
          AND    K127  
          SNA CLA  
          ISZ    LAS5  
          JMP I   LAS5  
LAS6,    0    LAS  
          AND    K40  
          SNA CLA  
          ISZ    LAS6  
          JMP I   LAS6  
LAS7,    0    LAS  
          AND    K20  
          SNA CLA  
          ISZ    LAS7  
          JMP I   LAS7  
LAS8,    0    LAS  
          AND    K10  
          SNA CLA  
          ISZ    LAS8  
          JMP I   LAS8  
LAS9,    0    LAS  
          AND    K4  
          SNA CLA  
          ISZ    LAS9  
          JMP I   LAS9  
K4,       4

6105 0000  
6106 7604  
6107 0027  
6110 7650  
6111 2305  
6112 5705  
6113 0000  
6114 7604  
6115 0025  
6116 7650  
6117 2313  
6120 5713  
6121 0000  
6122 7604  
6123 0024  
6124 7650  
6125 2321  
6126 5721  
6127 0000  
6130 7604  
6131 0023  
6132 7650  
6133 2327  
6134 5727  
6135 0000  
6136 7604  
6137 0343  
6140 7650  
6141 2335  
6142 5735  
6143 0004

/"LOOP6"

/"LOOP7"

/"LOOP8"

/"LOOP9"

/PP08-E PACKED ANSCII MESSAGE GENERATOR.  
/ENTERED WITH:  
/JMS AMG8E  
/MSGNP (MESSAGE POINTER)  
/

/00 IS TERMINATOR, 43 (#) IS CARRIAGE RETURN, LINEFEED,  
/THE FOLLOWING CHARACTERS ARE NOT ALLOWED: @ WHICH GIVES  
/TERMINATOR CODE; # WHICH RESULTS IN CRLF; AND 204-207,  
/212, 215, 375, 377, 233,  
/

/EXITS WITH AC CLEAR.  
PAGE

6200	AMG8E, 0	CLA CLL	
6201	7300	TAD I	AMG8E
6202	1600	DCA	AMG8E1
6203	3237	ISZ	AMG8E
6204	2200	TAD I	AMG8E1
6205	1637	BSW	
6206	4522	JMS	AMG8E2
6207	4214	TAD I	AMG8E1
6210	1637	JMS	AMG8E2
6211	4214	ISZ	AMG8E1
6212	2237	JMP	,=6
6213	5205	AMG8E2, 0	AND
6214	0000		K77
6215	0241	SNA	
6216	7450	JMP I	AMG8E
6217	5600	DCA	AMG8E3
6220	3240	TAD	AMG8E3
6221	1240	TAD	M43
6222	1243	SZA CLA	
6223	7640	JMP	,*3
6224	5227	JMS I	CRLFPP
6225	4466	JMP I	AMG8E2
6226	5614	TAD	AMG8E3
6227	1240	TAD	M40
6230	1242	SPA CLA	
6231	7710	TAD	K100
6232	1027	TAD	K200
6233	1030	TAD	AMG8E3
6234	1240	JMS I	TYPEP
6235	4470	JMP I	AMG8E2
6236	5614	AMG8E1, 0	
6237	3000	AMG8E3, 0	
6240	0000	K77, 77	
6241	0077	M40, -40	
6242	7740	M43, -43	
6243	7735		

/TYPE SUBROUTINE,  
/ENTER WITH ANSCII IN AC, EXIT WITH AC CLEAR  
TYPE, 0 DCA TYPTEM /SAVE ANSCII,

6244 0022  
6245 3261

6246 6041 TSF  
 6247 7410 SKP  
 6250 5254 JMP  
 6251 1173 TAD  
 6252 7700 SMA CLA  
 6253 5246 JMP  
 6254 3173 DCA  
 6255 1261 TAD  
 6256 6046 TFS  
 6257 7200 CLA  
 6260 5644 JMP I  
 6261 0000 TYPEM, 0

/TYPE CHARACTER IF REAL TIO FLAG  
 /IS SET OR SOFTWARE FLAG SET,

/RING ITY BELL,

6262 0000 BELL, 0  
 6263 7200 CLA  
 6264 1267 TAD  
 6265 4470 JMS I  
 6266 5662 JMP I  
 6267 0207 K207, 207

/CARRIAGE RETURN-LINE FEED,  
 CRLF, 0

6270 0000 CLA  
 6271 7200 TAD  
 6272 1031 JMS I  
 6273 4470 TAD  
 6274 1300 JMS I  
 6275 4470 JMS I  
 6276 4470 JMP I  
 6277 5670 JMP I  
 6300 0212 K212, 212

/EDIT ROUTINE,  
 /CONVERTS A 4 DIGIT OCTAL NUMBER IN AC TO 2 PACKED ANSCII  
 /WORDS, EXITS WITH LEAST SIGNIFICANT PORTION IN AC  
 /AND MOST SIGNIFICANT IN "EDTEM",

6301 0000 EDIT, 0  
 6302 3327 DCA EDTEM1  
 6303 1327 TAD EDTEM1  
 6304 4522 BSW  
 6305 4312 JMS EDIT1  
 6306 3326 DCA EDTEM  
 6307 1327 TAD EDTEM1  
 6310 4312 JMS EDIT1  
 6311 5701 JMP I EDIT  
 6312 0000 EDIT1, 0  
 6313 3330 DCA EDTEM2  
 6314 1330 TAD EDTEM2  
 6315 0022 AND K7  
 6316 3331 DCA EDTEM3  
 6317 1330 TAD EDTEM2  
 6320 7006 RTL  
 6321 7004 HAL  
 6322 0333 AND K700

6323	1331		TAD	EDTEM3
6324	1332		TAD	K6060
6325	5712		JMP 1	EDIT1
6326	2000			
6327	2000		EDTEM, 0	
6330	2000		EDTEM1, 0	
6331	2000		EDTEM2, 0	
6332	6060		EDTEM3, 0	
6333	0700		K6060, 6060	
			K700, 700	

/ROUTINES TO CLEAR ALL FLAGS SAFELY.

6400									
6400	CLR1,	PAGE							
6401		JMS I	INRSP						
6402									
6403									
6404		JMS	CLR2						
6405	CLR2,	JMP I	CLR1						
6406									
6407		JMS	CLR3						
6408		CLA CMA	INTFLG						
6409		DCA							
6410		ION							
6411		JMP I	CLR2						
6412									
6413									
6414	CLR3,								
6415		CLA CLL							
6416		TAD	TTOFLG						
6417		SZA	CLA						
6418		JMP	,+3						
6419		TSE							
6420		JMP	,+4						
6421		CLA CMA	CLL						
6422		DCA	TTOFLG						
6423		IOF							
6424		LDBR							
6425		CLT							
6426		DCA	FUNCT						
6427		JMP I	CLR3						
6428									
6429									
6430									
6431									
6432									
6433									

/SINCE "INITIALIZE WILL NOT CLEAR OB,  
/ZERO FR TEMP,



/FUNCTION SUBROUTINES.

/TO ENABLE RECALL OF BIT 6 IN FR, EACH TIME THE FR IS LOADED  
 /IN THE FUNC ROUTINE, THE FR CONSTANT LOADED IS SAVED IN CORE, IF INITIALIZE  
 /IS GENERATED, THIS CORE LOCATION IS SERVED.

6432	0000	OFFLINE, 0	/OFF LINE, (OFFLIN)
6433	4311	JMS	FUNC
6434	0000	0	
6435	5632	JMP I	OFFLINE
6436	0000	0	/REWIND, (REWIND)
6437	4311	JMS	FUNC
6440	1000	1000	
6441	5636	JMP I	REWIND
6442	0000	0	/READ, (READ)
6443	4311	JMS	FUNC
6444	2000	2000	
6445	5642	JMP I	READ
6446	0000	0	/HEAD COMPARE (RDCOMP)
6447	4311	JMS	FUNC
6450	3000	3000	
6451	5646	JMP I	RDCOMP
6452	0000	0	/WRITE, (WRITE)
6453	4311	JMS	FUNC
6454	4000	4000	
6455	5652	JMP I	WRIT
6456	0000	0	/WRITE EOF, (WEOF)
6457	4311	JMS	FUNC
6460	5000	5000	
6461	5656	JMP I	EOFW
6462	2000	2000	/SPACE FORWARD (SPCFWD)
6463	4311	JMS	FUNC
6464	6000	6000	
6465	5662	JMP I	SPFORD
6466	0000	0	/SPACE REVERSE, (SPCREV)
6467	4311	JMS	FUNC
6470	7000	7000	
6471	5666	JMP I	SPREV
6472	0000	0	/SET EXTENDED GAP BIT IN CONSTANT / (EXTGAP)
6473	7200	CLA	
6474	1032	TAD	K400
6475	3277	DCA	KXGAP
6476	5672	JMP I	XGAP
6477	0000	0	
6500	0000	0	/SET GO BIT NOT CHANGING FR /OTHERWISE, (GO)
6501	7200	CLA	
6502	1324	TAD	FUNCT
6503	2310	AND	K7600A
6504	1027	TAD	K100
6505	4500	LFGR	
6506	7200	CLA	
6507	5700	JMP I	GOE
6510	7600	K7600A	7600

6511 2000 /LOAD FR WITH FUNCTION +  
6512 7200 /EXTENDED GAP BIT CONSTANT

6513 1711  
6514 1277  
6515 3324  
6516 1324  
6517 4500  
6520 7200  
6521 3277  
6522 2311  
6523 5711  
6524 0000

CL A  
TAD I  
TAD  
DCA  
TAD  
LFGR  
CL A  
DCA  
ISZ  
JMP I  
FUNCT, 2

/SAVE FUNCTION,

FUNC  
KXGAP  
FUNC  
FUNC  
KXGAP  
FUNC  
FUNC

/LOAD THE COMMAND REGISTER WITH THE CONSTANT IN THE CALLING SEQUENCE,  
 /ENTERED BY:  
 /COMAND  
 /"CONSTANT"

EQUIVALENT JMS I CMDP  
 CONSTANT TO BE LOADED

```

6225 0000 0      /"COMAND"
6226 7200  CLA   /GET CONSTANT,
6227 1725  TAD I  CMD   /LOAD,
6230 4477  LCMR  /ENSURE AC CLEAR,
6231 7200  CLA   /UPDATE RETURN,
6232 2325  ISZ  /EXIT,
6233 5725  JMP I  CMD
  
```

/ISSUE ONE BREAK REQUEST  
 /ENTERED BY "BREAK",

```

6234 0000  DATBRK, 0      /"BREAK"
6235 4521  SBRM
6236 7000  NOP
6237 7000  NOP
6240 5734  JMP I  DATBRK
  
```

/WCOVER, 0  
 /GENERATE CONDITIONS NECESSARY  
 /FOR WCOV, SET GO PIT, WC TO  
 /7777 " LEAVE REMAINDER OF FR ALONE,

```

6241 0000  WCOVER, 0
6242 4550  GO
6243 7240  CLA CMA
6244 4473  LWCR
6245 4535  BREAK
6246 5741  JMP I  WCOVER
  
```

/BYTE SWAP ROUTINE.

```

6247 0000  SRBSW, 0
6250 7106  CLL  RTL
6251 7006  RTL
6252 7006  RTL
6253 3361  DCA
6254 7004  RAL
6255 1361  TAD
6256 2362  AND
6257 1361  TAD
6260 5747  JMP I
6261 0000  SRBSWT, 0
6262 0077  SRBSWN, 77
  
```

TST,	TEST01	TEST02	TEST03	TEST04	TEST05	TEST06	TEST07	TEST10	TEST11	TEST12	TEST13	TEST14	TAL,	TEST TALLIES FOR: TPA
6563	4203													/T02
6564	4600													/T03
6565	1000													/T04
6566	1235													/T05
6567	1400													/T06
6570	1600													/T07
6571	2000													/T10
6572	2200													/T11
6573	3000													/T12
6574	3200													/T13
6575	3400													/T14
6576	3600													
6577	0000													
6600	7766													
6601	0000													
6602	7766													
6603	7766													
6604	7766													
6605	0000													
6606	0000													
6607	0000													
6610	0000													
6611	0000													
6612	7766													
6613	4473													
6614	4475													
6615	4477													
6616	4501													
6617	4500													
6620	4474													
6621	4476													
6622	0000													
6623	0000													
6624	0000													
6625	4503													
6626	7777													
6627	4504													
6630	7777													
6631	4506													
6632	7777													
6633	4510													
6634	7777													
6635	4507													
6636	7600													
6637	4503													
6640	7777													
6641	4504													
6642	7777													
6643	4506													
6644	7777													
6645	4512													
6646	7777													
6647	4541													

/EXEC TEST POINTERS,

TEST TALLIES FOR: TPA

/T02  
/T03  
/T04  
/T05  
/T06  
/T07  
/T10  
/T11  
/T12  
/T13  
/T14

TILLS1, TILLS ARE UTILIZED BY  
LCMR THE TEST 01 LOCAL EXECUTIVE  
LDBR /FOR JOT DETERMINATION,  
LFGR  
CWCR  
CCAR

TILLS1, LCMR  
LDBR  
LFGR  
CWCR  
CCAR

TILLS2, LCMR  
LDBR  
LFGR  
CWCR  
CCAR

TILLS3, RCMR  
RCAR  
RCMR  
RDBR  
RFSR  
RWCR  
RCAR  
RCMR  
RDBR  
RFSR  
RWCR

TILLS1, READ  
RCAR  
RCMR  
RDBR  
RFSR  
RWCR

/FUN ON LIST USED BY

/TEST 03 ("T3EX1");

6650	4542	RDCOMP
6651	4543	WRITE
6652	4537	OFFLIN
6653	4540	REWIND
6654	4544	WEOF
6655	4545	SPCFWD
6656	4546	SPCREV
6657	4541	READ
6660	4542	RDCOMP
6661	4543	WRITE
6662	4544	WEOF
6663	5276	SYMPT, I1
6664	5304	I2
6665	5214	GD
6666	5221	BD
6667	5226	OD
6670	5233	WC
6671	5240	CA
6672	5245	CM
6673	5252	FS
6674	5257	MS
6675	5264	DB
6676	5271	AC

/SYMBOL ROUTINE POINTER TABLE;

/ERROR TABLE FOR ERRORS OCCURRING OUTSIDE OF FORMAL TESTS, (CODE 20X)

6677	4001	FE00A,	4001	/ILLEGAL INTERRUPT IN INTSEV, CURRENT CONTENTS OF
6700	7776		7776	/LOCATION 0 IN BAD,
6701	0002	ER00B,	0002	/BITS 4 AND 5 OF CH DID NOT CLEAR AFTER LCMR0,
6702	1020		1020	/DETECTED IN INSR0,
6703	0003	ER00C,	0003	
6704	7601		7601	

/ERROR TABLE FOR ERRORS OCCURRING IN FORMAL TESTS (CODE 01X-14X),

6705	0101	ER01A,	0101	
6706	1400		1400	
6707	0102	ER01B,	0102	
6710	1400		1400	
6711	0103	ER01C,	0103	
6712	1400		1400	
6713	0104	ER01D,	0104	
6714	5400		5400	
6715	0105	ER01E,	0105	
6716	6000		6000	
6717	0106	ER01F,	0106	
6720	7400		7400	
6721	0107	ER01G,	0107	
6722	6000		6000	
6723	0110	ER01H,	0110	
6724	7400		7400	
6725	0111	ER01I,	0111	
6726	7400		7400	
6727	0112	ER01J,	0112	
6730	3400		3400	
6731	0113	ER01K,	0113	
6732	6000		6000	
6733	0114	ER01L,	0114	
6734	7400		7400	
6735	0115	ER01M,	0115	
6736	5400		5400	
6737	0116	ER01N,	0116	
6740	7400		7400	
6741	0117	ER01O,	0117	
6742	7400		7400	
6743	0120	ER01P,	0120	
6744	7400		7400	
6745	2201	ER02A,	2201	
6746	1300		1300	
6747	2202	ER02B,	2202	
6750	1240		1240	
6751	2203	ER02C,	2203	
6752	1220		1220	
6753	2204	ER02D,	2204	
6754	1202		1202	
6755	2205	ER02E,	2205	
6756	1600		1600	
6757	0301	ER03A,	0301	

6760	1110		1110
6761	0302	ER03B,	0302
6762	1252		1252
6763	0303	ER03C,	0303
6764	1652		1652
6765	0304	ER03D,	0304
6766	1652		1652
6767	0305	ER03E,	0305
6770	1252		1252
6771	0306	ER03F,	0306
6772	1110		1110
6773	0307	ER03G,	0307
6774	1050		1050
6775	0310	ER03H,	0310
6776	1110		1110
6777	0311	ER03I,	0311
7000	1050		1050
7001	2401	ER04A,	2401
7002	1310		1310
7003	0402	ER04B,	0402
7004	1414		1414
7005	2403	ER04C,	2403
7006	1270		1270
7007	2404	ER04D,	2404
7010	1270		1270
7011	0405	ER04E,	0405
7012	1070		1070
7013	2501	ER05A,	2501
7014	1252		1252
7015	2502	ER05B,	2502
7016	1652		1652
7017	2601	ER06A,	2601
7020	1272		1272
7021	0701	ER07A,	0701
7022	1400		1400
7023	0702	ER07B,	0702
7024	1400		1400
7025	0703	ER07C,	0703
7026	1400		1400
7027	0704	ER07D,	0704
7030	1400		1400
7031	0705	ER07E,	0705
7032	1400		1400
7033	1001	ER10A,	1001
7034	1000		1000
7035	1002	ER10B,	1002
7036	0174		0174
7037	1003	ER10C,	1003
7040	0174		0174
7041	1004	ER10D,	1004
7042	4174		4174
7043	1005	ER10E,	1005
7044	0174		0174
7045	1006	ER10F,	1006
7046	0174		0174

7047	1007	ER106,	1007
7050	0174		0174
7051	1010	ER10H,	1010
7052	1574		1574
7053	1011	ER10I,	1011
7054	1574		1574
7055	1012	ER10J,	1012
7056	1574		1574
7057	1013	ER10K,	1013
7060	1574		1574
7061	1017	ER10O,	1017
7062	1574		1574
7063	1020	ER10P,	1020
7064	1574		1574
7065	1021	ER10Q,	1021
7066	1574		1574
7067	1022	ER10R,	1022
7070	1574		1574
7071	1023	ER10S,	1023
7072	1576		1576
7073	1024	ER10T,	1024
7074	1576		1576
7075	1025	ER10U,	1025
7076	1574		1574
7077	1026	ER10V,	1026
7100	1574		1574
7101	1027	ER10W,	1027
7102	1574		1574
7103	1030	ER10X,	1030
7104	1574		1574
7105	1032	ER10Z,	1032
7106	1576		1576
7107	1061	ER101,	1061
7110	1574		1574
7111	1062	ER102,	1062
7112	1574		1574
7113	1101	ER11A,	1101
7114	0004		0004
7115	1102	ER11B,	1102
7116	4004		4004
7117	1103	ER11C,	1103
7120	4114		4114
7121	1104	ER11D,	1104
7122	4114		4114
7123	1105	ER11E,	1105
7124	4014		4014
7125	1106	ER11F,	1106
7126	4014		4014
7127	1201	ER12A,	1201
7130	0024		0024
7131	1202	ER12B,	1202
7132	0134		0134
7133	1203	ER12C,	1203
7134	0134		0134
7135	1204	ER12D,	1204



7136 0034  
 7137 1205  
 7140 0034  
 7141 1301  
 7142 0014  
 7143 1302  
 7144 4014  
 7145 1303  
 7146 1040  
 7147 1304  
 7150 1050  
 7151 1305  
 7152 5110  
 7153 3401  
 7154 1656  
 7155 3402  
 7156 1656  
 7157 1403  
 7160 1056  
 7161 1404  
 7162 1404

0034  
 ER12E,  
 ER13A,  
 ER13B,  
 ER13C,  
 ER13D,  
 ER13E,  
 ER14A,  
 ER14B,  
 ER14C,  
 ER14D,

/MESSAGES!

7163 4324  
 7164 1570  
 7165 0540  
 7166 0317  
 7167 1624  
 7170 2217  
 7171 1440  
 7172 2405  
 7173 2324  
 7174 4020  
 7175 0122  
 7176 2440  
 7177 6143  
 7200 4315  
 7201 0111  
 7202 1604  
 7203 4503  
 7204 5560  
 7205 7055  
 7206 0410  
 7207 2415  
 7210 0155  
 7211 0143  
 7212 0000

MSG1, TEXT  
 "#TMBE CONTROL TEST PART 1#MAINDEC=08=DHTMA=A#"

7213 4324  
 7214 0923  
 7215 2440  
 7216 0000  
 7217 4300

/MSG2,  
 /MSG1,  
 /CR,T  
 /E/S  
 /T/SP  
 /% TEST NUMBER FILL IN.  
 /% END MSG

"#TEST NN#"

TEXT  
 4324  
 0523  
 2440  
 0000  
 4300

MSG3, TEXT "#SELECTED TESTS DONE#"

7220 4323  
7221 0514  
7222 2503  
7223 2405  
7224 0440  
7225 2405  
7226 2324  
7227 2340  
7230 0417  
7231 1605  
7232 4300

/MSG4, TEXT "#PASS NNN#"  
MSG4, 4320 /CR,P  
0123 /A,S  
2340 /S,SP  
M4P1, 0 /PASS NUMBER  
M4P2, 0 /FILL IN,  
0000  
MSG6, TEXT "?#"

MSG7, TEXT "##FATAL ERROR#"

7243 4343  
7244 3601  
7245 2401  
7246 1440  
7247 0522  
7250 2217  
7251 2252  
7252 0000

MSG8, TEXT "#["

7253 4333  
7254 0000

MSG9, TEXT "#SET SR0 FOR QUICK VERIFY#"

7255 4323  
7256 0524  
7257 4023  
7260 2260  
7261 4006  
7262 1722  
7263 4021  
7264 2511  
7265 2313  
7266 4026  
7267 0522  
7270 1106  
7271 3143  
7272 0000

MSG10, TEXT "#EXTENDED MEMORY?(0-/) "

7273 4305  
7274 3024  
7275 2516  
7276 0405  
7277 0440

7300 1505  
7301 1517  
7302 2231  
7303 7750  
7304 6055  
7305 6751  
7306 4000

/MSG11, TEXT " NNNN"

7307 4040 /SP,SP  
7310 0000 /CONTENTS OF MEMORY  
7311 0000 /FILL IN.  
7312 0000 /END MSG.

/THE FULL ERROR MESSAGE CALLED BY STA12=7777 WOULD BE:  
/ERMSG, TEXT "#\*ERNX PC:NNNN I1: NNNN I2: NNNN GDI NNNN BD: NNNN ODI NNNN  
WC: NNNN CA: NNNN CM: NNNN FS: NNNN MS: NNNN DS: NNNN AC: NNNN#"

7313 0000 /ERMSG, 0  
7314 0000 /ERRMP1, 0  
7315 0000 /ERRMP2, 0  
7316 0000 /ERRMP3, 0  
7317 4020 /SP,P  
7320 0372 /C,I  
7321 0000 /CONTENTS OF PG.  
7322 0000 /DITTO

7323 0000 /ALL OTHER INFORMATION INSERTED HERE,  
7324 0000 ZBLOCK 65  
7325 0000 ZBLOCK 65  
7326 0000 ZBLOCK 65  
7327 0000 ZBLOCK 65  
7330 0000 ZBLOCK 65  
7331 0000 ZBLOCK 65  
7332 0000 ZBLOCK 65  
7333 0000 ZBLOCK 65  
7334 0000 ZBLOCK 65  
7335 0000 ZBLOCK 65  
7336 0000 ZBLOCK 65  
7337 0000 ZBLOCK 65  
7340 0000 ZBLOCK 65  
7341 0000 ZBLOCK 65  
7342 0000 ZBLOCK 65  
7343 0000 ZBLOCK 65  
7344 0000 ZBLOCK 65  
7345 0000 ZBLOCK 65  
7346 0000 ZBLOCK 65  
7347 0000 ZBLOCK 65  
7350 0000 ZBLOCK 65  
7351 0000 ZBLOCK 65  
7352 0000 ZBLOCK 65  
7353 0000 ZBLOCK 65  
7354 0000 ZBLOCK 65  
7355 0000 ZBLOCK 65  
7356 0000 ZBLOCK 65  
7357 0000 ZBLOCK 65

7360	0000	ZBLOCK	65
7361	0000	ZBLOCK	65
7362	0000	ZBLOCK	65
7363	0000	ZBLOCK	65
7364	0000	ZBLOCK	65
7365	0000	ZBLOCK	65
7366	0000	ZBLOCK	65
7367	0000	ZBLOCK	65
7370	0000	ZBLOCK	65
7371	0000	ZBLOCK	65
7372	0000	ZBLOCK	65
7373	0000	ZBLOCK	65
7374	0000	ZBLOCK	65
7375	0000	ZBLOCK	65
7376	0000	ZBLOCK	65
7377	0000	ZBLOCK	65
7400	0000	ZBLOCK	65
7401	0000	ZBLOCK	65
7402	0000	ZBLOCK	65
7403	0000	ZBLOCK	65
7404	0000	ZBLOCK	65
7405	0000	ZBLOCK	65
7406	0000	ZBLOCK	65
7407	0000	ZBLOCK	65
7410	0000	ZBLOCK	65
7411	0000	ZBLOCK	65
7412	0000	ZBLOCK	65
7413	0000	ZBLOCK	65
7414	0000	ZBLOCK	65
7415	0000	ZBLOCK	65
7416	0000	ZBLOCK	65
7417	0000	ZBLOCK	65
7420	0000	ZBLOCK	65
7421	0000	ZBLOCK	65
7422	0000	ZBLOCK	65
7423	0000	ZBLOCK	65

/(DONE BY ASSEMBLER,)

OCIAL

7500	4472	*7500	
7501	4473	T10LS1,	
7502	4475	LWCR	16700
7503	4477	LCAR	
7504	4500	LCMR	
7505	4474	LFGR	
7506	4476	CWCR	
7507	4503	CCAR	
7510	4504	RWCR	
7511	4506	RCAR	
7512	4507	RCMR	
7513	4510	REFR	
		RDBR	

7514	4505	RMSR
7515	4502	16710
7516	4511	16720
7517	4515	SKIR
7520	4514	SKTD
7521	4513	SKCB
7522	4520	SDLE
7523	4516	CLF
7524	4512	SKEF

## /INSTRUCTION DEFINITIONS:

## /SPECIAL EXT MEM BASIC INSTRUCTIONS:

6201 /CHANGE DATA FIELD  
 6224 /READ INSTRUCTION FIELD.  
 6244 /RESTORE MEMORY FIELD.

## /TM8E IOT DEFINITIONS:

## /LOAD IOT'S:

4472 16700=JMS I S6700P /IOT 6700, /AC TO WC, 0 TO AC;  
 4473 LWCR=JMS I SLWCRP /0 TO WC;  
 4474 CWCR=JMS I SCWCRP /AC TO CA, 0 TO AC;  
 4475 LCAR=JMS I SLCARP /0 TO CA;  
 4476 CCAR=JMS I SCCARP /AC TO CM, 0 TO AC;  
 4477 LCMR=JMS I SLCMRP /AC0-3, 5 TO FR, 0 TO AC;  
 4500 LFGR=JMS I SLFGRP /AC TO DB, 0 TO AC  
 4501 LDBR=JMS I SLDBRP

## /READ IOT'S:

4502 16710=JMS I S6710P /IOT 6710, /0 TO AC, WC TO AC;  
 4503 RWCR=JMS I SRWCRP /0 TO AC, CA TO AC;  
 4504 RCAR=JMS I SRCARP /0 TO AC, MS TO AC;  
 4505 RMSR=JMS I SRMSRP /0 TO AC, CM TO AC;  
 4506 RCMR=JMS I SRCMRP /0 TO AC, FR TO AC0-4, GO BIT TO AC5, S1 TO AC6-11.  
 4507 RFSR=JMS I SRFSRP /0 TO AC, DB TO AC;  
 4510 RDBR=JMS I SRDBRP

## /CONTROL IOT'S:

4511 16720=JMS I S6720P /IOT 6720, /SKIP IF ERROR FLAG SET,  
 4512 SKEF=JMS I SSKEFP /SKIP IF CONTROL NOT BUSY,  
 4513 SKCB=JMS I SSKCBP /SKIP IF TRANSPORT DONE,  
 4514 SKTD=JMS I SSKTDP /SKIP IF TUR,  
 4515 SKTR=JMS I SSKTRP /0 TO REGISTERS AND FLAGS IF TUR; IF NOT 0 TO MTF, EF, SR  
 4516 CLF=JMS I SCLFP /POWER CLEAR TRANSPORT,  
 4517 CLT=JMS I SCLTP /SET DATA LATE ERRCH;  
 4520 SDLE=JMS I SSDLEP /SET ONE BREAK REQUEST;  
 4521 SBRM=JMS I SSBRMP

## /INSTRUCTION PSEUDO MNEMONICS DEFINITIONS:

4522 BSW=JMS I SRBSWP /SWAP BITS 0-5 WITH BITS 6-11 IN THE AC.  
 4523 CLEAR1=JMS I CLR1P /GENERATE "INITIALIZE" AND LCMR0, TURN INTERRUPT ON,  
 4524 CLEAR2=JMS I CLR2P /GENERATE "INITIALIZE", LEAVE CM ALONE, TURN INTERRUPT ON,  
 4525 CLEAR3=JMS I CLR3P /GENERATE "INITIALIZE" SAFELY WITH RESPECT TO TIO, DO NOT TURN  
 /INTERRUPT ON,  
 4526 LOOP5=JMS I LAS5P /CHECK SR5, IF CLEAR, SKIP NEXT INSTRUCTION,  
 4527 LOOP6=JMS I LAS6P /CHECK SR6, IF CLEAR, SKIP NEXT INSTRUCTION,  
 4530 LOOP7=JMS I LAS7P /CHECK SR7, IF CLEAR, SKIP NEXT INSTRUCTION,  
 4531 LOOP8=JMS I LAS8P /CHECK SR8, IF CLEAR, SKIP NEXT INSTRUCTION.

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CONTROL TEST PART 1 MAINDEC=08-DHTMA-A=L PAL10 .41 16=DEC=72 13:30 PAGE 34-1
4532 LOOPY=JMS I LAS9P /CHECK SRY, IF CLEAR, SKIP NEXT INSTRUCTION,
4533 COMPAR=JMS I COMPP /COMPARE "GOOD" AND "BAD". GO TO "ERRORS" ON FAILURE, CHECK LOOP 5,
4534 ERROR=JMS I ERRORP /GO DIRECTLY TO ERROR ROUTINE, MAY BEUSED FOR FATAL ERRORS ONLY,
4535 BREAK=JMS I DATBRP /ISSUE ONE BREAK REQUEST (SKRM),
4536 COMAND=JMS I CMDP /LOAD THE CM WITH THE CONSTANT IN THE NEXT LOCATION,
4537 OFFLIN=JMS I OFFLNP /0000+GAP CONSTANT TO FR,
4540 REWIND=JMS I REWNP /1000+GAP CONSTANT FR,
4541 READ=JMS I READP /2000+GAP CONSTANT TO FR,
4542 RDCOMP=JMS I RDCMP /3000+GAP CONSTANT TO FR,
4543 WRITE=JMS I WRITP /4000+GAP CONSTANT TO FR,
4544 WEOF=JMS I WEOF /5000+GAP CONSTANT TO FR,
4545 SPCFWD=JMS I SPCFORP /6000+GAP CONSTANT TO FR,
4546 SPCREV=JMS I SPCREVP /7000+GAP CONSTANT TO FR,
4547 EXTGAP=JMS I XGAPP /SET GAP CONSTANT TO 2400, CONSTANT CLEARED AFTER NEXT FR LOAD,
4550 GO=JMS I GOEP /SET THE "GO" BIT AND LEAVE REST OF FR ALONE,
4553 WCOV=JMS I WCOVEP /GENERATE WORD COUNT OVERFLOW USING FUNCTION ALREADY IN FR,
4551 LXXR=JMS I XIOT1P /IOT SUBROUTINES FOR TEST 01,
4551 CXXR=JMS I XIOT1P
4552 RXXR=JMS I XIOT2P
$$$

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AC	5271	EDIT1	6312	ER07D	7027	ERMP4	7021
ACLOC	5170	EDITP	6063	ER07E	7031	ERMP4P	5152
ACTFLG	2175	EDIEM	6326	ER101	7107	ERMP5	7322
AMG8E	6220	EDIEM1	6327	ER102	7111	ERMP5P	5153
AMG8E1	5237	EDIEM2	6330	ER10A	7033	ERMP6	5363
AMG8E2	6214	EDIEM3	6331	ER10B	7035	ERMP7	5156
AMG8E3	6240	EDIEMP	6064	ER10C	7037	ERMSG	7313
AMG8EP	0061	EOFP	0144	ER10D	7041	ERMSGP	0067
BAD	1155	EOFW	6456	ER10E	7043	ERMSYM	5364
BD	5221	ER00B	6701	ER10F	7045	ERRIP	5636
BELL	6262	ER00C	6703	ER10G	7047	ERROR	4934
BELLP	0065	ER01A	6705	ER10H	7051	ERRORP	0134
BREAK	4335	ER01B	6707	ER10I	7053	ERRORS	5002
BSW	4522	ER01C	6711	ER10J	7055	ERRPTR	5145
CA	5240	ER01D	6713	ER10K	7057	ES	4527
CCAR	4476	ER01E	6715	ER10L	7061	EXEC	4200
CCF	6201	ER01F	6717	ER10M	7063	EXECFP	0071
CLEAR1	4523	ER01G	6721	ER10N	7065	EXECFX	5907
CLEAR2	4524	ER01H	6723	ER10O	7067	EXECL1	4256
CLEAR3	4525	ER01I	6725	ER10R	7071	EXECL2	4245
CLF	4516	ER01J	6727	ER10S	7073	EXECP	0055
CLR1	6400	ER01K	6731	ER10T	7075	EXITFL	0174
CLRIP	0123	ER01L	6733	ER10U	7077	EXOMT	4303
CLR2	6406	ER01M	6735	ER10V	7077	EXTAL	4354
CLR2P	0124	ER01N	6737	ER10W	7101	EXTEMP	4362
CLR3	6414	ER01O	6741	ER10X	7103	EXTGAP	4547
CLR3P	0125	ER01P	6743	ER10Z	7105	FATERR	5031
CLY	4517	ER02A	6745	ER11A	7113	FE00A	6677
CM	5245	ER02B	6747	ER11B	7115	FS	5252
CMD	5225	ER02C	6751	ER11C	7117	FUNC	6511
CMDB	0136	ER02D	6753	ER11D	7121	FUNCP	0056
COMMAND	4536	ER02E	6755	ER11E	7123	FUNCT	6524
COMP	5600	ER03A	6757	ER11F	7125	GO	5214
COMPAR	4533	ER03B	6761	ER12A	7127	GO	4950
COMPF	5631	ER03C	6763	ER12B	7131	GOE	6500
COMPP	4133	ER03D	6765	ER12C	7133	GOEP	2150
CRLEP	6270	ER03E	6767	ER12D	7135	GOOD	0154
CRLEPP	0066	ER03F	6771	ER12E	7137	GOOD	5211
CS	4420	ER03G	6773	ER13A	7141	HALTC	5213
CAGR	4474	ER03H	6775	ER13B	7143	HALTCP	5276
CAXR	4551	ER03I	6777	ER13C	7145	I1	5312
CATBRK	6534	ER04A	7001	ER13D	7147	I112	3145
CATBRP	2135	ER04B	7003	ER13E	7151	I1P	5304
CB	5264	ER04C	7005	ER14A	7153	I2	4472
CBR	4665	ER04D	7007	ER14B	7155	I6700	4502
CLF	4636	ER04E	7011	ER14C	7157	I6710	4511
CLF1	4640	ER05A	7013	ER14D	7161	I6720	5463
CLF2	4621	ER05B	7015	ERMP1	7314	INSRS	5225
CS	4600	ER06A	7017	ERMP1P	5146	INSRSF	2060
CS1	4667	ER07A	7021	ERMP2	7315	INSRSP	5427
CS2	4670	ER07B	7023	ERMP2P	5147	INTAGN	0164
CS3	6301	ER07C	7025	ERMP3	7316	INTFL	2172
		ER07C	7025	ERMP3P	5150	INTFLG	

INTMTF	0166	KRMF	4347	M4P2P	4563	RDCMP	6446
INTNGA	5432	KXGAP	6477	MS	0041	RDCMPP	2142
INTOK	5444	LAS5	6105	MARK1	1622	RDCOMP	4542
INTOKP	4544	LAS5P	0126	MARK1P	1762	READ	4541
INTSEV	4345	LAS6	6113	MARK2	1927	READD	6442
INTSEV	5400	LAS6P	0127	MARK2P	1763	READDR	2141
IOT1	2566	LAS7	6121	MARK3	1624	RESTP	6021
IOT1P	5303	LAS7P	0130	MEMFLD	0167	RESTPF	4541
IOT2	0971	LAS8	6127	MONIT	4900	REWIND	4540
IOT2P	5311	LAS8P	0131	MONITP	0062	REWIND	6436
K1	0020	LAS9	6135	MONP	4676	REWNP	2140
K10	0023	LAS9P	0132	MS	5257	REX	4240
K100	0027	LCAR	4475	MSG1	7163	REXP	4536
K1000	5366	LDBR	4477	MSG10	7273	RFSR	4507
K17	5165	LDBR	4501	MSG11	7307	RIF	6224
K2	0021	LFGR	4500	MSG2	7213	RMF	6244
K20	0024	LISN	4677	MSG3	7220	RMSR	4505
K200	0030	LISN1	4712	MSG4	7233	RWCR	4503
K2000	0033	LISN2	4724	MSG6	7241	RXXR	4552
K207	6267	LISN3	4740	MSG7	7243	S670W	4000
K212	6300	LISNP	4543	MSG8	7253	S670PP	0072
K215	0031	LISNT1	4746	MSG9	7255	S6710	4040
K2252	5641	LISNUM	4727	NOEXLP	4336	S6710P	0102
K3	0005	LOOP5	4526	NOTSPH	4277	S6720P	0111
K377	5370	LOOP6	4527	NOTSRN	4315	SAVEAC	0162
K3774	2051	LOOP7	4530	OD	5226	SAVEL	0163
K4	6143	LOOP8	4531	OFFLIN	4537	SAVEP	6000
K40	0025	LOOP9	4532	OFFLINE	6432	SAVEP1	6043
K400	0032	LWCR	4473	OFFLNP	0137	SAVEP2	6044
K4000	0034	LXXR	4551	OLD	0156	SAVEP3	6045
K4040	0035	M1	5461	P0	5462	SAVEP4	6046
K4300	5166	M11P1	7310	P1	4351	SAVEPP	4540
K4324	5642	M11P1P	4671	P2	4352	SBRM	4521
K4343	5167	M11P2	7311	P3	4353	SCCAR	4020
K4352	5170	M11P2P	4672	PASCNT	4346	SCCARP	0076
K522	5171	M14	0042	PRCNT1	5154	SCDF	4144
K605	5172	M142	5460	PRCNT2	5155	SCDF1	4145
K6060	6332	M16	6042	PRGFLO	0165	SCDFIP	4350
K7	0022	M2	0037	PRMPT	5160	SCDFP	0057
K70	0026	M20	0043	PRNT	5053	SCLF	4124
K700	6333	M233	5457	PRNTCK	5135	SCLFP	0116
K72	5367	M240	4747	PRNTK	5151	SCLTP	0130
K7400	0036	M260	0044	PRNTLP	5117	SCLTP	0117
K7600	0574	M270	0045	PRNTP	5637	SCWCR	4010
K7600A	6510	M2P1	7216	QHALT	5200	SCWCRP	0074
K77	6241	M2P1P	4360	QHALTP	5164	SDLE	4520
K7700	5173	M4	0040	GPRNT	5040	SETEF	3157
KCDF	0046	M40	6242	QUES	4673	SETEFP	3345
KJMP61	1757	M43	6243	QUESP	4542	S10T1	0565
KJMP62	1762	M4P1	7236	RCAR	4504	S10T1P	0151
KJMP13	4344	M4P1P	4364	RCMR	4576	S10T2	7570
KNOP	1756	M4P2	7237	ROBR	4512		

SI012P	0152	START	0200	T04T1	1376	T11D	3045
SKCR	4513	SYMADR	5151	T04T2	1377	T11D1	3055
SKEF	4512	SYMBOL	9157	T05A	1402	T11E	3066
SKIPR	4147	SYMPT	6663	T05ASL	1406	T11E1	3074
SKTD	4514	SYMPTP	9162	T05B	1424	T11EX1	3122
SKTR	4515	SYMSEV	9330	T05BSL	1427	T11EX2	3141
SLCAR	4014	SYMTEM	5365	T05LDR	1451	T11EX3	3134
SLCARP	0075	T01A	0211	T05LDS	1450	T11F	3122
SLCMR	4024	T01B	0216	T06A	1602	T11F1	3110
SLCMRP	0077	T01C	0223	T06ADF	1615	T11LS1	3146
SLDBRP	4034	T01D	0235	T06AMP	2002	T12A	3207
SLDBRP	0101	T01E	0243	T07A	2011	T12A1	3215
SLFGR	4030	T01F	0255	T07B	2011	T12B	3225
SLFGRP	0100	T01G	0270	T07C	2021	T12C	3240
SLWCR	4004	T01H	0304	T07D	2032	T12D	3256
SLWCRP	0073	T01I	0313	T07E	2037	T12E	3267
SPCFND	4545	T01J	0323	T101	2637	T12EX1	3305
SPCREV	4546	T01K	0336	T102	2660	T12EX2	3322
SPFORD	6462	T01L	0347	T10A	2204	T12EX3	3317
SPFORP	0145	T01M	0355	T10B	2216	T12EX4	3326
SPREV	6466	T01N	0362	T10C	2231	T12INT	3335
SPREVP	0146	T01O	0403	T10D	2244	T12LS1	3337
SRBSW	6547	T01OL	0400	T10E	2264	T13A	3402
SRBSWN	6562	T01P	0427	T10F	2273	T13B	3415
SRBSWP	0122	T02A	0602	T10G	2302	T13B1	3424
SRBSWT	6561	T02B	0617	T10H	2313	T13C	3434
SRCAR	4050	T02C	0634	T10I	2324	T13D	3447
SRCARP	0104	T02D	0654	T10J	2336	T13E	3461
SRCLR	4060	T02E	0670	T10K	2350	T13E1	3471
SRCMRP	0106	T02LDM	0745	T10KP	2552	T13EX1	3503
SRDBR	4070	T02LDR	0731	T10LPB	2546	T13EX2	3504
SRDBRP	0110	T02LDS	0707	T10LPH	2705	T14A	3602
SRFSR	4064	T02LDT	0716	T10LS1	7500	T14B	3612
SRFSRP	0107	T02LDU	0714	T100	2400	T14BIT	3777
SRHOLD	6047	T02LP9	0704	T10P	2410	T14C	3623
SRMSR	4054	T03A	1004	T10Q	2421	T14C1	3637
SRMSRP	1105	T03B	1015	T10R	2430	T14D	3650
SRPTS	6066	T03C	1027	T10S	2443	T14EX1	3671
SRWCR	4044	T03D	1042	T10T	2454	T14EX2	3677
SRWCRP	0103	T03E	1062	T10U	2500	T14EX3	3701
SSBRM	4140	T03F	1101	T10V	2511	T14EX4	3710
SSBRMP	1121	T03G	1120	T10W	2531	T14EX5	3725
SSDLE	4134	T03H	1141	T10X	2602	T14EX6	3727
SSDLEP	0120	T03I	1156	T10X1P	2361	T14EX7	3747
SSKCB	4105	T04A	1237	T10X2P	2362	T14EX8	3760
SSKCBP	2113	T04AL	1243	T10X3P	2363	T14TM1	3775
SSKEF	4100	T04ASL	1252	T10Z	2613	T14TM2	3776
SSKEFP	4112	T04B	1264	T11A	3003	T1EX1	0450
SSKTD	4112	T04CD	1276	T11B	3011	T1EX1A	0461
SSKTD P	0114	T04CL	1302	T11B1	3016	T1EX1P	2375
SSKTR	4117	T04CSL	1324	T11C	3027	T1EX2	2473
SSKTRP	2115	T04E	1357	T11C1	3037	T1EX2P	0376

T1EX3	0501	TEST04	1235
T1EX3P	0377	TEST05	1400
T1EX4	0205	TEST06	1600
T1EX5	0340	TEST07	2000
T1EX6	0524	TEST10	2200
T1EX7	0516	TEST11	3000
T1EX8	0530	TEST12	3200
T1EX11	0561	TEST13	3400
T1EX12	0562	TEST14	3600
T1EX13	0563	TEST1P	4366
T1EX14	0564	TR	4501
T1LP7	0371	TRACE	0171
T1LP7P	0573	TRPP	5640
T1LS1	6613	TS	4435
T1LS1P	0556	TSI	6563
T1LS2	6620	TSI10P	2704
T1LS2P	0557	TS1P	0447
T1LS3	6625	TSIAEX	4357
T1LS3P	0560	TSIAT	0176
T3EX1	1213	TSIATM	4537
T3EX1P	1176	TSIEM1	4535
T3EX2	1231	TSINUM	0177
T3EX2P	1175	TSIP	4365
T3EX3	1225	TSIPP	4355
T3EX32	1177	TSITAL	4361
T3LOAD	1200	TT0FLG	0173
T3LODP	2004	TXXTM1	0157
T3LS1	6647	TXXTM2	0160
T4LP6	1262	TXXTM3	0161
T4LP7	1373	TYPE	6244
T5LP6	1422	TYPEP	0070
T5LP7	1445	TYPIEM	6261
T6CNT1	1764	WC	5233
T6CNT2	1765	WCOV	4553
T6CNT3	1766	WCOVER	0153
T6EX1	1642	WCOVER	6541
T6EX2	1664	WEOF	4544
T6EX3	1727	WHAT	5163
T6EX4	1747	WRIT	6452
T6EX5	1737	WRITE	4543
T6EX6	1753	WRITP	0143
T6FLAG	1767	XBUFF	0050
T6I1	1713	XBUFFP	0047
T6SAVE	1761	XGAP	6472
TA	4517	XGAPP	0147
TAL	6577		
TALP	4367		
TALPP	4356		
TCR	4513		
TEST01	0203		
TEST02	0600		
TEST03	1000		

ERRORS DETECTED: 0

LINKS GENERATED: 0

RUN-TIME: 35 SECONDS

4K CORE USED